

文 書 番 号	シート No.	版 数
	15	1

3.7 試験/作業項目 (試験フロー)

1. SETUP

- 1.1. セットアップ
- 1.2. MIS 及び I/F 変換装置立ち上げ(バックアップ)

2. 共通機器立ち上げ

- 2.1. 衛星立ち上げ
 - 2.1.1. Power ON
 - 2.1.2. BAT INT/BUS SW ON
 - 2.1.3. K1/K2/CD/CV 設定
 - 2.1.4. インデックスパルス出力
- 2.2. RF LINK ON
- 2.3. TLM_MOD 1.5rad CHECK
- 2.4. ACS 立ち上げ
- 2.5. DR 設定
- 2.6. TCFS 設定
- 2.7. 消費電力取得

3. PI 機器立ち上げ

- 3.1. MDP 立ち上げ／初期化
 - 3.1.1. MDP 立ち上げ
 - 3.1.2. MDP チェックアウト (MDP ハード健全性確認)
- 3.2. PME 立ち上げ／初期化
- 3.3. MGF 立ち上げ／初期化
- 3.4. ENA 立ち上げ／初期化
- 3.5. MEA1 立ち上げ／初期化
- 3.6. MEA2 立ち上げ／初期化
- 3.7. MSA 立ち上げ／初期化
- 3.8. MIA 立ち上げ／初期化
- 3.9. HEP-E 立ち上げ／初期化
- 3.10. HEP-I 立ち上げ／初期化
- 3.11. PWI 立ち上げ／初期化 [EWO, SORBET, AM2P, MEFISTO]
- 3.12. MAST 立ち上げ／初期化
- 3.13. MOTOR-PSU チェック
- 3.14. MDM 立ち上げ／初期化
- 3.15. MSASI 立ち上げ／初期化
- 3.16. SPM チェック
- 3.17. 消費電力取得

4. L/M-Mode Check

- 4.1. MDP L/M-Mode 設定
- 4.2. ENA L/M-Mode check
- 4.3. MEA1 L/M-Mode check
- 4.4. MEA2 L/M-Mode check
- 4.5. MSA L/M-Mode check
- 4.6. MIA L/M-Mode check
- 4.7. HEP-E L/M-Mode check
- 4.8. HEP-I L/M-Mode check

文 書 番 号	シート No.	版 数
	16	1

- 4. 9. PWI (EWO/AM2P/SORBET/MEFISTO/WPT/SC) L/M-Mode check
- 4. 10. MGF CHECKOUT (含磁場キャンセル)
 - 4. 10. 1. DR REC START
 - 4. 10. 2. MGF-0 CHECKOUT START / STOP, DR REPRO
 - 4. 10. 3. DR Repro
 - 4. 10. 4. MGF-0 L/M-Mode check (Normal Function-D sequence)
 - 4. 10. 5. MGF-I CHECKOUT START / STOP, DR REPRO
 - 4. 10. 6. DR Repro
 - 4. 10. 7. DR Rec OFF
- 4. 11. MAST L/M-Mode check
- 4. 12. MDM L/M-Mode check
- 4. 13. MSASI L/M-Mode check
- 4. 14. 消費電力取得
- 5. H-Mode Check
 - 5. 1. PI 機器設定
 - 5. 2. ARO compression
- 6. RF → RD 切り替え (XTWTA OFF)
- 7. DR Repro
- 8. 機器立ち下げ
 - 8. 1. SI 系立ち下げ
 - 8. 2. MDP 立ち下げ
 - 8. 3. DRU/ACFS センサ/HTR CTRL OFF
- 9. 立ち下げ
 - 9. 1. MC_DIS
 - 9. 2. 衛星 OFF
 - 9. 3. 試験コンフィギュレーションの解除

[illegible]

文 書 番 号	シート No.	版 数
	18	1

```

0050 # ENTER キーを押下
0051 # (4) State: REMOTE と表示されることを確認する
0052 #
0053 # 【MIS(MPO Interface Simulator)】
0054 # 下記手順により立ち上げ実施
0055 # (1) PC の電源 ON
0056 # (2) PC にログインする
0057 # ID = CMDVS_user
0058 # Password = CMDVS
0059 # (3) MIS 本体の電源を、以下の順に ON する
0060 # (a) External Bus Power Supply
0061 # (b) PFE Platform
0062 # (c) MIL-1553/Discrete Front-End
0063 # (4) 1 分待つ
0064 # (5) PC で、Start CMDVS アイコンをダブルクリック
0065 # (CMDVS ソフトウェアが起動する)
0066 # (6) Main Menu: Mode -> System On-Line Mode を選択
0067 # (PFE の、System Enabled の LED が点灯)
0068 # (7) Main Menu: Control -> System Control を選択
0069 # (8) System Control 画面の FE status Storage selection において
0070 # ☐ PFE
0071 # ☐ MFE
0072 # ☐ DFE
0073 # をクリックする。チェックマークが表示される
0074 # (9) Enabled Selected Categories ボタンをクリック
0075 # (ログ取得開始)
0076 # (10) System Control 画面で“CCI”をクリック
0077 # (11) Interface Management:
0078 # Open Interface
0079 # TM Distribution: Enable
0080 # TC Handling: Enable
0081 # をクリック
0082 # (12) I/F 変換装置の Operation Log 画面で以下のメッセージを確認する
0083 # Socket opened CCS/MIS-EGSE 3000
0084 # Socket opened CCS/MIS-EGSE 3001
0085 # (13) System Control 画面で“MFE”をクリック
0086 # (14) Bus profile ボタンをクリック
0087 # (15) Polling Sequence Table Selection and Verification において
0088 # ファイル BCMIS_MMO PST defs\BCMIS_MMO.PST を選択し、Verify ボタンを
0089 # クリック → load ボタンをクリック
0090 # (16) Polling Sequence Table Control において、start ボタンをクリック
0091 # (1553B バスへの Sync メッセージ出力開始)
0092 # (17) System Control 画面で“PFE”をクリック
0093 # (18) Power Control Bus A の“OFF”をクリック
0094 # “OFF”→“ON”になる
0095 # (19) External Bus Power Supply の、DC VOLTS 表示 = 28.00 を確認する
0096 # (20) PFE Platform の、Under Voltage の LED=消灯を確認する
0097 #
0098 # *****
0099 # 2. 共通機器立ち上げ
0100 # *****
0101 #

```

文 書 番 号	シート No.	版 数
	19	1

```

0102  # =====
0103  . # 2.1. 衛星立ち上げ
0104  # =====
0105  # -----
0106  . # 2.1.1. Power ON
0107  # -----
0108  #
0109  . # 【局管制設定】
0110  # 初期設定          : INIT_MMO_RF -> INIT_MMO_RD.
0111  # 設備状況          : 正常動作(緑色表示)
0112  #
0113  . # 【局管制設定】
0114  # 時刻発生装置      : 試験当日の日付になっていることを確認
0115  #
0116  . # 【衛星管制設定】
0117  # 接続 CMD          : cccmd2
0118  # 接続 TLM          : cctlm2
0119  # プリチェック      : OFF
0120  # センドベリファイ  : 強制 TYPE-B
0121  # サクセスベリファイ : OFF
0122  # RF/LINE 切り替え   : LINE
0123  #
0124  . # 【地上装置設定】
0125  # EXT-PS CV: 90.0V
0126  #          CC: 6.0A
0127  # OVP    54.0V
0128  # UVP    32.0V
0129  #
0130  . # 【共通 QL 設定】
0131  . # MMO_RL_X 選択
0132  . #
0133  . # 【操作】
0134  . # EXT-PS ON
0135  . # RD-LINK ON
0136  . # BAT_MEASURE START (BAT 温度計測開始)
0137  #
0138  . # テレメトリ 1553B → TCIM 変更
0139  . DH. TLM_REATCIMSEL
0140  WAIT_SEC 2
0141  # Operation mode change (HK mode: Real 32kbps)
0142  #          RATE CHK  CATE DHFS
0143  DH. OPE_MODE 0x0D 0x4F 0x23 0x00 0x000000000000000000000000
0144  #
0145  . # < CHECK >
0146  CHECK DH. CATEGORY_TBL_NO          0x23
0147  CHECK DH. DWNLNK_RATE              =BPS32768
0148  CHECK DH. ATMC_FMT_ERR             =OK
0149  CHECK DH. ATMC_SUM_STAT            =OK
0150  CHECK DH. MC_FMT_ERR               =OK
0151  CHECK DH. MC_SUM_STAT              =OK
0152  CHECK DH. RQMC_FMT_ERR             =OK
0153  CHECK DH. RQMC_SUM_STAT            =OK

```

文 書 番 号	シ-ト No.	版 数
	20	1

0154	CHECK DH. SYSTMTR_FMT_ERR	=OK
0155	CHECK DH. SYSTMTR_SUM_STAT	=OK
0156	CHECK DH. TL_FMT_ERR	=OK
0157	CHECK DH. TL_SUM_STAT	=OK
0158	CHECK DH. CATGRY_SUM_STAT	=OK
0159	CHECK DH. TM1TBL_SUM_STAT	=OK
0160	CHECK DH. TM2TBL_SUM_STAT	=OK
0161	CHECK DH. ONOFKY_SUMERR	=OK
0162	CHECK DH. CPU_CMD_RESET	=NON
0163	CHECK DH. CPUA_ECC_2BE	=NON
0164	CHECK DH. CPUA_SW_RST	=NON
0165	CHECK DH. CPUA_WDT_OVF	=NON
0166	CHECK DH. CPUB_ECC_2BE	=NON
0167	CHECK DH. CPUB_SW_RST	=NON
0168	CHECK DH. CPUB_WDT_OVF	=NON
0169	CHECK DH. CPU_AB	=CPU_A
0170	CHECK DH. HK_ACF5_VALID	=ENA
0171	CHECK DH. HK_DHFS_VALID	=ENA
0172	CHECK DH. HK_SPW_VALID	=ENA
0173	CHECK DH. HK_TCFS_VALID	=ENA
0174	CHECK DH. NML_IPL	=NML
0175	CHECK DH. TC1U_ECC_2BE	=NON
0176	CHECK DH. DR_SUM_STAT	=OK
0177	CHECK PCD. APR1_ON_OFF	=OFF
0178	CHECK PCD. APR2_ON_OFF	=ON
0179	#	
0180	# < CHECK >	
0181	CHECK DH. ENA_TEMP	-
0182	CHECK DH. TANK_TEMP	-
0183	CHECK DH. ADM_TEMP	-
0184	CHECK DH. APM_TEMP	-
0185	CHECK DH. UP_PNL2_TEMP	-
0186	CHECK DH. UP_PNL3_TEMP	-
0187	CHECK DH. UP_PNL6_TEMP	-
0188	CHECK DH. UP_PNL7_TEMP	-
0189	CHECK DH. UP_PNL8_TEMP	-
0190	CHECK DH. LO_PNL1_TEMP	-
0191	CHECK DH. LO_PNL2_TEMP	-
0192	CHECK DH. LO_PNL5_TEMP	-
0193	CHECK DH. LO_PNL6_TEMP	-
0194	CHECK DH. LO_PNL8_TEMP	-
0195	CHECK DH. SSAS_TEMP	-
0196	CHECK DH. THR_T1T2_TEMP	-
0197	CHECK DH. SSC_TEMP1	-
0198	CHECK DH. ACM1_TEMP	-
0199	CHECK DH. THR_T3T4_TEMP	-
0200	CHECK DH. ACM2_TEMP	-
0201	CHECK DH. THR_A1_TEMP	-
0202	CHECK DH. THR_A2_TEMP	-
0203	CHECK DH. VLV_M_TEMP	-
0204	CHECK DH. EPC_A_TEMP	-
0205	CHECK DH. EPC_B_TEMP	-

文 書 番 号	シート No.	版 数
	21	1

```

0206 CHECK DH. TWT_A_TEMP -
0207 CHECK DH. TWT_B_TEMP -
0208 CHECK DH. XTRP_A_TEMP -
0209 CHECK DH. XTRP_B_TEMP -
0210 CHECK DH. BAT_TEMP1 -
0211 CHECK DH. BAT_TEMP2 -
0212 CHECK DH. SSC_TEMP2 -
0213 CHECK DH. SIDE1_TEMP -
0214 CHECK DH. SIDE2_TEMP -
0215 CHECK DH. SIDE3_TEMP -
0216 CHECK DH. SIDE4_TEMP -
0217 CHECK DH. UP_PNL1_TEMP -
0218 CHECK DH. UP_PNL5_TEMP -
0219 CHECK DH. MAST_MGF_TEMP -
0220 CHECK DH. LO_PNL3_TEMP -
0221 CHECK DH. LO_PNL7_TEMP -
0222 CHECK DH. SIDE5_TEMP -
0223 CHECK DH. SIDE6_TEMP -
0224 CHECK DH. SIDE7_TEMP -
0225 CHECK DH. SIDE8_TEMP -
0226 . #
0227 . # < TI_PKT_REQ >
0228 . DH. TI_PKT_REQ
0229 DH. TI_PKT_REQ
0230 WAIT_SEC 1
0231 DH. MC_START
0232 . #
0233 . # < CHECK >
0234 CHECK DH. MC_ENA_DIS =ENA
0235 CHECK PCD. ON_OFF_ENA_DIS =DIS
0236 CHECK PCD. SA_V 86 94
0237 CHECK PCD. BUS_V 48 52
0238 #
0239 . # QL Check-Item Spec Check
0240 # -----+-----+-----+-----
0241 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0242 #
0243 # -----
0244 . # 2.1.2. BAT INT/BUS SW ON
0245 # -----
0246 . #
0247 . PCD. CEL_V_MONI_ON # 【BAT 管理手順書 (TPP-7S8238-03K) へ記録】
0248 WAIT_SEC 1
0249 PCD. BAT_OT_DIS
0250 WAIT_SEC 1
0251 PCD. BAT_OT_ON_SET 0xBD # 28degC
0252 WAIT_SEC 1
0253 DH. BAT_SM_EXE # DH. MC_EACH_EXE 0x0054
0254 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0255 #: 0x00 PCD. BAT_SM_EXE
0256 #: 0x02 PCD. MGA_P_DIS
0257 WAIT_SEC 1

```

文 書 番 号	シート No.	版 数
	22	1

```

0258 PCD. BAT_OT_OFF_SET 0xC5 # 26degC
0259 WAIT_SEC 1
0260 DH. BAT_SM_EXE # DH. MC_EACH_EXE 0x0054
0261 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0262 #: 0x00 PCD. BAT_SM_EXE
0263 #: 0x02 PCD. MGA_P_DIS
0264 #
0265 # < CHECK >
0266 CHECK PCD. CEL1_V >3. 705
0267 CHECK PCD. CEL2_V >3. 705
0268 CHECK PCD. CEL3_V >3. 705
0269 CHECK PCD. CEL4_V >3. 705
0270 CHECK PCD. CEL5_V >3. 705
0271 CHECK PCD. CEL6_V >3. 705
0272 CHECK PCD. CEL7_V >3. 705
0273 CHECK PCD. CEL8_V >3. 705
0274 CHECK PCD. CEL9_V >3. 705
0275 CHECK PCD. CEL10_V >3. 705
0276 CHECK PCD. CEL11_V >3. 705
0277 CHECK PCD. CEL12_V >3. 705
0278 CHECK PCD. BAT_INT_EXT =EXT
0279 CHECK PCD. BUS_SW_ON_OFF =OFF
0280 CHECK PCD. BAT_OT_ENA_DIS =DIS
0281 #
0282 CHECK PCD. BAT_V -
0283 CHECK PCD. CEL1_V -
0284 CHECK PCD. CEL2_V -
0285 CHECK PCD. CEL3_V -
0286 CHECK PCD. CEL4_V -
0287 CHECK PCD. CEL5_V -
0288 CHECK PCD. CEL6_V -
0289 CHECK PCD. CEL7_V -
0290 CHECK PCD. CEL8_V -
0291 CHECK PCD. CEL9_V -
0292 CHECK PCD. CEL10_V -
0293 CHECK PCD. CEL11_V -
0294 CHECK PCD. CEL12_V -
0295 #
0296 #
0297 # < CHECK >
0298 CHECK PCD. OT_ON_SET 27. 5 28. 5
0299 CHECK PCD. OT_OFF_SET 25. 5 26. 5
0300 #
0301 PCD. BAT_OT_ENA
0302 #
0303 # < CHECK >
0304 CHECK PCD. BAT_OT_ENA_DIS =ENA
0305 CHECK PCD. BAT_OT_ON_OFF =OFF
0306 #
0307 # 【確認】 Check
0308 # BUS-V > BAT-V であることを確認する。 G N
0309 #

```

文 書 番 号	シ-ト No.	版 数
	23	1

0310	. PCD. INT_EXT_ENA	
0311	. PCD. BAT_INT	
0312	. #	
0313	. # < CHECK >	
0314	CHECK PCD. INT_EXT_ENA_DIS	=DIS
0315	CHECK PCD. BAT_INT_EXT	=INT
0316	CHECK PCD. BUS_SW_ENA_DIS	=DIS
0317	CHECK PCD. BUS_SW_ON_OFF	=OFF
0318	CHECK PCD. CHG_I	-0.1 0.1
0319	CHECK PCD. DCHG_I	-0.2 0.2
0320	. #	
0321	. PCD. CHG_LR1_ON	
0322	. #	
0323	. # < CHECK >	
0324	CHECK PCD. CHG_LR1_ON_OFF	=ON
0325	CHECK PCD. CHG_LR2_ON_OFF	=OFF
0326	CHECK PCD. CHG_I	0.1 0.3
0327	CHECK PCD. DCHG_I	-0.2 0.2
0328	. #	
0329	. PCD. CHG_ENA	
0330	. #	
0331	. # < CHECK >	
0332	CHECK PCD. CHG_ENA_DIS	=ENA
0333	. #	
0334	. PCD. CHG_CC_L0	
0335	. #	
0336	. # < CHECK >	
0337	CHECK PCD. CCCV_MODE	=L0
0338	. #	
0339	. PCD. CHG_DIS	
0340	. #	
0341	. # < CHECK >	
0342	CHECK PCD. CHG_ENA_DIS	=DIS
0343	CHECK PCD. CHG_I	-0.1 0.1
0344	CHECK PCD. DCHG_I	-0.2 0.2
0345	. #	
0346	. # 【確認】	Check
0347	. # BUS-V と BAT-V の電位差が 2V 以下であることを確認する。	G N
0348	. #	
0349	. PCD. BUS_SW_ENA	
0350	. PCD. BUS_SW_ON	
0351	. #	
0352	. # < CHECK >	
0353	CHECK PCD. INT_EXT_ENA_DIS	=DIS
0354	CHECK PCD. BAT_INT_EXT	=INT
0355	CHECK PCD. BUS_SW_ENA_DIS	=DIS
0356	CHECK PCD. BUS_SW_ON_OFF	=ON
0357	. #	
0358	. PCD. CHG_LR_OFF	
0359	. #	
0360	. # < CHECK >	
0361	CHECK PCD. CHG_LR1_ON_OFF	=OFF

文 書 番 号	シート No.	版 数
	24	1

```

0362 CHECK PCD. CHG_LR2_ON_OFF =OFF
0363 #
0364 . # QL Check-Item Spec Check
0365 # -----+-----+-----+
0366 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0367 #
0368 # -----
0369 . # 2.1.3. K1/K2/CD/CV 設定
0370 # -----
0371 . #
0372 . PCD. BAT_K1_SET 0xA7 # K1 167
0373 WAIT_SEC 1
0374 DH. BAT_SM_EXE # DH. MC_EACH_EXE 0x0054
0375 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0376 #: 0x00 PCD. BAT_SM_EXE
0377 #: 0x02 PCD. MGA_P_DIS
0378 WAIT_SEC 1
0379 PCD. BAT_K2_SET 0xA5 # K2 165
0380 WAIT_SEC 1
0381 DH. BAT_SM_EXE # DH. MC_EACH_EXE 0x0054
0382 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0383 #: 0x00 PCD. BAT_SM_EXE
0384 #: 0x02 PCD. MGA_P_DIS
0385 WAIT_SEC 1
0386 PCD. BAT_CD_SET 0x80 # 10.56Ah
0387 WAIT_SEC 1
0388 DH. BAT_SM_EXE # DH. MC_EACH_EXE 0x0054
0389 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0390 #: 0x00 PCD. BAT_SM_EXE
0391 #: 0x02 PCD. MGA_P_DIS
0392 WAIT_SEC 1
0393 PCD. BAT_CV_SET 0xCC # 48.047V (SOC フル充電)
0394 WAIT_SEC 1
0395 DH. BAT_SM_EXE # DH. MC_EACH_EXE 0x0054
0396 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0397 #: 0x00 PCD. BAT_SM_EXE
0398 #: 0x02 PCD. MGA_P_DIS
0399 . #
0400 . # < CHECK >
0401 CHECK PCD. BAT_K1 167
0402 CHECK PCD. BAT_K2 165
0403 CHECK PCD. BAT_CD 10.55 10.57
0404 CHECK PCD. BAT_CV 47.97 48.12
0405 #
0406 . # QL Check-Item Spec Check
0407 # -----+-----+-----+
0408 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0409 #
0410 # -----
0411 . # 2.1.4. インデックスパルス出力
0412 # -----
0413 #

```

文 書 番 号	シート No.	版 数
	25	1

```

0414 . # < CHECK >
0415 CHECK ACS.F_ACS_PKT_FMT =ACSHK1
0416 CHECK ACS.STS_IP_SRC =NULL
0417 CHECK ACS.STS_IP_OUT =DIS
0418 CHECK ACS.STS_IP_GNRT =DIS
0419 CHECK ACS.I_IPCHK =RST
0420 CHECK ACS.I_IP_GNRT =SELF
0421 CHECK ACS_SYS.I_IPCHK =RST
0422 CHECK ACS_SYS.I_IP_GNRT =SELF
0423 CHECK ACS.BIT_I_IPCHK =RST
0424 #
0425 . # SSAS を擬似モードにする
0426 . ACS.SSAS_SEL_IMT
0427 #
0428 # 疑似サンパルスを発生
0429 . # (SIN:2.5V, COS:5.0V, START:0x80, COARSE:0x10, Period:4.138sec)
0430 . ACS.DUM_SP_ON 2047 4095 128 16 8475
0431 #
0432 . # < CHECK >
0433 CHECK ACS.SSAS_ST_PT =IMT
0434 CHECK ACS.SSAS_ST =IMT
0435 CHECK ACS.F_SPN_ERR_SEL =OFF
0436 CHECK ACS.P_SPIN_SSAS_SEL 4.137 4.139
0437 CHECK ACS.THT_CRS_SEL 0x10
0438 CHECK ACS.SUN_ANG_COS 4.999 5.001
0439 CHECK ACS.SUN_ANG_SIN 2.499 2.501
0440 CHECK ACS.SUN_ANG_SEL -2 2
0441 CHECK ACS_SYS.SSAS_ST =IMT
0442 CHECK ACS_SYS.P_SPIN_SSAS_SEL 4.137 4.139
0443 CHECK ACS_SYS.THT_CRS_SEL 0x10
0444 CHECK ACS_SYS.SUN_ANG_SEL -2 2
0445 CHECK ACS.BIT_SSAS_ST =IMT
0446 CHECK ACS.BIT_P_SPIN_SSAS_SEL 4.013 4.263
0447 CHECK ACS.BIT_SUN_ANG_SEL -0.125 0.125
0448 #
0449 . # ACS HK パケットフォーマットを ACS_HK4 に設定
0450 . ACS.ACS_PKT_FMT_4
0451 #
0452 . # < CHECK >
0453 CHECK ACS.SSAS_IMT_SP_STS =ON
0454 CHECK ACS.E_SSAS_SPIN 0.0999 0.1001
0455 CHECK ACS.N_SPINRATE_AVE 5 5
0456 CHECK ACS_SYS.F_ECL =OFF
0457 CHECK ACS_SYS.I_ATT =STBY
0458 CHECK ACS_SYS.P_B_AVE 4.137 4.139
0459 #
0460 . # IP フィードバックゲインを設定する。
0461 . ACS.IP_K_DT 0x3C9BA5E3 0x3C9BA5E3
0462 #
0463 . # < CHECK >
0464 CHECK ACS.K_DT0 0.018999 0.019001
0465 CHECK ACS.K_DT1 0.018999 0.019001

```

```

0466 . #
0467 . # IP 源泉を IP にする
0468 . ACS. IP_SRC_IP
0469 . # IP 生成機能を ON にする
0470 . ACS. IP_GNRT_ON
0471 . # MDP への IP 出力を開始する
0472 . ACS. IP_OUT_ENA
0473 . # ACS HK パケットフォーマットを ACS_HK1 に設定
0474 . ACS. ACS_PKT_FMT_1
0475 . #
0476 . # < CHECK >
0477 . CHECK ACS. STS_IP_SRC =IP
0478 . CHECK ACS. STS_IP_OUT =ENA
0479 . CHECK ACS. STS_IP_GNRT =ENA
0480 . CHECK ACS. I_IP_GNRT =SSAS
0481 . CHECK ACS_SYS. I_IP_GNRT =SSAS
0482 . #
0483 . # << DR STATUS CHECK >>
0484 . #
0485 . # < CHECK >
0486 . CHECK DH. DR_STATE =STANDBY
0487 . CHECK DH. DR_CHK_ON_OFF =OFF
0488 . #
0489 . #
0490 . DH. DR_ERRCLR
0491 . #
0492 . # QL Check-Item Spec Check
0493 . # -----+-----+-----+-----+
0494 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0495 . #
0496 . #
0497 . # =====
0498 . # 2.2. RF LINK ON
0499 . # =====
0500 . #
0501 . DH. XSW1_CROSS
0502 . TCIU. XSW2_CROSS
0503 . #
0504 . # < CHECK >
0505 . CHECK DH. XSW1_PARA_CROSS_SEL =MGA_TO_TWTA_A
0506 . CHECK DH. XSW2_PARA_CROSS_SEL =MGA_TO_XTRP_A
0507 . #
0508 . PCD. ON_OFF_ENA
0509 . #
0510 . # < CHECK >
0511 . CHECK PCD. ON_OFF_ENA_DIS =ENA
0512 . #
0513 . PCD. EPC_B_ON
0514 . PCD. ON_OFF_DIS
0515 . #
0516 . # < CHECK >
0517 . CHECK PCD. EPC_B_ON_OFF =ON

```

文 書 番 号	シート No.	版 数
TPP-FS8238-31K	27	1

0518	CHECK PCD.ON_OFF_ENA_DIS	=DIS		
0519	#			
0520	TCIU.XTWTB_B_FIL_ON	#【有効寿命・特性値管理手順書(TPP-7S8238-02K)へ記録】		
0521	#			
0522	# < CHECK >			
0523	CHECK ACS_SYS.TWTA_B_FIL_ON_OFF	=ON		
0524	CHECK ACS_SYS.TWTA_B_ARU_STATUS	=DIS		
0525	CHECK ACS.TWTA_B_FIL_ON_OFF	=ON		
0526	CHECK ACS.TWTA_B_ARU_STATUS	=DIS		
0527	CHECK DH.XTRP_B_CARR_LOCK	=OFF		
0528	CHECK DH.XTRP_B_DEM_LOCK	=OFF		
0529	CHECK DH.XTRP_B_RRNG_LOCK	=OFF		
0530	CHECK DH.XTRP_B_BIT_RATE	=BPS15_6		
0531	CHECK DH.XTRP_B_AUTO_COH_ENA_DIS	=ENA		
0532	CHECK DH.XTRP_B_COH_NCOH	=NCOH		
0533	CHECK DH.XTRP_B_TX_ON_OFF	=OFF		
0534	CHECK DH.XTRP_B_TLM_ON_OFF	=OFF		
0535	CHECK DH.XTRP_B_RNG_ON_OFF	=OFF		
0536	CHECK DH.XTRP_B_RRNG_ON_OFF	=OFF		
0537	CHECK DH.XTRP_B_RNG_THRU_LPF	=LPF		
0538	CHECK DH.XTRP_B_TL_MOD	0		
0539	CHECK DH.XTRP_B_RN_MOD	0		
0540	#			
0541	# 【局管制設定】			
0542	# 地上装置の Uplink を以下の通り設定する。			
0543	# ・入力レベル : -100dBm(XTRP 入力端)			
0544	# 以下の手順で X 帯インタフェース装置架を設定する			
0545	# (1)X 帯インタフェース装置架 フロントパネルの【ATTENUATOR/SWITCH DRIVER】			
0546	# の【10】と【20】が点灯、それ以外が消灯していることを確認			
0547	# (2)監視制御端末【SUMMARY】X-TX の ATT の値を【79dB → 0dB】に変更			
0548	# (3)X 帯インタフェース装置架 RF OUTPUT PANEL(TX 側)の RF OUT LEVEL デジタル			
0549	# 表示が【30】になっていることを確認(この時点で XTRP 入力レベル-100dBm)			
0550	# ・周波数 : 7158.844908MHz			
0551	# ・SWEEP : RATE 300Hz/s, WIDTH 8kHz INC			
0552	# ・ROUTE : X-ANT			
0553	#			
0554	# 【Uplink】			
0555	# (1)監視制御端末【X-TCRMOD】の IF を【ON】にする			
0556	# (2)監視制御端末【X 帯送信制御ウィンドウ】の RF POWER【ON】にする			
0557	# (3)QL の TCIU 画面で AGC のレベルを確認(U/L ATT の値を調整)			
0558	# (参考: 2017/2 月実施時は-95.79dBm)			
0559	# (4)監視制御端末【X 帯送信制御ウィンドウ】の SWEEP-CONT の【INC】を実行する			
0560	#			
0561	# < CHECK >			
0562	CHECK DH.XTRP_B_CARR_LOCK	=ON		
0563	CHECK DH.XTRP_B_AUTO_COH_ENA_DIS	=ENA		
0564	CHECK DH.XTRP_B_COH_NCOH	=COH		
0565	#			
0566	# QL	Check-Item	Spec	Data
0567	#			
0568	# TCIU	DH.XTRP_B_LPS	記録のみ	N/A
0569	#			

文 書 番 号	シート No.	版 数
	28	1

```

0570 . # !!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
0571 . # 【注意】FIL ON から HV ON まで 5min 以上待機のこと。
0572 . # !!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
0573 . #
0574 . TCIU.XTWTB_B_HV_ON      # 【有効寿命・特性値管理手順書 (TPP-7S8238-02K) へ記録】
0575 . #
0576 . # < CHECK >
0577 . CHECK ACS_SYS.TWTA_B_FIL_ON_OFF          =ON
0578 . CHECK ACS_SYS.TWTA_B_ARU_STATUS          =ENA
0579 . CHECK ACS.TWTA_B_FIL_ON_OFF              =ON
0580 . CHECK ACS.TWTA_B_ARU_STATUS              =ENA
0581 . CHECK DH.TWTA_B_HLX_CURR                 <5.0
0582 . #
0583 . TCIU.XTRP_B_T_MOD095
0584 . TCIU.XTRP_B_TLMMOD_ON
0585 . DH.XTRP_B_TX_ON
0586 . #
0587 . # < CHECK >
0588 . CHECK DH.XTRP_B_TX_ON_OFF                 =ON
0589 . CHECK DH.XTRP_B_TLM_ON_OFF               =ON
0590 . CHECK DH.XTRP_B_TL_MOD                   0.94 0.96
0591 . CHECK DH.XTRP_B_RN_MOD                   0.05
0592 . #
0593 . # 【局管制設定】
0594 . # 監視制御端末【SUMMARY】X-RX の ATT の値を 79dB → 20dB に徐々に下げていく
0595 . # (X 帯受信計測装置架の DIGITAL DEMODULATOR のディスプレイで
0596 . # 【DIGITAL DEMODULATOR(1/12)】の【INPUT LEVEL】が-40dBm 程度に調整する。)
0597 . #
0598 . # 【局管制設定】
0599 . # SUMMARY 画面にて、X-TLM: 32kbps_RF を選択実行する。
0600 . # SUMMARY 画面にて、SCR/BIT/VIT/FRM が全て ON となることを確認する。
0601 . #
0602 . # 【衛星管制設定】
0603 . # RF/LINE 切り替え      : RF
0604 . #
0605 . # 【地上装置設定】
0606 . # RD LINK OFF
0607 . #
0608 . # 【局管制設定】
0609 . # SUMMARY 画面にて、X-CMD: L(15.625) を選択実行する。
0610 . #
0611 . # 【局管制設定】
0612 . # CMD 変調: 0.85rad (目標値 0.8rad)
0613 . # UP LINK:  CMD MOD ON
0614 . # UP LINK:  HOLD TONE ON
0615 . #
0616 . # < CHECK >
0617 . CHECK DH.XTRP_B_CARR_LOCK                 =ON
0618 . CHECK DH.XTRP_B_DEM_LOCK                 =ON
0619 . CHECK DH.XTRP_B_BIT_RATE                 =BPS15_6
0620 . #
0621 . TCIU.XTRP_B_CMDRT_1K

```

```

0622  #
0623  # < CHECK >
0624  CHECK DH. XTRP_B_CARR_LOCK                =ON
0625  CHECK DH. XTRP_B_DEM_LOCK                  =ON
0626  CHECK DH. XTRP_B_BIT_RATE                  =BPS1000
0627  #
0628  # 【局管制設定】
0629  # CMDRT_1K コマンド送信後 130s 以内に SUMMARY 画面にて、X-CMD: H(1000) を選択実
0630  # 行する。
0631  #
0632  #
0633  DH. DUMMY
0634  #
0635  # QL          Check-Item          Spec          Check
0636  # -----+-----+-----+-----+
0637  # DH1          DH. CMD_EXE_CNT      +1 増加      G    N
0638  # DH1          DH. CMD_DLV_CNT      +1 増加      G    N
0639  #
0640  #
0641  # QL          Check-Item          Spec          Check
0642  # -----+-----+-----+-----+
0643  # CMD 出力装置  Message          STATUS CHECK 結果 OK    G    N
0644  #
0645  # =====
0646  # 2.3. TLM_MOD 1.5rad CHECK
0647  # =====
0648  #
0649  # TLM MOD 1.5rad 設定(コマンド設定値 : 1.1rad/目標値 : 1.5rad)
0650  TCIU. XTRP_B_TLMMOD_OFF
0651  #
0652  TCIU. XTRP_B_T_MOD110
0653  TCIU. XTRP_B_TLMMOD_ON
0654  #
0655  # < CHECK >
0656  CHECK DH. XTRP_B_TX_ON_OFF                =ON
0657  CHECK DH. XTRP_B_TLM_ON_OFF                =ON
0658  CHECK DH. XTRP_B_TL_MOD                    1.099 1.101
0659  #
0660  #
0661  # TLM MOD 0.95rad に戻す
0662  TCIU. XTRP_B_TLMMOD_OFF
0663  #
0664  TCIU. XTRP_B_T_MOD095
0665  TCIU. XTRP_B_TLMMOD_ON
0666  #
0667  # < CHECK >
0668  CHECK DH. XTRP_B_TX_ON_OFF                =ON
0669  CHECK DH. XTRP_B_TLM_ON_OFF                =ON
0670  CHECK DH. XTRP_B_TL_MOD                    0.949 0.951
0671  #
0672  # 【衛星管制設定】
0673  # プリチェック          : ON

```

文 書 番 号	シート No.	版 数
	30	1

```

0674 . # センドベリファイ : ON
0675 . # サクセスベリファイ: ON
0676 . # COP N(S)値設定
0677 . #
0678 . # QL          Check-Item          Spec          Check
0679 . # -----+-----+-----+-----
0680 . # CMD 出力装置  Message          STATUS CHECK 結果 OK   G   N
0681 . #
0682 . # =====
0683 . # 2.4. ACS 立ち上げ
0684 . # =====
0685 . #
0686 . DH. OPE_MODE 0x0D 0x4F 0x23 0x00 0x000000000000000000000000
0687 . #
0688 . # < CHECK >
0689 . CHECK DH. CATEGORY_TBL_NO          0x23
0690 . CHECK DH. DWNLNK_RATE              =BPS32768
0691 . #
0692 . ACS. ACS_PKT_FMT_1
0693 . #
0694 . # < CHECK >
0695 . CHECK ACS. F_ACS_PKT_FMT          =ACSHK1
0696 . #
0697 . TCIU. SSAS_A_ON
0698 . #
0699 . # < CHECK >
0700 . CHECK ACS. STS_SSASA_PWR          =ON
0701 . CHECK ACS. STS_SSASB_PWR          =OFF
0702 . CHECK ACS_SYS. STS_SSASA_PWR      =ON
0703 . CHECK ACS_SYS. STS_SSASB_PWR      =OFF
0704 . CHECK ACS. BIT_STS_SSASA_PWR      =ON
0705 . CHECK ACS. BIT_STS_SSASB_PWR      =OFF
0706 . #
0707 . ACS. SSC_ON
0708 . #
0709 . # < CHECK >
0710 . CHECK ACS. SSC_CAL                =MES
0711 . CHECK ACS. SSC_PWR                =ON
0712 . CHECK ACS_SYS. SSC_CAL            =MES
0713 . CHECK ACS_SYS. SSC_PWR            =ON
0714 . CHECK ACS. BIT_SSC_PWR            =ON
0715 . #
0716 . PCD. ON_OFF_ENA
0717 . #
0718 . # < CHECK >
0719 . CHECK PCD. ON_OFF_ENA_DIS          =ENA
0720 . #
0721 . PCD. DRU_PSU_A_ON
0722 . #
0723 . # < CHECK >
0724 . CHECK PCD. DRU_PSU_A_ON_OFF        =ON
0725 . #

```

文 書 番 号	シ-ト No.	版 数
	31	1

```

0726 . PCD.ON_OFF_DIS
0727 . #
0728 . # < CHECK >
0729 . CHECK PCD.ON_OFF_ENA_DIS =DIS
0730 . #
0731 . ACS.DRU_SEL_A
0732 . #
0733 . # < CHECK >
0734 . CHECK ACS.DRU_SEL_PT =A
0735 . CHECK ACS_SYS.DRU_SEL_PT =A
0736 . CHECK ACS.BIT_DRU_SEL_PT =A
0737 . #
0738 . ACS.ACS_IOPERR_CLR
0739 . #
0740 . # < CHECK >
0741 . CHECK ACS.DRU_A_ONOFF_ST =ON
0742 . CHECK ACS.DRU_B_ONOFF_ST =OFF
0743 . CHECK ACS.DRU_ACIMIF_A_ST =ON
0744 . CHECK ACS.DRU_ACIMIF_B_ST =OFF
0745 . CHECK ACS.SRAM1_MEMST =MEMO
0746 . CHECK ACS.SRAM2_MEMST =MEMO
0747 . CHECK ACS.C_DRU_CMD_ECC2 0
0748 . CHECK ACS.C_DRU_TLM_ECC2 0
0749 . CHECK ACS.C_LBUSU_ECC1 0
0750 . CHECK ACS.C_LBUSL_ECC1 0
0751 . CHECK ACS.C_DRU_CMD_Prt 0
0752 . CHECK ACS.C_DRU_TLM_Prt 0
0753 . CHECK ACS.DATA_ERR_STATUS =OK
0754 . CHECK ACS.STS_DRUAPM_ON =OFF
0755 . CHECK ACS.I_ADMCE_A_PWR =OFF
0756 . CHECK ACS.I_ADMCE_B_PWR =OFF
0757 . CHECK ACS.ADM_SEL_PORT1 =A
0758 . CHECK ACS.SRAM_ST =SRAM1
0759 . CHECK ACS_SYS.DRU_A_ONOFF_ST =ON
0760 . CHECK ACS_SYS.DRU_B_ONOFF_ST =OFF
0761 . CHECK ACS_SYS.DRU_ACIMIF_A_ST =ON
0762 . CHECK ACS_SYS.DRU_ACIMIF_B_ST =OFF
0763 . CHECK ACS_SYS.SRAM1_MEMST =MEMO
0764 . CHECK ACS_SYS.SRAM2_MEMST =MEMO
0765 . CHECK ACS_SYS.C_DRU_CMD_ECC2 0
0766 . CHECK ACS_SYS.C_DRU_TLM_ECC2 0
0767 . CHECK ACS_SYS.C_LBUSU_ECC1 0
0768 . CHECK ACS_SYS.C_LBUSL_ECC1 0
0769 . CHECK ACS_SYS.C_DRU_CMD_Prt 0
0770 . CHECK ACS_SYS.C_DRU_TLM_Prt 0
0771 . CHECK ACS_SYS.DATA_ERR_STATUS =OK
0772 . CHECK ACS_SYS.STS_DRUAPM_ON =OFF
0773 . CHECK ACS_SYS.I_ADMCE_A_PWR =OFF
0774 . CHECK ACS_SYS.I_ADMCE_B_PWR =OFF
0775 . CHECK ACS_SYS.ADM_SEL_PORT1 =A
0776 . CHECK ACS_SYS.SRAM_ST =SRAM1
0777 . CHECK ACS.BIT_DRU_A_ONOFF_ST =ON

```

文 書 番 号	シート No.	版 数
	32	1

```

0778 CHECK ACS.BIT_DRU_B_ONOFF_ST =OFF
0779 CHECK ACS.BIT_I_ADMCE_A_PWR =OFF
0780 CHECK ACS.BIT_I_ADMCE_B_PWR =OFF
0781 . #
0782 . ACS.ACS_PKT_FMT_4
0783 . #
0784 . # < CHECK >
0785 CHECK ACS.F_ACS_PKT_FMT =ACSHK4
0786 CHECK ACS.I_ECCERR_S_D 0
0787 CHECK ACS.I_ECCERR_S_L 0
0788 CHECK ACS.I_DRU_ACIMIF1 0
0789 CHECK ACS.I_DRU_ACIMIF2 0
0790 CHECK ACS.I_DRU_ACIMIF3 0
0791 CHECK ACS.I_DRU_ACIMIF4 0
0792 CHECK ACS.DRU_CMD_ECC2 0
0793 CHECK ACS.DRU_TLM_ECC2 0
0794 CHECK ACS.DRU_CMD_Parity1 0
0795 CHECK ACS.DRU_CMD_Parity2 0
0796 CHECK ACS.DRU_TLM_Parity1 0
0797 CHECK ACS.DRU_TLM_Parity2 0
0798 CHECK ACS.I_CMD_FLAG1 0x02ec0c00
0799 CHECK ACS.I_CMD_FLAG2 0
0800 . #
0801 . ACS.ACS_PKT_FMT_1
0802 . #
0803 . # < CHECK >
0804 CHECK ACS.F_ACS_PKT_FMT =ACSHK1
0805 . #
0806 . # QL Check-Item Spec Check
0807 . # -----+-----+-----+-----
0808 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0809 . #
0810 . # =====
0811 . # 2.5. DR 設定
0812 . # =====
0813 . #
0814 . # 各パーティション記録数通知のクリア
0815 . DH.DR_RECNTCLRALL
0816 . #
0817 . # DR ポインタログ出力周期設定 (32s)
0818 . DH.DR_PTLOGCYC_SET 0x00
0819 . #
0820 . # DR ポインタログ出力 Enable
0821 . DH.DR_PTRLOG_ENA
0822 . #
0823 . # < CHECK >
0824 CHECK DH.DR_PTRLOG_EN_DS =ENA
0825 . #
0826 . # パーティション分割状況通知パッケージを出力
0827 . DH.DR_PARTDVNOTIFY
0828 . #
0829 . # 各パーティション記録数通知パッケージを出力

```

```

0830 . DH_DR_RECNTNOTIFY
0831 #
0832 . # QL                      Check-Item          Spec                      Check
0833 . # -----+-----+-----+-----
0834 . # CMD 出力装置      Message          STATUS CHECK 結果 OK      G      N
0835 . #
0836 . # =====
0837 . # 2.6. TCFS 設定
0838 . # =====
0839 . #
0840 . TCS.SET_TBL_ENA
0841 . #
0842 . # < CHECK >
0843 . CHECK TCS.SET_TBL_ST          =ENA
0844 . #
0845 . # < BKUP DTY TBL1 >
0846 . TCS.SET_BKUP_DTY_TBL 0x00 0x00 0x0020      # BKUP DTY TBL1 CH01: 32
0847 . TCS.SET_BKUP_DTY_TBL 0x00 0x01 0x0020      # BKUP DTY TBL1 CH02: 32
0848 . TCS.SET_BKUP_DTY_TBL 0x00 0x02 0x0020      # BKUP DTY TBL1 CH03: 32
0849 . TCS.SET_BKUP_DTY_TBL 0x00 0x03 0x0020      # BKUP DTY TBL1 CH04: 32
0850 . TCS.SET_BKUP_DTY_TBL 0x00 0x04 0x0020      # BKUP DTY TBL1 CH05: 32
0851 . TCS.SET_BKUP_DTY_TBL 0x00 0x05 0x0020      # BKUP DTY TBL1 CH06: 32
0852 . TCS.SET_BKUP_DTY_TBL 0x00 0x06 0x0020      # BKUP DTY TBL1 CH07: 32
0853 . TCS.SET_BKUP_DTY_TBL 0x00 0x07 0x0020      # BKUP DTY TBL1 CH08: 32
0854 . TCS.SET_BKUP_DTY_TBL 0x00 0x08 0x0020      # BKUP DTY TBL1 CH09: 32
0855 . TCS.SET_BKUP_DTY_TBL 0x00 0x09 0x0020      # BKUP DTY TBL1 CH10: 32
0856 . TCS.SET_BKUP_DTY_TBL 0x00 0x0A 0x0020      # BKUP DTY TBL1 CH11: 32
0857 . TCS.SET_BKUP_DTY_TBL 0x00 0x0B 0x0020      # BKUP DTY TBL1 CH12: 32
0858 . TCS.SET_BKUP_DTY_TBL 0x00 0x0C 0x0020      # BKUP DTY TBL1 CH13: 32
0859 . TCS.SET_BKUP_DTY_TBL 0x00 0x0D 0x0000      # BKUP DTY TBL1 CH14: 0
0860 . TCS.SET_BKUP_DTY_TBL 0x00 0x0E 0x0000      # BKUP DTY TBL1 CH15: 0
0861 . TCS.SET_BKUP_DTY_TBL 0x00 0x0F 0x0000      # BKUP DTY TBL1 CH16: 0
0862 . TCS.SET_BKUP_DTY_TBL 0x00 0x10 0x0020      # BKUP DTY TBL1 CH17: 32
0863 . TCS.SET_BKUP_DTY_TBL 0x00 0x11 0x0020      # BKUP DTY TBL1 CH18: 32
0864 . TCS.SET_BKUP_DTY_TBL 0x00 0x12 0x0020      # BKUP DTY TBL1 CH19: 32
0865 . TCS.SET_BKUP_DTY_TBL 0x00 0x13 0x0020      # BKUP DTY TBL1 CH20: 32
0866 . TCS.SET_BKUP_DTY_TBL 0x00 0x14 0x0020      # BKUP DTY TBL1 CH21: 32
0867 . TCS.SET_BKUP_DTY_TBL 0x00 0x15 0x0020      # BKUP DTY TBL1 CH22: 32
0868 . TCS.SET_BKUP_DTY_TBL 0x00 0x16 0x0020      # BKUP DTY TBL1 CH23: 32
0869 . TCS.SET_BKUP_DTY_TBL 0x00 0x17 0x0020      # BKUP DTY TBL1 CH24: 32
0870 . TCS.SET_BKUP_DTY_TBL 0x00 0x18 0x0020      # BKUP DTY TBL1 CH25: 32
0871 . TCS.SET_BKUP_DTY_TBL 0x00 0x19 0x0020      # BKUP DTY TBL1 CH26: 32
0872 . TCS.SET_BKUP_DTY_TBL 0x00 0x1A 0x0020      # BKUP DTY TBL1 CH27: 32
0873 . TCS.SET_BKUP_DTY_TBL 0x00 0x1B 0x0020      # BKUP DTY TBL1 CH28: 32
0874 . TCS.SET_BKUP_DTY_TBL 0x00 0x1C 0x0020      # BKUP DTY TBL1 CH29: 32
0875 . TCS.SET_BKUP_DTY_TBL 0x00 0x1D 0x0000      # BKUP DTY TBL1 CH30: 0
0876 . TCS.SET_BKUP_DTY_TBL 0x00 0x1E 0x0000      # BKUP DTY TBL1 CH31: 0
0877 . TCS.SET_BKUP_DTY_TBL 0x00 0x1F 0x0000      # BKUP DTY TBL1 CH32: 0
0878 . #
0879 . # < NMNL DTY TBL1 >
0880 . TCS.SET_NMNL_DTY_TBL 0x00 0x00 0x0040      # NMNL DTY TBL1 CH01: 64
0881 . TCS.SET_NMNL_DTY_TBL 0x00 0x01 0x0040      # NMNL DTY TBL1 CH02: 64

```

文 書 番 号	シート No.	版 数
	34	1

```

0882 TCS.SET_NMNL_DTY_TBL 0x00 0x02 0x0040 # NMNL DTY TBL1 CH03: 64
0883 TCS.SET_NMNL_DTY_TBL 0x00 0x03 0x0040 # NMNL DTY TBL1 CH04: 64
0884 TCS.SET_NMNL_DTY_TBL 0x00 0x04 0x0040 # NMNL DTY TBL1 CH05: 64
0885 TCS.SET_NMNL_DTY_TBL 0x00 0x05 0x0040 # NMNL DTY TBL1 CH06: 64
0886 TCS.SET_NMNL_DTY_TBL 0x00 0x06 0x0040 # NMNL DTY TBL1 CH07: 64
0887 TCS.SET_NMNL_DTY_TBL 0x00 0x07 0x0040 # NMNL DTY TBL1 CH08: 64
0888 TCS.SET_NMNL_DTY_TBL 0x00 0x08 0x0040 # NMNL DTY TBL1 CH09: 64
0889 TCS.SET_NMNL_DTY_TBL 0x00 0x09 0x0040 # NMNL DTY TBL1 CH10: 64
0890 TCS.SET_NMNL_DTY_TBL 0x00 0x0A 0x0040 # NMNL DTY TBL1 CH11: 64
0891 TCS.SET_NMNL_DTY_TBL 0x00 0x0B 0x0040 # NMNL DTY TBL1 CH12: 64
0892 TCS.SET_NMNL_DTY_TBL 0x00 0x0C 0x0040 # NMNL DTY TBL1 CH13: 64
0893 TCS.SET_NMNL_DTY_TBL 0x00 0x0D 0x0000 # NMNL DTY TBL1 CH14: 0
0894 TCS.SET_NMNL_DTY_TBL 0x00 0x0E 0x0000 # NMNL DTY TBL1 CH15: 0
0895 TCS.SET_NMNL_DTY_TBL 0x00 0x0F 0x0000 # NMNL DTY TBL1 CH16: 0
0896 TCS.SET_NMNL_DTY_TBL 0x00 0x10 0x0040 # NMNL DTY TBL1 CH17: 64
0897 TCS.SET_NMNL_DTY_TBL 0x00 0x11 0x0040 # NMNL DTY TBL1 CH18: 64
0898 TCS.SET_NMNL_DTY_TBL 0x00 0x12 0x0040 # NMNL DTY TBL1 CH19: 64
0899 TCS.SET_NMNL_DTY_TBL 0x00 0x13 0x0040 # NMNL DTY TBL1 CH20: 64
0900 TCS.SET_NMNL_DTY_TBL 0x00 0x14 0x0040 # NMNL DTY TBL1 CH21: 64
0901 TCS.SET_NMNL_DTY_TBL 0x00 0x15 0x0040 # NMNL DTY TBL1 CH22: 64
0902 TCS.SET_NMNL_DTY_TBL 0x00 0x16 0x0040 # NMNL DTY TBL1 CH23: 64
0903 TCS.SET_NMNL_DTY_TBL 0x00 0x17 0x0040 # NMNL DTY TBL1 CH24: 64
0904 TCS.SET_NMNL_DTY_TBL 0x00 0x18 0x0040 # NMNL DTY TBL1 CH25: 64
0905 TCS.SET_NMNL_DTY_TBL 0x00 0x19 0x0040 # NMNL DTY TBL1 CH26: 64
0906 TCS.SET_NMNL_DTY_TBL 0x00 0x1A 0x0040 # NMNL DTY TBL1 CH27: 64
0907 TCS.SET_NMNL_DTY_TBL 0x00 0x1B 0x0040 # NMNL DTY TBL1 CH28: 64
0908 TCS.SET_NMNL_DTY_TBL 0x00 0x1C 0x0040 # NMNL DTY TBL1 CH29: 64
0909 TCS.SET_NMNL_DTY_TBL 0x00 0x1D 0x0000 # NMNL DTY TBL1 CH30: 0
0910 TCS.SET_NMNL_DTY_TBL 0x00 0x1E 0x0000 # NMNL DTY TBL1 CH31: 0
0911 TCS.SET_NMNL_DTY_TBL 0x00 0x1F 0x0000 # NMNL DTY TBL1 CH32: 0
0912 #
0913 # < NMNL TBL1 ヒータ ON/OFF スレッシュホールド設定 >
0914 TCS.SET_NMNL_TMP_TBL 0x00 0x00 0x9E 0xA2 # NMNL TBL1 CH01 ON: 27.94 OFF: 30
0915 TCS.SET_NMNL_TMP_TBL 0x00 0x01 0x9C 0xA0 # NMNL TBL1 CH02 ON: 27.94 OFF: 30
0916 TCS.SET_NMNL_TMP_TBL 0x00 0x02 0x9B 0x9F # NMNL TBL1 CH03 ON: 27.94 OFF: 30
0917 TCS.SET_NMNL_TMP_TBL 0x00 0x03 0x98 0x9C # NMNL TBL1 CH04 ON: 27.94 OFF: 30
0918 TCS.SET_NMNL_TMP_TBL 0x00 0x04 0x9C 0xA0 # NMNL TBL1 CH05 ON: 27.94 OFF: 30
0919 TCS.SET_NMNL_TMP_TBL 0x00 0x05 0x9B 0x9F # NMNL TBL1 CH06 ON: 27.94 OFF: 30
0920 TCS.SET_NMNL_TMP_TBL 0x00 0x06 0x9C 0xA0 # NMNL TBL1 CH07 ON: 27.94 OFF: 30
0921 TCS.SET_NMNL_TMP_TBL 0x00 0x07 0x83 0x84 # NMNL TBL1 CH08 ON: 28.46 OFF: 29.49
0922 TCS.SET_NMNL_TMP_TBL 0x00 0x08 0x9D 0xA1 # NMNL TBL1 CH09 ON: 27.94 OFF: 30
0923 TCS.SET_NMNL_TMP_TBL 0x00 0x09 0x9C 0xA0 # NMNL TBL1 CH10 ON: 27.94 OFF: 30
0924 TCS.SET_NMNL_TMP_TBL 0x00 0x0A 0x9A 0x9E # NMNL TBL1 CH11 ON: 28.17 OFF: 30.22
0925 TCS.SET_NMNL_TMP_TBL 0x00 0x0B 0x9A 0x9E # NMNL TBL1 CH12 ON: 27.94 OFF: 30
0926 TCS.SET_NMNL_TMP_TBL 0x00 0x0C 0x9B 0x9F # NMNL TBL1 CH13 ON: 27.94 OFF: 30
0927 TCS.SET_NMNL_TMP_TBL 0x00 0x0D 0x00 0x00 # NMNL TBL1 CH14 ON: -50.41 OFF: -50.41
0928 TCS.SET_NMNL_TMP_TBL 0x00 0x0E 0x00 0x00 # NMNL TBL1 CH15 ON: -50.41 OFF: -50.41
0929 TCS.SET_NMNL_TMP_TBL 0x00 0x0F 0x00 0x00 # NMNL TBL1 CH16 ON: -50.41 OFF: -50.41
0930 TCS.SET_NMNL_TMP_TBL 0x00 0x10 0x9E 0xA2 # NMNL TBL1 CH17 ON: 27.94 OFF: 30
0931 TCS.SET_NMNL_TMP_TBL 0x00 0x11 0x9D 0xA1 # NMNL TBL1 CH18 ON: 27.94 OFF: 30
0932 TCS.SET_NMNL_TMP_TBL 0x00 0x12 0x9C 0xA0 # NMNL TBL1 CH19 ON: 27.94 OFF: 30
0933 TCS.SET_NMNL_TMP_TBL 0x00 0x13 0x9C 0xA0 # NMNL TBL1 CH20 ON: 27.94 OFF: 30

```

文 書 番 号	シート No.	版 数
	35	1

0934	TCS. SET_NMNL_TMP_TBL 0x00 0x14 0x9D 0xA1	# NMNL TBL1 CH21 ON: 27.94 OFF: 30
0935	TCS. SET_NMNL_TMP_TBL 0x00 0x15 0x9B 0x9F	# NMNL TBL1 CH22 ON: 27.94 OFF: 30
0936	TCS. SET_NMNL_TMP_TBL 0x00 0x16 0x9A 0x9E	# NMNL TBL1 CH23 ON: 27.94 OFF: 30
0937	TCS. SET_NMNL_TMP_TBL 0x00 0x17 0x9F 0xA3	# NMNL TBL1 CH24 ON: 27.94 OFF: 30
0938	TCS. SET_NMNL_TMP_TBL 0x00 0x18 0x9A 0x9E	# NMNL TBL1 CH25 ON: 27.94 OFF: 30
0939	TCS. SET_NMNL_TMP_TBL 0x00 0x19 0x9C 0xA0	# NMNL TBL1 CH26 ON: 27.94 OFF: 30
0940	TCS. SET_NMNL_TMP_TBL 0x00 0x1A 0x9B 0x9F	# NMNL TBL1 CH27 ON: 27.94 OFF: 30
0941	TCS. SET_NMNL_TMP_TBL 0x00 0x1B 0x99 0x9D	# NMNL TBL1 CH28 ON: 27.95 OFF: 30
0942	TCS. SET_NMNL_TMP_TBL 0x00 0x1C 0x9B 0x9F	# NMNL TBL1 CH29 ON: 27.94 OFF: 30
0943	TCS. SET_NMNL_TMP_TBL 0x00 0x1D 0x00 0x00	# NMNL TBL1 CH30 ON: -50.41 OFF: -50.41
0944	TCS. SET_NMNL_TMP_TBL 0x00 0x1E 0x00 0x00	# NMNL TBL1 CH31 ON: -50.41 OFF: -50.41
0945	TCS. SET_NMNL_TMP_TBL 0x00 0x1F 0x00 0x00	# NMNL TBL1 CH32 ON: -50.41 OFF: -50.41
0946	#	
0947	# <ヒータ ENA/DIS テーブル設定>	
0948	TCS. SET_HTR_ENA 0x05 0x00	# ヒータ CH06: BAT1-1 DIS
0949	TCS. SET_HTR_ENA 0x0C 0x00	# ヒータ CH13: BAT2-1 DIS
0950	TCS. SET_HTR_ENA 0x15 0x00	# ヒータ CH22: BAT1-2 DIS
0951	TCS. SET_HTR_ENA 0x1C 0x00	# ヒータ CH29: BAT2-2 DIS
0952	#	
0953	TCS. ONESHOT_TLM_0x11	
0954	TCS. ONESHOT_TLM_0x19	
0955	TCS. ONESHOT_TLM_0x1F	
0956	TCS. ONESHOT_TLM_0x25	
0957	#	
0958	# < CHECK >	
0959	CHECK TCS. BK_DTY_TBL1_01CH	32
0960	CHECK TCS. BK_DTY_TBL1_02CH	32
0961	CHECK TCS. BK_DTY_TBL1_03CH	32
0962	CHECK TCS. BK_DTY_TBL1_04CH	32
0963	CHECK TCS. BK_DTY_TBL1_05CH	32
0964	CHECK TCS. BK_DTY_TBL1_06CH	32
0965	CHECK TCS. BK_DTY_TBL1_07CH	32
0966	CHECK TCS. BK_DTY_TBL1_08CH	32
0967	CHECK TCS. BK_DTY_TBL1_09CH	32
0968	CHECK TCS. BK_DTY_TBL1_10CH	32
0969	CHECK TCS. BK_DTY_TBL1_11CH	32
0970	CHECK TCS. BK_DTY_TBL1_12CH	32
0971	CHECK TCS. BK_DTY_TBL1_13CH	32
0972	CHECK TCS. BK_DTY_TBL1_14CH	0
0973	CHECK TCS. BK_DTY_TBL1_15CH	0
0974	CHECK TCS. BK_DTY_TBL1_16CH	0
0975	CHECK TCS. BK_DTY_TBL1_17CH	32
0976	CHECK TCS. BK_DTY_TBL1_18CH	32
0977	CHECK TCS. BK_DTY_TBL1_19CH	32
0978	CHECK TCS. BK_DTY_TBL1_20CH	32
0979	CHECK TCS. BK_DTY_TBL1_21CH	32
0980	CHECK TCS. BK_DTY_TBL1_22CH	32
0981	CHECK TCS. BK_DTY_TBL1_23CH	32
0982	CHECK TCS. BK_DTY_TBL1_24CH	32
0983	CHECK TCS. BK_DTY_TBL1_25CH	32
0984	CHECK TCS. BK_DTY_TBL1_26CH	32
0985	CHECK TCS. BK_DTY_TBL1_27CH	32

文 書 番 号	シート No.	版 数
	36	1

0986	CHECK TCS. BK_DTY_TBL1_28CH	32
0987	CHECK TCS. BK_DTY_TBL1_29CH	32
0988	CHECK TCS. BK_DTY_TBL1_30CH	0
0989	CHECK TCS. BK_DTY_TBL1_31CH	0
0990	CHECK TCS. BK_DTY_TBL1_32CH	0
0991	CHECK TCS. NM_DTY_TBL1_01CH	64
0992	CHECK TCS. NM_DTY_TBL1_02CH	64
0993	CHECK TCS. NM_DTY_TBL1_03CH	64
0994	CHECK TCS. NM_DTY_TBL1_04CH	64
0995	CHECK TCS. NM_DTY_TBL1_05CH	64
0996	CHECK TCS. NM_DTY_TBL1_06CH	64
0997	CHECK TCS. NM_DTY_TBL1_07CH	64
0998	CHECK TCS. NM_DTY_TBL1_08CH	64
0999	CHECK TCS. NM_DTY_TBL1_09CH	64
1000	CHECK TCS. NM_DTY_TBL1_10CH	64
1001	CHECK TCS. NM_DTY_TBL1_11CH	64
1002	CHECK TCS. NM_DTY_TBL1_12CH	64
1003	CHECK TCS. NM_DTY_TBL1_13CH	64
1004	CHECK TCS. NM_DTY_TBL1_14CH	0
1005	CHECK TCS. NM_DTY_TBL1_15CH	0
1006	CHECK TCS. NM_DTY_TBL1_16CH	0
1007	CHECK TCS. NM_DTY_TBL1_17CH	64
1008	CHECK TCS. NM_DTY_TBL1_18CH	64
1009	CHECK TCS. NM_DTY_TBL1_19CH	64
1010	CHECK TCS. NM_DTY_TBL1_20CH	64
1011	CHECK TCS. NM_DTY_TBL1_21CH	64
1012	CHECK TCS. NM_DTY_TBL1_22CH	64
1013	CHECK TCS. NM_DTY_TBL1_23CH	64
1014	CHECK TCS. NM_DTY_TBL1_24CH	64
1015	CHECK TCS. NM_DTY_TBL1_25CH	64
1016	CHECK TCS. NM_DTY_TBL1_26CH	64
1017	CHECK TCS. NM_DTY_TBL1_27CH	64
1018	CHECK TCS. NM_DTY_TBL1_28CH	64
1019	CHECK TCS. NM_DTY_TBL1_29CH	64
1020	CHECK TCS. NM_DTY_TBL1_30CH	0
1021	CHECK TCS. NM_DTY_TBL1_31CH	0
1022	CHECK TCS. NM_DTY_TBL1_32CH	0
1023	CHECK TCS. NM_OFF_TBL1_01CH	29.5 30.5
1024	CHECK TCS. NM_OFF_TBL1_02CH	29.5 30.5
1025	CHECK TCS. NM_OFF_TBL1_03CH	29.5 30.5
1026	CHECK TCS. NM_OFF_TBL1_04CH	29.5 30.5
1027	CHECK TCS. NM_OFF_TBL1_05CH	29.5 30.5
1028	CHECK TCS. NM_OFF_TBL1_06CH	29.5 30.5
1029	CHECK TCS. NM_OFF_TBL1_07CH	29.5 30.5
1030	CHECK TCS. NM_OFF_TBL1_08CH	28.99 29.99
1031	CHECK TCS. NM_OFF_TBL1_09CH	29.5 30.5
1032	CHECK TCS. NM_OFF_TBL1_10CH	29.5 30.5
1033	CHECK TCS. NM_OFF_TBL1_11CH	29.72 30.72
1034	CHECK TCS. NM_OFF_TBL1_12CH	29.5 30.5
1035	CHECK TCS. NM_OFF_TBL1_13CH	29.5 30.5
1036	CHECK TCS. NM_OFF_TBL1_17CH	29.5 30.5
1037	CHECK TCS. NM_OFF_TBL1_18CH	29.5 30.5

文 書 番 号	シート No.	版 数
	37	1

1038	CHECK TCS. NM_OFF_TBL1_19CH	29.5 30.5
1039	CHECK TCS. NM_OFF_TBL1_20CH	29.5 30.5
1040	CHECK TCS. NM_OFF_TBL1_21CH	29.5 30.5
1041	CHECK TCS. NM_OFF_TBL1_22CH	29.5 30.5
1042	CHECK TCS. NM_OFF_TBL1_23CH	29.5 30.5
1043	CHECK TCS. NM_OFF_TBL1_24CH	29.5 30.5
1044	CHECK TCS. NM_OFF_TBL1_25CH	29.5 30.5
1045	CHECK TCS. NM_OFF_TBL1_26CH	29.5 30.5
1046	CHECK TCS. NM_OFF_TBL1_27CH	29.5 30.5
1047	CHECK TCS. NM_OFF_TBL1_28CH	29.5 30.5
1048	CHECK TCS. NM_OFF_TBL1_29CH	29.5 30.5
1049	CHECK TCS. NM_ON_TBL1_01CH	27.44 28.44
1050	CHECK TCS. NM_ON_TBL1_02CH	27.44 28.44
1051	CHECK TCS. NM_ON_TBL1_03CH	27.44 28.44
1052	CHECK TCS. NM_ON_TBL1_04CH	27.44 28.44
1053	CHECK TCS. NM_ON_TBL1_05CH	27.44 28.44
1054	CHECK TCS. NM_ON_TBL1_06CH	27.44 28.44
1055	CHECK TCS. NM_ON_TBL1_07CH	27.44 28.44
1056	CHECK TCS. NM_ON_TBL1_08CH	27.96 28.96
1057	CHECK TCS. NM_ON_TBL1_09CH	27.44 28.44
1058	CHECK TCS. NM_ON_TBL1_10CH	27.44 28.44
1059	CHECK TCS. NM_ON_TBL1_11CH	27.67 28.67
1060	CHECK TCS. NM_ON_TBL1_12CH	27.44 28.44
1061	CHECK TCS. NM_ON_TBL1_13CH	27.44 28.44
1062	CHECK TCS. NM_ON_TBL1_17CH	27.44 28.44
1063	CHECK TCS. NM_ON_TBL1_18CH	27.44 28.44
1064	CHECK TCS. NM_ON_TBL1_19CH	27.44 28.44
1065	CHECK TCS. NM_ON_TBL1_20CH	27.44 28.44
1066	CHECK TCS. NM_ON_TBL1_21CH	27.44 28.44
1067	CHECK TCS. NM_ON_TBL1_22CH	27.44 28.44
1068	CHECK TCS. NM_ON_TBL1_23CH	27.44 28.44
1069	CHECK TCS. NM_ON_TBL1_24CH	27.44 28.44
1070	CHECK TCS. NM_ON_TBL1_25CH	27.44 28.44
1071	CHECK TCS. NM_ON_TBL1_26CH	27.44 28.44
1072	CHECK TCS. NM_ON_TBL1_27CH	27.44 28.44
1073	CHECK TCS. NM_ON_TBL1_28CH	27.45 28.45
1074	CHECK TCS. NM_ON_TBL1_29CH	27.44 28.44
1075	CHECK TCS. HTR_ENADIS_06CH	=DIS
1076	CHECK TCS. HTR_ENADIS_13CH	=DIS
1077	CHECK TCS. HTR_ENADIS_22CH	=DIS
1078	CHECK TCS. HTR_ENADIS_29CH	=DIS
1079	. #	
1080	. TCS. SET_PRM_ENA	
1081	. #	
1082	. # < CHECK >	
1083	CHECK TCS. SET_PRM_ST	=ENA
1084	. #	
1085	. TCS. SET_PEAK_PWR 0x2328	
1086	TCS. CTRL_PKPWR_EXE	
1087	. #	
1088	. # < CHECK >	
1089	CHECK TCS. SET_POWER	90

```

1090 . #
1091 . TCS. CTRL_HTR_P1_ENA
1092 . TCS. CTRL_HTR_P2_ENA
1093 . TCS. CTRL_HTR_P3_ENA
1094 . TCS. CTRL_HTR_P4_ENA
1095 . #
1096 . # < CHECK >
1097 . CHECK TCS. HTR01_P_VOL_ST =ON
1098 . CHECK TCS. HTR09_P_VOL_ST =ON
1099 . CHECK TCS. HTR17_P_VOL_ST =ON
1100 . CHECK TCS. HTR25_P_VOL_ST =ON
1101 . #
1102 . # < TABLE 選択 >
1103 . TCS. SET_NMNL_SEL_TBL_1
1104 . TCS. SET_ATSF_SEL_TBL_1
1105 . TCS. SET_BKUP_SEL_TBL_1
1106 . #
1107 . # < CHECK >
1108 . CHECK TCS. SET_NMNL_TBL =TBL1
1109 . CHECK TCS. SET_SAFE_TBL1 =ENA
1110 . CHECK TCS. SET_SAFE_TBL2 =DIS
1111 . CHECK TCS. SET_SAFE_TBL3 =DIS
1112 . CHECK TCS. SET_SAFE_TBL4 =DIS
1113 . CHECK TCS. SET_BKUP_TBL1 =ENA
1114 . CHECK TCS. SET_BKUP_TBL2 =DIS
1115 . CHECK TCS. SET_BKUP_TBL3 =DIS
1116 . CHECK TCS. SET_BKUP_TBL4 =DIS
1117 . #
1118 . # < ピーク電力制御 ON >
1119 . TCS. CTRL_PKPWR_ON
1120 . #
1121 . # < CHECK >
1122 . CHECK TCS. PPWR_CTR =ON
1123 . #
1124 . # < ノミナル制御モード移行 >
1125 . TCS. MODE_NMNL
1126 . #
1127 . # < CHECK >
1128 . CHECK TCS. MODE_ST =NMNL
1129 . #
1130 . # QL          Check-Item          Spec          Check
1131 . # -----+-----+-----+-----+
1132 . # CMD 出力装置  Message          STATUS CHECK 結果 OK   G   N
1133 . #
1134 . # =====
1135 . # 2.7. 消費電力取得
1136 . # =====
1137 . #
1138 . # 【記録】
1139 . # 電圧/電流値の取得を実施する。
1140 . # 結果を<データシート 2.7-1>に記録すること。
1141 . #

```

文 書 番 号	シート No.	版 数
	39	1

```

1142 # *****
1143 # 3. PI 機器立ち上げ
1144 # *****
1145 # =====
1146 # 3.1. MDP 立ち上げ／初期化
1147 # =====
1148 # -----
1149 # 3.1.1. MDP 立ち上げ
1150 # -----
1151 #
1152 # MDP-DPU1,2(A系) の LINK Enable を確認
1153 #
1154 # < CHECK >
1155 CHECK DH. SPW_POTMD_L1 =ENA
1156 CHECK DH. SPW_POTMD_L2 =ENA
1157 #
1158 # MDP-DPU1,2 を電源 ON する。(HW_STATE1 は 16 秒以内にチェックすること。)
1159 #
1160 # <<< MDP-DPU1/2 Power ON >>>
1161 #
1162 # DH. MDP_DPU12_ON # DH. MC_EACH_EXE 0x099
1163 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
1164 #: 0x00 PCD. ON_OFF_ENA
1165 #: 0x01 PCD. PSU_MDP1_ON
1166 #: 0x02 PCD. PSU_MDP2_ON
1167 #: 0x03 PCD. ON_OFF_DIS
1168 #
1169 # < CHECK >
1170 CHECK MD1F. DPU_HW_STATE1 =DPU_RESET
1171 CHECK MD2F. DPU_HW_STATE1 =DPU_RESET
1172 #
1173 # < CHECK >
1174 CHECK PCD. ON_OFF_ENA_DIS =DIS
1175 CHECK PCD. PSU_MDP1_ON_OFF =ON
1176 CHECK PCD. PSU_MDP2_ON_OFF =ON
1177 CHECK DH. SPW_LNKST_L1 =RUN
1178 CHECK DH. SPW_LNKST_L2 =RUN
1179 #
1180 # *****
1181 # WAIT 16 sec (HW Initialization of DPU1 & DPU2)
1182 # *****
1183 #
1184 # <<< MDP-DPU Initial HK check (2): 16 sec after PWR-ON >>>
1185 #
1186 # < CHECK >
1187 CHECK MD1F. DPU_HW_STATE1 =DPU_RUN
1188 CHECK MD1F. DPU_HW_STATE2 =CPUOFF_DPUBUS
1189 CHECK MD1F. DMC_IF_RMAPERR =na
1190 CHECK MD1F. DMC_IF_2BITERR =na
1191 CHECK MD1F. DMC_IF_BTOERR =na
1192 CHECK MD1F. BTO_ERR =na
1193 CHECK MD1F. WDT_ERR =na

```

文 書 番 号	シート No.	版 数
	40	1

```

1194 CHECK MD1F.DPU_SELECT =DPU1
1195 CHECK MD2F.DPU_HW_STATE1 =DPU_RUN
1196 CHECK MD2F.DPU_HW_STATE2 =CPUOFF_DPUBUS
1197 CHECK MD2F.DMC_IF_RMAPERR =na
1198 CHECK MD2F.DMC_IF_2BITERR =na
1199 CHECK MD2F.DMC_IF_BTOERR =na
1200 CHECK MD2F.BTO_ERR =na
1201 CHECK MD2F.WDT_ERR =na
1202 CHECK MD2F.DPU_SELECT =DPU2
1203 #
1204 . # <<< MDP-DPU CPU_ON / RUN >>>
1205 . #
1206 . DH.MDP_DPU12_CPU_RUN # DH.MC_EACH_EXE 0x09A
1207 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
1208 #: 0x00 DH.ATMC_EACH_ENA 0x34
1209 #: 0x01 DH.SPW_RTEFDIR_SET 0x1E
1210 #: 0x05 DH.ATMC_EACH_ENA 0x36
1211 #: 0x06 MD1F.CPU_ON
1212 #: 0x07 MD2F.CPU_ON
1213 #: 0x08 MD1F.CPU_RUN
1214 #: 0x09 MD2F.CPU_RUN
1215 #: 0x0A DH.ATMC_EACH_ENA 0x30
1216 #: 0x0F DH.ATMC_EACH_ENA 0x31
1217 #
1218 CHECK DH.CMD_EXE_CNT -updatewait 30
1219 WAIT_SEC 5
1220 # < CHECK >
1221 CHECK DH.SPW_FDIR_ENA_MDP1 =ENA
1222 CHECK DH.SPW_FDIR_ENA_MDP2 =ENA
1223 CHECK DH.SPW_FDIR_ENA_PCD =ENA
1224 CHECK DH.SPW_FDIR_ENA_RMTRTR =ENA
1225 CHECK DH.ATMC_ENA_DIS30 =ENA
1226 CHECK DH.ATMC_ENA_DIS31 =ENA
1227 CHECK DH.ATMC_ENA_DIS34 =ENA
1228 CHECK DH.ATMC_ENA_DIS36 =ENA
1229 CHECK MD1F.DPU_HW_STATE2 =CPU_RUN
1230 CHECK MD1F.DPU_CMD_CODE 0x0701
1231 CHECK MD2F.DPU_HW_STATE2 =CPU_RUN
1232 CHECK MD2F.DPU_CMD_CODE 0x0701
1233 #
1234 #
1235 # < MDP1 SATELITE TIME CHECK (0x200/s) >
1236 #
1237 CHECK MD1F.SAT_TIME -updatewait 180
1238 LET LocalA0 = {MD1F.SAT_TIME} & 0xFFFFFFFF
1239 CHECK MD1F.SAT_TIME -updatewait 4
1240 LET LocalA1 = {MD1F.SAT_TIME} & 0xFFFFFFFF
1241 LET LocalA1 = LocalA1 - LocalA0
1242 SYSTEM test {LocalA1} = 512
1243 #
1244 # < MDP1 SUNPULSE TIME CHECK (0x800-0xB00/4s) >
1245 #

```

文 書 番 号	シート No.	版 数
	41	1

```

1246 CHECK MD1F.SUNPULSE_TIME -updatewait 8
1247 LET LocalA0 = {MD1F.SUNPULSE_TIME} & 0xFFFFFFFF
1248 CHECK MD1F.SUNPULSE_TIME -updatewait 8
1249 LET LocalA1 = {MD1F.SUNPULSE_TIME} & 0xFFFFFFFF
1250 LET LocalA1 = LocalA1 - LocalA0
1251 SYSTEM test {LocalA1} -ge 2048 -a {LocalA1} -le 2816
1252 #
1253 # < MDP2 SATELITE TIME CHECK (0x200/s) >
1254 #
1255 CHECK MD2F.SAT_TIME -updatewait 8
1256 LET LocalA0 = {MD2F.SAT_TIME} & 0xFFFFFFFF
1257 CHECK MD2F.SAT_TIME -updatewait 4
1258 LET LocalA1 = {MD2F.SAT_TIME} & 0xFFFFFFFF
1259 LET LocalA1 = LocalA1 - LocalA0
1260 SYSTEM test {LocalA1} = 512
1261 #
1262 # < MDP2 SUNPULSE TIME CHECK (0x800-0xB00/4s) >
1263 #
1264 CHECK MD2F.SUNPULSE_TIME -updatewait 8
1265 LET LocalA0 = {MD2F.SUNPULSE_TIME} & 0xFFFFFFFF
1266 CHECK MD2F.SUNPULSE_TIME -updatewait 8
1267 LET LocalA1 = {MD2F.SUNPULSE_TIME} & 0xFFFFFFFF
1268 LET LocalA1 = LocalA1 - LocalA0
1269 SYSTEM test {LocalA1} -ge 2048 -a {LocalA1} -le 2816
1270 #
1271 # < CHECK >
1272 CHECK MD1F.DPU_HW_STATE1 =DPU_RUN
1273 CHECK MD1F.DPU_HW_STATE2 =CPU_RUN
1274 CHECK MD1F.DMC_IF_RMAPERR =na
1275 CHECK MD1F.DMC_IF_2BITERR =na
1276 CHECK MD1F.DMC_IF_BTERR =na
1277 CHECK MD1F.BTO_ERR =na
1278 CHECK MD1F.WDT_ERR =na
1279 CHECK MD1F.DPU_SELECT =DPU1
1280 CHECK MD1F.DPU_SW_STATE 2
1281 CHECK MD1F.SW_CMD_CODE 0
1282 CHECK MD1F.SW_CMD_CNT 0
1283 CHECK MD1F.SW_CMD_REJ_CNT 0
1284 CHECK MD1F.SW_CMD_REJ_CODE 0
1285 CHECK MD1F.MS_CMD_PRIO =DMC
1286 CHECK MD1F.RDN_MODE =OFF
1287 CHECK MD1F.SUNPULSE_MODE =NML
1288 CHECK MD1F.LOAD_DUMP_DIS =DIS
1289 CHECK MD1F.MEMLOAD_BUSY =OFF
1290 CHECK MD1F.MEMLOAD_NODE 0
1291 CHECK MD1F.MEMDUMP_BUSY =OFF
1292 CHECK MD1F.MEMDUMP_NODE 0
1293 CHECK MD1F.TASK_STAT_AP01 =OFF
1294 CHECK MD1F.TASK_STAT_AP02 =OFF
1295 CHECK MD1F.TASK_STAT_AP03 =OFF
1296 CHECK MD1F.TASK_STAT_AP04 =OFF
1297 CHECK MD1F.TASK_STAT_AP05 =OFF

```

文 書 番 号	シート No.	版 数
	42	1

1298	CHECK MD1F. TASK_STAT_AP06	=OFF
1299	CHECK MD1F. TASK_STAT_AP07	=OFF
1300	CHECK MD1F. TASK_STAT_AP08	=OFF
1301	CHECK MD1F. TASK_STAT_AP09	=OFF
1302	CHECK MD1F. TASK_STAT_AP10	=OFF
1303	CHECK MD1F. TASK_STAT_AP11	=OFF
1304	CHECK MD1F. TASK_STAT_AP12	=OFF
1305	CHECK MD1F. TASK_STAT_AP13	=OFF
1306	CHECK MD1F. TASK_STAT_AP14	=OFF
1307	CHECK MD1F. TASK_STAT_AP15	=OFF
1308	CHECK MD1F. TASK_STAT_AP16	=OFF
1309	CHECK MD1F. UNEXPECT_CNT	0
1310	CHECK MD1F. UNEXPECT_CODE	0
1311	CHECK MD1F. DMC_IF_ERR	=na
1312	CHECK MD1F. TIME_ERR	=na
1313	CHECK MD1F. INST_CACHE_ERR	=na
1314	CHECK MD1F. DATA_CACHE_ERR	=na
1315	CHECK MD1F. SRAM_1BERR	=na
1316	CHECK MD1F. CMD_BUFF_1BERR	=na
1317	CHECK MD1F. CMD_BUFF_2BERR	=na
1318	CHECK MD1F. TLM_BUFF_1BERR	=na
1319	CHECK MD1F. TLM_BUFF_2BERR	=na
1320	CHECK MD1F. COMP_1BERR	=na
1321	CHECK MD1F. DS_1BERR	=na
1322	CHECK MD1F. DS_2BERR	=na
1323	CHECK MD1F. RDN_LINK_STAT	=ON
1324	CHECK MD1F. RIF_LINK_ERR	=na
1325	CHECK MD1F. RIF_ERR	=na
1326	CHECK MD1F. MS0_LINK_STAT	=OFF
1327	CHECK MD1F. MS1_LINK_STAT	=OFF
1328	CHECK MD1F. MS2_LINK_STAT	=OFF
1329	CHECK MD1F. MS3_LINK_STAT	=OFF
1330	CHECK MD1F. MS4_LINK_STAT	=OFF
1331	CHECK MD1F. MS5_LINK_STAT	=OFF
1332	CHECK MD1F. MS6_LINK_STAT	=OFF
1333	CHECK MD1F. MS7_LINK_STAT	=OFF
1334	CHECK MD1F. MS0_DAT_CLCT	=OFF
1335	CHECK MD1F. MS1_DAT_CLCT	=OFF
1336	CHECK MD1F. MS2_DAT_CLCT	=OFF
1337	CHECK MD1F. MS3_DAT_CLCT	=OFF
1338	CHECK MD1F. MS4_DAT_CLCT	=OFF
1339	CHECK MD1F. MS5_DAT_CLCT	=OFF
1340	CHECK MD1F. MS6_DAT_CLCT	=OFF
1341	CHECK MD1F. MS7_DAT_CLCT	=OFF
1342	CHECK MD1F. MS0_BUFF_SEL	=L
1343	CHECK MD1F. MS1_BUFF_SEL	=L
1344	CHECK MD1F. MS2_BUFF_SEL	=L
1345	CHECK MD1F. MS3_BUFF_SEL	=L
1346	CHECK MD1F. MS4_BUFF_SEL	=L
1347	CHECK MD1F. MS5_BUFF_SEL	=L
1348	CHECK MD1F. MS6_BUFF_SEL	=L
1349	CHECK MD1F. MS7_BUFF_SEL	=L

文 書 番 号	シート No.	版 数
	43	1

1350	CHECK MD1F. MS0_SGL_CMD_ERR	=na
1351	CHECK MD1F. MS1_SGL_CMD_ERR	=na
1352	CHECK MD1F. MS2_SGL_CMD_ERR	=na
1353	CHECK MD1F. MS3_SGL_CMD_ERR	=na
1354	CHECK MD1F. MS4_SGL_CMD_ERR	=na
1355	CHECK MD1F. MS5_SGL_CMD_ERR	=na
1356	CHECK MD1F. MS6_SGL_CMD_ERR	=na
1357	CHECK MD1F. MS7_SGL_CMD_ERR	=na
1358	CHECK MD1F. MS0_MTI_CMD_ERR	=na
1359	CHECK MD1F. MS1_MTI_CMD_ERR	=na
1360	CHECK MD1F. MS2_MTI_CMD_ERR	=na
1361	CHECK MD1F. MS3_MTI_CMD_ERR	=na
1362	CHECK MD1F. MS4_MTI_CMD_ERR	=na
1363	CHECK MD1F. MS5_MTI_CMD_ERR	=na
1364	CHECK MD1F. MS6_MTI_CMD_ERR	=na
1365	CHECK MD1F. MS7_MTI_CMD_ERR	=na
1366	CHECK MD2F. DPU_HW_STATE1	=DPU_RUN
1367	CHECK MD2F. DPU_HW_STATE2	=CPU_RUN
1368	CHECK MD2F. DMC_IF_RMAPERR	=na
1369	CHECK MD2F. DMC_IF_2BITERR	=na
1370	CHECK MD2F. DMC_IF_BTERR	=na
1371	CHECK MD2F. BTO_ERR	=na
1372	CHECK MD2F. WDT_ERR	=na
1373	CHECK MD2F. DPU_SELECT	=DPU2
1374	CHECK MD2F. DPU_SW_STATE	2
1375	CHECK MD2F. SW_CMD_CODE	0
1376	CHECK MD2F. SW_CMD_CNT	0
1377	CHECK MD2F. SW_CMD_REJ_CNT	0
1378	CHECK MD2F. SW_CMD_REJ_CODE	0
1379	CHECK MD2F. MS_CMD_PRIO	=DMC
1380	CHECK MD2F. RDN_MODE	=OFF
1381	CHECK MD2F. SUNPULSE_MODE	=NML
1382	CHECK MD2F. LOAD_DUMP_DIS	=DIS
1383	CHECK MD2F. MEMLOAD_BUSY	=OFF
1384	CHECK MD2F. MEMLOAD_NODE	0
1385	CHECK MD2F. MEMDUMP_BUSY	=OFF
1386	CHECK MD2F. MEMDUMP_NODE	0
1387	CHECK MD2F. TASK_STAT_AP01	=OFF
1388	CHECK MD2F. TASK_STAT_AP02	=OFF
1389	CHECK MD2F. TASK_STAT_AP03	=OFF
1390	CHECK MD2F. TASK_STAT_AP04	=OFF
1391	CHECK MD2F. TASK_STAT_AP05	=OFF
1392	CHECK MD2F. TASK_STAT_AP06	=OFF
1393	CHECK MD2F. TASK_STAT_AP07	=OFF
1394	CHECK MD2F. TASK_STAT_AP08	=OFF
1395	CHECK MD2F. TASK_STAT_AP09	=OFF
1396	CHECK MD2F. TASK_STAT_AP10	=OFF
1397	CHECK MD2F. TASK_STAT_AP11	=OFF
1398	CHECK MD2F. TASK_STAT_AP12	=OFF
1399	CHECK MD2F. TASK_STAT_AP13	=OFF
1400	CHECK MD2F. TASK_STAT_AP14	=OFF
1401	CHECK MD2F. TASK_STAT_AP15	=OFF

文 書 番 号	シート No.	版 数
	44	1

1402	CHECK MD2F. TASK_STAT_AP16	=OFF
1403	CHECK MD2F. UNEXPECT_CNT	0
1404	CHECK MD2F. UNEXPECT_CODE	0
1405	CHECK MD2F. DMC_IF_ERR	=na
1406	CHECK MD2F. TIME_ERR	=na
1407	CHECK MD2F. INST_CACHE_ERR	=na
1408	CHECK MD2F. DATA_CACHE_ERR	=na
1409	CHECK MD2F. SRAM_1BERR	=na
1410	CHECK MD2F. CMD_BUFF_1BERR	=na
1411	CHECK MD2F. CMD_BUFF_2BERR	=na
1412	CHECK MD2F. TLM_BUFF_1BERR	=na
1413	CHECK MD2F. TLM_BUFF_2BERR	=na
1414	CHECK MD2F. COMP_1BERR	=na
1415	CHECK MD2F. DS_1BERR	=na
1416	CHECK MD2F. DS_2BERR	=na
1417	CHECK MD2F. RDN_LINK_STAT	=ON
1418	CHECK MD2F. RIF_LINK_ERR	=na
1419	CHECK MD2F. RIF_ERR	=na
1420	CHECK MD2F. MS0_LINK_STAT	=OFF
1421	CHECK MD2F. MS1_LINK_STAT	=OFF
1422	CHECK MD2F. MS2_LINK_STAT	=OFF
1423	CHECK MD2F. MS3_LINK_STAT	=OFF
1424	CHECK MD2F. MS4_LINK_STAT	=OFF
1425	CHECK MD2F. MS5_LINK_STAT	=OFF
1426	CHECK MD2F. MS6_LINK_STAT	=OFF
1427	CHECK MD2F. MS7_LINK_STAT	=OFF
1428	CHECK MD2F. MS0_DAT_CLCT	=OFF
1429	CHECK MD2F. MS1_DAT_CLCT	=OFF
1430	CHECK MD2F. MS2_DAT_CLCT	=OFF
1431	CHECK MD2F. MS3_DAT_CLCT	=OFF
1432	CHECK MD2F. MS4_DAT_CLCT	=OFF
1433	CHECK MD2F. MS5_DAT_CLCT	=OFF
1434	CHECK MD2F. MS6_DAT_CLCT	=OFF
1435	CHECK MD2F. MS7_DAT_CLCT	=OFF
1436	CHECK MD2F. MS0_BUFF_SEL	=L
1437	CHECK MD2F. MS1_BUFF_SEL	=L
1438	CHECK MD2F. MS2_BUFF_SEL	=L
1439	CHECK MD2F. MS3_BUFF_SEL	=L
1440	CHECK MD2F. MS4_BUFF_SEL	=L
1441	CHECK MD2F. MS5_BUFF_SEL	=L
1442	CHECK MD2F. MS6_BUFF_SEL	=L
1443	CHECK MD2F. MS7_BUFF_SEL	=L
1444	CHECK MD2F. MS0_SGL_CMD_ERR	=na
1445	CHECK MD2F. MS1_SGL_CMD_ERR	=na
1446	CHECK MD2F. MS2_SGL_CMD_ERR	=na
1447	CHECK MD2F. MS3_SGL_CMD_ERR	=na
1448	CHECK MD2F. MS4_SGL_CMD_ERR	=na
1449	CHECK MD2F. MS5_SGL_CMD_ERR	=na
1450	CHECK MD2F. MS6_SGL_CMD_ERR	=na
1451	CHECK MD2F. MS7_SGL_CMD_ERR	=na
1452	CHECK MD2F. MS0_MTI_CMD_ERR	=na
1453	CHECK MD2F. MS1_MTI_CMD_ERR	=na

文 書 番 号	シート No.	版 数
	45	1

1454	CHECK MD2F.MS2_MTI_CMD_ERR	=na	
1455	CHECK MD2F.MS3_MTI_CMD_ERR	=na	
1456	CHECK MD2F.MS4_MTI_CMD_ERR	=na	
1457	CHECK MD2F.MS5_MTI_CMD_ERR	=na	
1458	CHECK MD2F.MS6_MTI_CMD_ERR	=na	
1459	CHECK MD2F.MS7_MTI_CMD_ERR	=na	
1460	#		
1461	# QL	Check-Item	Spec
1462	#		Check
1463	# CMD 出力装置	Message	STATUS CHECK 結果 OK G N
1464	#		
1465	#		
1466	# 3.1.2. MDP チェックアウト (MDP ハード健全性確認)		
1467	#		
1468	#		
1469	# << No.1 DMC-MDP 間 SpW Channel 切替確認 >>		
1470	#		
1471	# <<< SpW A 系→B 系切替 >>>		
1472	#		
1473	# MDP-DPU1、MDP-DPU2 のルーティングテーブルを A 系から B 系に切替		
1474	. DH.SPW_ROUTE_RDN 0x22 0x01		
1475	. DH.SPW_ROUTE_RDN 0x23 0x01		
1476	#		
1477	# < CHECK >		
1478	CHECK DH.SPW_LCLRTRTBL_MDP1	=RED1	
1479	CHECK DH.SPW_LCLRTRTBL_MDP2	=RED1	
1480	CHECK DH.SPW_RMTRTRTBL_MDP1	=OK	
1481	CHECK DH.SPW_RMTRTRTBL_MDP2	=OK	
1482	CHECK DH.SPW_POTMD_R1	=ENA	
1483	CHECK DH.SPW_POTMD_R2	=ENA	
1484	CHECK MD1F.WDT_ERR	=na	
1485	CHECK MD1F.DMC_IF_LINK_CH	=B	
1486	CHECK MD1F.DPU_SW_STATE	2	
1487	CHECK MD2F.WDT_ERR	=na	
1488	CHECK MD2F.DMC_IF_LINK_CH	=B	
1489	CHECK MD2F.DPU_SW_STATE	2	
1490	#		
1491	# <<< SpW B 系→A 系切替 >>>		
1492	# MDP-DPU1、MDP-DPU2 のルーティングテーブルを B 系から A 系に切替		
1493	#		
1494	. DH.SPW_ROUTE_RDN 0x22 0x00		
1495	. DH.SPW_ROUTE_RDN 0x23 0x00		
1496	#		
1497	# < CHECK >		
1498	CHECK DH.SPW_LCLRTRTBL_MDP1	=MAIN	
1499	CHECK DH.SPW_LCLRTRTBL_MDP2	=MAIN	
1500	CHECK DH.SPW_RMTRTRTBL_MDP1	=OK	
1501	CHECK DH.SPW_RMTRTRTBL_MDP2	=OK	
1502	CHECK DH.SPW_POTMD_L1	=ENA	
1503	CHECK DH.SPW_POTMD_L2	=ENA	
1504	CHECK MD1F.WDT_ERR	=na	
1505	CHECK MD1F.DMC_IF_LINK_CH	=A	

文 書 番 号	シート No.	版 数
	46	1

```

1506 CHECK MD1F.DPU_SW_STATE 2
1507 CHECK MD2F.WDT_ERR =na
1508 CHECK MD2F.DMC_IF_LINK_CH =A
1509 CHECK MD2F.DPU_SW_STATE 2
1510 #
1511 . # << No.2 EEPROM SUM Check、他 DPU の SDRAM Check >>
1512 #
1513 . # Operation mode change ( Real 32kbps)
1514 . # RATE CHK CATE DHFS
1515 . DH.OPE_MODE 0x0D 0x4F 0x2D 0x00 0x0000000000000000000000000000
1516 #
1517 . # < CHECK >
1518 LET MD1_SW_CNT = ({MD1F.SW_CMD_CNT} + 1 ) & 0xFF
1519 LET MD1_REJ_CNT = ({MD1F.SW_CMD_REJ_CNT} & 0xFF)
1520 #
1521 # <(CHK1) MDP-DPU1 CHECKOUT EEPRM and SDRAM Check >
1522 # <<< RDN_FUNC_ON >>>
1523 . #
1524 . MD1F.RDN_FUNC_ON
1525 #
1526 . # < CHECK >
1527 SYSTEM test {MD1F.SW_CMD_CNT} = {MD1_SW_CNT}
1528 SYSTEM test {MD1F.SW_CMD_REJ_CNT} = {MD1_REJ_CNT}
1529 CHECK MD1F.RDN_MODE =ON
1530 WAIT_SEC 8
1531 LET MD1_SW_CNT = ({MD1F.SW_CMD_CNT} + 1 ) & 0xFF
1532 LET MD1_REJ_CNT = ({MD1F.SW_CMD_REJ_CNT} & 0xFF)
1533 #
1534 . # <<< APP16_START >>>
1535 . MD1F.APP16_START
1536 #
1537 . # < CHECK >
1538 SYSTEM test {MD1F.SW_CMD_CNT} = {MD1_SW_CNT}
1539 SYSTEM test {MD1F.SW_CMD_REJ_CNT} = {MD1_REJ_CNT}
1540 CHECK MD1F.TASK_STAT_AP16 =ON
1541 WAIT_SEC 2
1542 LET MD1_SW_CNT = ({MD1F.SW_CMD_CNT} + 1 ) & 0xFF
1543 LET MD1_REJ_CNT = ({MD1F.SW_CMD_REJ_CNT} & 0xFF)
1544 #
1545 . # 16 進ダンプ画面の準備 (APID 0x619 Category 0x29 の確認)
1546 #
1547 . # <<< CHKOUT_START >>>
1548 . MD1U.CHKOUT_START 0x000A # PLAN_COUNT( =1+8+1 )
1549 & # [WRT_VAL] [START_ADDR] [END_ADDR ] [SUM_VAL ]
1550 & 0x03 0x1FC00000 0x1FC007FF 0x00042C77 # EEPROM01
1551 & 0x04 0xA5 0x08FFF800 0x090007FF 0x000A5000 # RDNSDRAM01
1552 & 0x04 0xA5 0x0AFF800 0x0B0007FF 0x0005A000 # RDNSDRAM02
1553 & 0x04 0xA5 0x0CFFF800 0x0D0007FF 0x000A5000 # RDNSDRAM03
1554 & 0x04 0xA5 0x0EFF800 0x0F0007FF 0x0005A000 # RDNSDRAM04
1555 & 0x04 0xA5 0x10FFF800 0x110007FF 0x000A5000 # RDNSDRAM05
1556 & 0x04 0xA5 0x12FFF800 0x130007FF 0x0005A000 # RDNSDRAM06
1557 & 0x04 0xA5 0x14FFF800 0x150007FF 0x000A5000 # RDNSDRAM07

```

文 書 番 号	シート No.	版 数
	47	1

```

1558 &          0x04 0x5A 0x16FFF800 0x170007FF 0x0005A000 # RDNSDRAM08
1559 &          0x04 0x00 0x16FFF800 0x170007FF 0x00000000 # RDNSDRAM09
1560 &          0x04 0x00 0x00000000 0x00000000 0x00000000 # RDNSDRAM10
1561 &          0x04 0x00 0x00000000 0x00000000 0x00000000 # RDNSDRAM11
1562 &          0x04 0x00 0x00000000 0x00000000 0x00000000 # RDNSDRAM12
1563 &          0x04 0x00 0x00000000 0x00000000 0x00000000 # RDNSDRAM13
1564 &          0x04 0x00 0x00000000 0x00000000 0x00000000 # RDNSDRAM14
1565 #
1566 . # < CHECK >
1567 SYSTEM test {MD1F.SW_CMD_CNT} = {MD1_SW_CNT}
1568 SYSTEM test {MD1F.SW_CMD_REJ_CNT} = {MD1_REJ_CNT}
1569 WAIT_SEC 2
1570 LET MD1_SW_CNT = ({MD1F.SW_CMD_CNT} + 1 ) & 0xFF
1571 LET MD1_REJ_CNT = ({MD1F.SW_CMD_REJ_CNT} & 0xFF)
1572 #
1573 . # <<< CHKOUT_FUNCS_RESULT TLM 確認 >>>
1574 . # CHKOUT QL 画面立上げ (APID 0x619, Category 0x29 のパケット確認)
1575 #
1576 . # < CHECK >
1577 CHECK MD1U.ERROR_COUNT 0
1578 CHECK MD1U.PLAN_COUNT 10 # QL でチェック可能
1579 . # <<< CheckOut アプリ終了 >>>
1580 . #
1581 . MD1U.CHKOUT_EXIT
1582 #
1583 . # < CHECK >
1584 SYSTEM test {MD1F.SW_CMD_CNT} = {MD1_SW_CNT}
1585 SYSTEM test {MD1F.SW_CMD_REJ_CNT} = {MD1_REJ_CNT}
1586 CHECK MD1F.TASK_STAT_AP16 =OFF
1587 CHECK MD1F.RIF_LINK_ERR =na
1588 CHECK MD1F.RIF_ERR =na
1589 WAIT_SEC 2
1590 LET MD1_SW_CNT = ({MD1F.SW_CMD_CNT} + 1 ) & 0xFF
1591 LET MD1_REJ_CNT = ({MD1F.SW_CMD_REJ_CNT} & 0xFF)
1592 #
1593 . # <<< 冗長モード OFF >>>
1594 . #
1595 . MD1F.RDN_FUNC_OFF
1596 . #
1597 . # < CHECK >
1598 SYSTEM test {MD1F.SW_CMD_CNT} = {MD1_SW_CNT}
1599 SYSTEM test {MD1F.SW_CMD_REJ_CNT} = {MD1_REJ_CNT}
1600 CHECK MD1F.RDN_MODE =OFF
1601 WAIT_SEC 2
1602 LET MD2_SW_CNT = ({MD2F.SW_CMD_CNT} + 1 ) & 0xFF
1603 LET MD2_REJ_CNT = ({MD2F.SW_CMD_REJ_CNT} & 0xFF)
1604 #
1605 . # <(CHK2) MDP-DPU2 CHECKOUT EEPRM and SDRAM Check >
1606 . # <<< RDN_FUNC_ON >>>
1607 . #
1608 . MD2F.RDN_FUNC_ON
1609 #

```

```

1610 . # < CHECK >
1611 SYSTEM test {MD2F.SW_CMD_CNT} = {MD2_SW_CNT}
1612 SYSTEM test {MD2F.SW_CMD_REJ_CNT} = {MD2_REJ_CNT}
1613 CHECK MD2F.RDN_MODE =ON
1614 WAIT_SEC 2
1615 LET MD2_SW_CNT = ({MD2F.SW_CMD_CNT} + 1 ) & 0xFF
1616 LET MD2_REJ_CNT = ({MD2F.SW_CMD_REJ_CNT} & 0xFF)
1617 #
1618 . # <<< APP16_START >>>
1619 . #
1620 . MD2F.APP16_START
1621 #
1622 . # < CHECK >
1623 SYSTEM test {MD2F.SW_CMD_CNT} = {MD2_SW_CNT}
1624 SYSTEM test {MD2F.SW_CMD_REJ_CNT} = {MD2_REJ_CNT}
1625 CHECK MD2F.TASK_STAT_AP16 =ON
1626 WAIT_SEC 2
1627 LET MD2_SW_CNT = ({MD2F.SW_CMD_CNT} + 1 ) & 0xFF
1628 LET MD2_REJ_CNT = ({MD2F.SW_CMD_REJ_CNT} & 0xFF)
1629 #
1630 . # 16 進ダンプ画面の準備 (APID 0x621 Category 0x29 の確認)
1631 #
1632 . # <<< CHKOUT_START >>>
1633 . MD2U.CHKOUT_START 0x000A # PLAN_COUNT( =1+8+1 )
1634 & # [WRT_VAL] [START_ADDR] [END_ADDR ] [SUM_VAL ]
1635 & 0x04 0x1FC00000 0x1FC007FF 0x00042C77 # EEPROM01
1636 & 0x03 0xA5 0x08FFF800 0x090007FF 0x000A5000 # RDNSDRAM01
1637 & 0x03 0x5A 0x0AFF800 0x0B0007FF 0x0005A000 # RDNSDRAM02
1638 & 0x03 0xA5 0x0CFFF800 0x0D0007FF 0x000A5000 # RDNSDRAM03
1639 & 0x03 0x5A 0x0EFF800 0x0F0007FF 0x0005A000 # RDNSDRAM04
1640 & 0x03 0xA5 0x10FFF800 0x110007FF 0x000A5000 # RDNSDRAM05
1641 & 0x03 0x5A 0x12FFF800 0x130007FF 0x0005A000 # RDNSDRAM06
1642 & 0x03 0xA5 0x14FFF800 0x150007FF 0x000A5000 # RDNSDRAM07
1643 & 0x03 0x5A 0x16FFF800 0x170007FF 0x0005A000 # RDNSDRAM08
1644 & 0x03 0x00 0x16FFF800 0x170007FF 0x00000000 # RDNSDRAM09
1645 & 0x03 0x00 0x00000000 0x00000000 0x00000000 # RDNSDRAM10
1646 & 0x03 0x00 0x00000000 0x00000000 0x00000000 # RDNSDRAM11
1647 & 0x03 0x00 0x00000000 0x00000000 0x00000000 # RDNSDRAM12
1648 & 0x03 0x00 0x00000000 0x00000000 0x00000000 # RDNSDRAM13
1649 & 0x03 0x00 0x00000000 0x00000000 0x00000000 # RDNSDRAM14
1650 #
1651 . # < CHECK >
1652 SYSTEM test {MD2F.SW_CMD_CNT} = {MD2_SW_CNT}
1653 SYSTEM test {MD2F.SW_CMD_REJ_CNT} = {MD2_REJ_CNT}
1654 WAIT_SEC 2
1655 LET MD2_SW_CNT = ({MD2F.SW_CMD_CNT} + 1 ) & 0xFF
1656 LET MD2_REJ_CNT = ({MD2F.SW_CMD_REJ_CNT} & 0xFF)
1657 #
1658 . # <<< CHKOUT_FUNCS_RESULT TLM 確認 >>>
1659 . # CHKOUT QL 画面立上げ (APID 0x621, Category 0x29 のパケット確認)
1660 #
1661 . # < CHECK >

```

文 書 番 号	シート No.	版 数
	49	1

```

1662 CHECK MD2U.ERROR_COUNT 0
1663 CHECK MD2U.PLAN_COUNT 10 # QL でチェック可能
1664 . # <<< CheckOut アプリ終了 >>>
1665 . #
1666 . MD2U.CHKOUT_EXIT
1667 . #
1668 . # < CHECK >
1669 SYSTEM test {MD2F.SW_CMD_CNT} = {MD2_SW_CNT}
1670 SYSTEM test {MD2F.SW_CMD_REJ_CNT} = {MD2_REJ_CNT}
1671 CHECK MD2F.TASK_STAT_AP16 =OFF
1672 CHECK MD2F.RIF_LINK_ERR =na
1673 CHECK MD2F.RIF_ERR =na
1674 WAIT_SEC 2
1675 LET MD2_SW_CNT = ({MD2F.SW_CMD_CNT} + 1 ) & 0xFF
1676 LET MD2_REJ_CNT = ({MD2F.SW_CMD_REJ_CNT} & 0xFF)
1677 . #
1678 . # <<< 冗長モード OFF >>>
1679 . #
1680 . MD2F.RDN_FUNC_OFF
1681 . # < CHECK >
1682 . #
1683 SYSTEM test {MD2F.SW_CMD_CNT} = {MD2_SW_CNT}
1684 SYSTEM test {MD2F.SW_CMD_REJ_CNT} = {MD2_REJ_CNT}
1685 CHECK MD2F.RDN_MODE =OFF
1686 . #
1687 . # Telemetry Operation Mode 設定
1688 . # (Mission User HK Packet をリアルタイムに出力するように設定。
1689 . # リアルタイムは System HK 8 秒周期, DH, DR, Mission User HK 出力,
1690 . # 32kbps)
1691 . # DHFS
1692 . DH.OPE_MODE_32KBPS_CAT0x26 0x03 0x000000000000000000000000
1693 . #
1694 . # < CHECK >
1695 CHECK DH.CATEGORY_TBL_NO 0x26
1696 CHECK DH.DWNLNK_RATE =BPS32768
1697 WAIT_SEC 2
1698 LET MD1_SW_CNT = ({MD1F.SW_CMD_CNT} + 5 ) & 0xFF
1699 LET MD2_SW_CNT = ({MD2F.SW_CMD_CNT} + 5 ) & 0xFF
1700 LET MD1_REJ_CNT = ({MD1F.SW_CMD_REJ_CNT} & 0xFF)
1701 LET MD2_REJ_CNT = ({MD2F.SW_CMD_REJ_CNT} & 0xFF)
1702 . #
1703 . DH.MDP_DPU12_APL_START # DH.MC_EACH_EXE 0x09B
1704 . # << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
1705 . # 0x00 DH.ATMC_EACH_ENA 0x32
1706 . # 0x02 MD1F.APP01_START
1707 . # 0x03 MD1F.APP02_START
1708 . # 0x04 MD1F.APP03_START
1709 . # 0x05 MD1F.APP04_START
1710 . # 0x06 MD1F.APP05_START
1711 . # 0x07 DH.ATMC_EACH_ENA 0x33
1712 . # 0x08 MD2F.APP01_START
1713 . # 0x09 MD2F.APP02_START

```

文 書 番 号	シート No.	版 数
	50	1

```

1714 #: 0x0A MD2F. APP13_START
1715 #: 0x0B MD2F. APP14_START
1716 #: 0x0C MD2F. APP05_START
1717 #
1718 # < CHECK >
1719 CHECK DH. ATMC_ENA_DIS32 =ENA
1720 CHECK DH. ATMC_ENA_DIS33 =ENA
1721 #
1722 # < CHECK >
1723 SYSTEM test {MD1F. SW_CMD_CNT} = {MD1_SW_CNT}
1724 SYSTEM test {MD2F. SW_CMD_CNT} = {MD2_SW_CNT}
1725 SYSTEM test {MD1F. SW_CMD_REJ_CNT} = {MD1_REJ_CNT}
1726 SYSTEM test {MD2F. SW_CMD_REJ_CNT} = {MD2_REJ_CNT}
1727 #
1728 # < CHECK >
1729 CHECK MD1F. TASK_STAT_AP01 =ON
1730 CHECK MD1F. TASK_STAT_AP02 =ON
1731 CHECK MD1F. TASK_STAT_AP03 =ON
1732 CHECK MD1F. TASK_STAT_AP04 =ON
1733 CHECK MD1F. TASK_STAT_AP05 =ON
1734 CHECK MD1F. TASK_STAT_AP06 =OFF
1735 CHECK MD2F. TASK_STAT_AP01 =ON
1736 CHECK MD2F. TASK_STAT_AP02 =ON
1737 CHECK MD2F. TASK_STAT_AP13 =ON
1738 CHECK MD2F. TASK_STAT_AP14 =ON
1739 CHECK MD2F. TASK_STAT_AP05 =ON
1740 CHECK MD2F. TASK_STAT_AP06 =OFF
1741 #
1742 # < MDP USER-TASK: Initial Sequence - MDP-DPU-INITSET>
1743 #
1744 # < CHECK >
1745 LET MD1_GET = ({MD1U. U_CmdDmc_GetCNT} + 5 ) & 0x1F
1746 LET MD2_GET = ({MD2U. U_CmdDmc_GetCNT} + 5 ) & 0x1F
1747 LET MD1_EXE = ({MD1U. U_CmdDmc_ExecCNT} + 5 ) & 0x1F
1748 LET MD2_EXE = ({MD2U. U_CmdDmc_ExecCNT} + 5 ) & 0x1F
1749 #
1750 DH. MDP_U_DPU12_INIT_SET # DH. MC_EACH_EXE 0x080
1751 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
1752 #: 0x00 MD1U. HK_SET 0xFF 0x08
1753 #: 0x01 MD1U. HK_MODE_CHK
1754 #: 0x02 MD1U. TLM_CMP 0xFF 0x00 0x11
1755 #: 0x03 MD2U. HK_SET 0xFF 0x08
1756 #: 0x04 MD2U. HK_MODE_CHK
1757 #: 0x05 MD2U. TLM_CMP 0xFF 0x00 0x11
1758 #: 0x0E MD1U. WDT_ENA
1759 #: 0x0F MD2U. WDT_ENA
1760 #
1761 WAIT_SEC 5
1762 MD1U. HK_SET 0xFF 0x03
1763 MD2U. HK_SET 0xFF 0x03
1764 #
1765 # < CHECK >

```

文 書 番 号	シート No.	版 数
	51	1

```

1766 SYSTEM test {MD1U. U_CmdDmc_GetCNT} = {MD1_GET}
1767 SYSTEM test {MD2U. U_CmdDmc_GetCNT} = {MD2_GET}
1768 SYSTEM test {MD1U. U_CmdDmc_ExecCNT} = {MD1_EXE}
1769 SYSTEM test {MD2U. U_CmdDmc_ExecCNT} = {MD2_EXE}
1770 CHECK MD1U. U_WdtEna =ENA
1771 CHECK MD1U. U_HkMode =CHK
1772 CHECK MD1U. U_Hk_RepInt_DPU1 3
1773 CHECK MD1U. U_Hk_RepInt_ENA 3
1774 CHECK MD1U. U_Hk_RepInt_HEPE 3
1775 CHECK MD1U. U_Hk_RepInt_HEPI 3
1776 CHECK MD1U. U_Hk_RepInt_MEA1 3
1777 CHECK MD1U. U_Hk_RepInt_MEA2 3
1778 CHECK MD1U. U_Hk_RepInt_MGFO 3
1779 CHECK MD1U. U_Hk_RepInt_MIA 3
1780 CHECK MD1U. U_Hk_RepInt_MSA 3
1781 CHECK MD1U. U_TlmML_CMP_DPU1 =CMP
1782 CHECK MD1U. U_TlmML_CMP_ENA =USR
1783 CHECK MD1U. U_TlmML_CMP_HEPE =ARO
1784 CHECK MD1U. U_TlmML_CMP_HEPI =ARO
1785 CHECK MD1U. U_TlmML_CMP_MEA1 =USR
1786 CHECK MD1U. U_TlmML_CMP_MEA2 =USR
1787 CHECK MD1U. U_TlmML_CMP_MGFO =ARO
1788 CHECK MD1U. U_TlmML_CMP_MIA =USR
1789 CHECK MD1U. U_TlmML_CMP_MSA =USR
1790 CHECK MD1U. U_TlmH_CMP_DPU1 =CMP
1791 CHECK MD1U. U_TlmH_CMP_HEPE =ARO
1792 CHECK MD1U. U_TlmH_CMP_HEPI =ARO
1793 CHECK MD1U. U_TlmH_CMP_MEA1 =USR
1794 CHECK MD1U. U_TlmH_CMP_MEA2 =USR
1795 CHECK MD1U. U_TlmH_CMP_MGFO =ARO
1796 CHECK MD1U. U_TlmH_CMP_MIA =USR
1797 CHECK MD1U. U_TlmH_CMP_MSA =USR
1798 CHECK MD2U. U_WdtEna =ENA
1799 CHECK MD2U. U_HkMode =CHK
1800 CHECK MD2U. U_Hk_RepInt_DPU2 3
1801 CHECK MD2U. U_Hk_RepInt_EWO 3
1802 CHECK MD2U. U_Hk_RepInt_MEF 3
1803 CHECK MD2U. U_Hk_RepInt_MGFI 3
1804 CHECK MD2U. U_Hk_RepInt_MSAS 3
1805 CHECK MD2U. U_Hk_RepInt_MWE 3
1806 CHECK MD2U. U_TlmML_CMP_DPU2 =CMP
1807 CHECK MD2U. U_TlmML_CMP_AM2P =ARO
1808 CHECK MD2U. U_TlmML_CMP_EFD =ARO
1809 CHECK MD2U. U_TlmML_CMP_EWOB =ARO
1810 CHECK MD2U. U_TlmML_CMP_EWOE =ARO
1811 CHECK MD2U. U_TlmML_CMP_MDM =ARO
1812 CHECK MD2U. U_TlmML_CMP_MGFI =ARO
1813 CHECK MD2U. U_TlmML_CMP_MSAS =ARO
1814 CHECK MD2U. U_TlmML_CMP_SOR =ARO
1815 CHECK MD2U. U_TlmH_CMP_DPU2 =CMP
1816 CHECK MD2U. U_TlmH_CMP_EFD =ARO
1817 CHECK MD2U. U_TlmH_CMP_EWOB =USR

```

文 書 番 号	シート No.	版 数
	52	1

```

1818 CHECK MD2U.U_TImH_CMP_EWOE =USR
1819 CHECK MD2U.U_TImH_CMP_MGFI =ARO
1820 #
1821 . # QL Check-Item Spec Check
1822 # -----+-----+-----+-----
1823 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1824 #
1825 # =====
1826 . # 3.2. PME 立ち上げ／初期化
1827 # =====
1828 . #
1829 . PCD.ON_OFF_ENA
1830 . #
1831 . # < CHECK >
1832 CHECK PCD.ON_OFF_ENA_DIS =ENA
1833 . #
1834 . PCD.PSU_PME_ON
1835 . #
1836 . # < CHECK >
1837 CHECK PCD.PSU_PME_ON_OFF =ON
1838 . #
1839 . # QL Check-Item Spec Check
1840 # -----+-----+-----+-----
1841 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1842 #
1843 # =====
1844 . # 3.3. MGF 立ち上げ／初期化
1845 # =====
1846 . #
1847 . DH.MGF_ON # DH.MC_EACH_EXE 0x095
1848 #: << MACRO >> 内容について以下に示す(DH.DUMMYは省略)
1849 #: 0x00 MD1F.PME_MGFO_ON
1850 #: 0x01 PCD.ON_OFF_ENA
1851 #: 0x02 PCD.PSU_MGF_I_ON
1852 #: 0x03 PCD.ON_OFF_DIS
1853 #: 0x0D MD1F.DPU_SW_ERR_CLR
1854 #: 0x0E MD2F.DPU_SW_ERR_CLR
1855 #: 0x0F MD2U.MGFI_HW_CMD 0xF0E8EE70
1856 #
1857 . # < CHECK >
1858 CHECK PCD.PSU_MGF_I_ON_OFF =ON
1859 CHECK MGFI.CRC_ERR_CNTR 0
1860 CHECK MGFI.RESET_CNTR 0
1861 CHECK MGFI.PHASE_VAL_X_A -28.44 -28.35
1862 CHECK MGFI.PHASE_VAL_Y_A -43.64 -43.55
1863 CHECK MGFI.PHASE_VAL_Z_A -32.24 -32.15
1864 CHECK MGFI.SYNC_DLY_VAL_A 976.55 976.64
1865 CHECK MGFI.TLM_OUT_STAT_L =OUTPUT
1866 CHECK MGFI.TLM_OUT_RATE_L =SPIN
1867 CHECK MGFI.TLM_OUT_STAT_M =OUTPUT
1868 CHECK MGFI.TLM_OUT_RATE_M =F8HZ
1869 CHECK MGFI.TLM_OUT_STAT_H =STOP

```

文 書 番 号	シ-ト No.	版 数
	53	1

1870	CHECK MGFI. TLM_OUT_RATE_H	=CHECKOUT
1871	CHECK MD1F. PME_MGFO_STS	=ON
1872	CHECK MGFO. PRAM_STAT	0
1873	CHECK MGFO. MDATRAM_STAT	0
1874	CHECK MGFO. EXTSYNC_OFF	0
1875	CHECK MGFO. RELAY_ON_OFF	1
1876	CHECK MGFO. OPEN_LOOP	0
1877	CHECK MGFO. EXCIT_ON	1
1878	CHECK MGFO. EEPROM_PAGE	0
1879	CHECK MGFO. BOOT_OP	0
1880	CHECK MGFO. BOOT_MAIN	1
1881	CHECK MGFO. CS_OK	0
1882	CHECK MGFO. RELAY1_STAT	1
1883	CHECK MGFO. SYNC_PRESENT	1
1884	CHECK MGFO. LINK_DISCONNECT	0
1885	CHECK MGFO. LINK_EERR	0
1886	CHECK MGFO. LINK_CERR	0
1887	CHECK MGFO. LINK_PERR	0
1888	CHECK MGFO. RMAP_ERR_FLAGS	0
1889	CHECK MGFO. VALID_WCMDS	0
1890	CHECK MGFO. INVALID_CMDS	0
1891	CHECK MGFO. SYNC_OFFSET	0xa1fb 0xa1fb
1892	CHECK MGFO. SOFTWARE_VER	0x0007
1893	CHECK MGFO. EEPROM_CHECKSUM	0
1894	CHECK MGFO. CALC_CHECKSUM	0
1895	CHECK MGFO. ELECTR_TEMP	-30 70
1896	CHECK MGFO. OS_TEMP	-60 180
1897	CHECK MGFO. IS_TEMP	-60 180
1898	CHECK MGFO. CURRENT_M12V	-40 -29
1899	CHECK MGFO. CURRENT_3V3	32 39
1900	CHECK MGFO. CURRENT_1V5	55 77
1901	CHECK MGFO. VOLTAGE_PANA	7.3 7.9
1902	CHECK MGFO. VOLTAGE_MANA	-7.9 -7.5
1903	CHECK MGFO. VOLTAGE_P12V	11.3 12.5
1904	CHECK MGFO. VOLTAGE_M12V	-12.5 -11.3
1905	CHECK MGFO. VOLTAGE_3V3	3.1 3.5
1906	CHECK MGFO. VOLTAGE_P5V	4.7 5.3
1907	CHECK MGFO. VOLTAGE_M5V	-5.3 -4.7
1908	CHECK MGFO. VOLTAGE_1V5	1.37 1.63
1909	CHECK MGFO. TLM_OUT_STAT_L	=OUTPUT
1910	CHECK MGFO. TLM_OUT_RATE_L	=SPIN
1911	CHECK MGFO. TLM_OUT_STAT_M	=OUTPUT
1912	CHECK MGFO. TLM_OUT_RATE_M	=F8HZ
1913	CHECK MGFO. TLM_OUT_STAT_H	=STOP
1914	CHECK MGFO. TLM_OUT_RATE_H	=CHECKOUT
1915	CHECK MD1F. MS0_LINK_STAT	=ON
1916	CHECK MD2F. MS5_LINK_STAT	=ON
1917	CHECK MD1F. MS0_LINK_ERR	=na
1918	CHECK MD1F. SLOT_ERR	=na
1919	CHECK MD2F. MS5_LINK_ERR	=na
1920	CHECK MD2F. SLOT_ERR	=na
1921	#	

文 書 番 号	シート No.	版 数
	54	1

```

1922  #
1923  #
1924  . # MGF 用磁場キャンセラーの電流粗調整を開始する (PI 作業)
1925  # start coarse adjustment of magnetic compensation coil current (MGF team)
1926  #
1927  . # QL          Check-Item          Spec          Check
1928  # -----+-----+-----+-----
1929  # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
1930  #
1931  # =====
1932  . # 3.4. ENA 立ち上げ／初期化
1933  # =====
1934  . #
1935  . PCD.ON_OFF_ENA
1936  . #
1937  . # < CHECK >
1938  CHECK PCD.ON_OFF_ENA_DIS          =ENA
1939  . #
1940  . PCD.PSU_ENA_ON
1941  . #
1942  . MD1F.DPU_SW_ERR_CLR
1943  . #
1944  . # < CHECK >
1945  CHECK PCD.PSU_ENA_ON_OFF          =ON
1946  CHECK MD1F.MS3_LINK_STAT          =ON
1947  CHECK MD1F.MS3_DAT_CLCT          =ON
1948  #
1949  . # QL          Check-Item          Spec          Check
1950  # -----+-----+-----+-----
1951  # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
1952  #
1953  # =====
1954  . # 3.5. MEA1 立ち上げ／初期化
1955  # =====
1956  . #
1957  . PCD.PSU_MEA1_ON
1958  . #
1959  . MD1F.DPU_SW_ERR_CLR
1960  . #
1961  . # < CHECK >
1962  CHECK PCD.PSU_MEA1_ON_OFF          =ON
1963  CHECK MD1F.MS1_LINK_STAT          =ON
1964  CHECK MD1F.MS1_DAT_CLCT          =ON
1965  #
1966  . # QL          Check-Item          Spec          Check
1967  # -----+-----+-----+-----
1968  # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
1969  #
1970  # =====
1971  . # 3.6. MEA2 立ち上げ／初期化
1972  # =====
1973  . #

```

```

1974 . PCD.PSU_MEA2_ON
1975 . #
1976 . MD1F.DPU_SW_ERR_CLR
1977 . #
1978 . # < CHECK >
1979 CHECK PCD.PSU_MEA2_ON_OFF =ON
1980 CHECK MD1F.MS2_LINK_STAT =ON
1981 CHECK MD1F.MS2_DAT_CLCT =ON
1982 . #
1983 . # QL          Check-Item          Spec          Check
1984 . # -----+-----+-----+-----
1985 . # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
1986 . #
1987 . # =====
1988 . # 3.7. MSA 立ち上げ／初期化
1989 . # =====
1990 . #
1991 . MD1U.MSA_DATA_MODE 0x00 0x01
1992 . PCD.PSU_MSA_ON
1993 . #
1994 . MD1F.DPU_SW_ERR_CLR
1995 . #
1996 . # < CHECK >
1997 CHECK PCD.PSU_MSA_ON_OFF =ON
1998 CHECK MD1F.MS5_LINK_STAT =ON
1999 CHECK MD1F.MS5_DAT_CLCT =ON
2000 CHECK MSA.S_DATA_MODE_SE =SCI
2001 CHECK MSA.S_DATA_MODE 01
2002 . #
2003 . # QL          Check-Item          Spec          Check
2004 . # -----+-----+-----+-----
2005 . # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2006 . #
2007 . # =====
2008 . # 3.8. MIA 立ち上げ／初期化
2009 . # =====
2010 . #
2011 . PCD.PSU_MIA_ON
2012 . #
2013 . MD1F.DPU_SW_ERR_CLR
2014 . #
2015 . # < CHECK >
2016 CHECK PCD.PSU_MIA_ON_OFF =ON
2017 CHECK MD1F.MS4_LINK_STAT =ON
2018 CHECK MD1F.MS4_DAT_CLCT =ON
2019 . #
2020 . # QL          Check-Item          Spec          Check
2021 . # -----+-----+-----+-----
2022 . # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2023 . #
2024 . # =====
2025 . # 3.9. HEP-E 立ち上げ／初期化

```

文 書 番 号	シート No.	版 数
	56	1

```

2026  # =====
2027  . #
2028  . PCD. PSU_HEP_e_ON
2029  . #
2030  . MD1F. DPU_SW_ERR_CLR
2031  . #
2032  . # < CHECK >
2033  CHECK PCD. PSU_HEP_E_ON_OFF          =ON
2034  CHECK MD1F. MS6_LINK_STAT             =ON
2035  CHECK MD1F. MS6_DAT_CLCT              =ON
2036  #
2037  . # QL          Check-Item          Spec          Check
2038  # -----+-----+-----+-----+
2039  # CMD 出力装置   Message          STATUS CHECK 結果 OK      G  N
2040  #
2041  # =====
2042  . # 3.10. HEP-I 立ち上げ／初期化
2043  # =====
2044  . #
2045  . PCD. PSU_HEP_i_ON
2046  . #
2047  . MD1F. DPU_SW_ERR_CLR
2048  . #
2049  . # < CHECK >
2050  CHECK PCD. PSU_HEP_I_ON_OFF          =ON
2051  CHECK MD1F. MS7_LINK_STAT             =ON
2052  CHECK MD1F. MS7_DAT_CLCT              =ON
2053  #
2054  . PCD. ON_OFF_DIS
2055  . #
2056  . # < CHECK >
2057  CHECK PCD. ON_OFF_ENA_DIS             =DIS
2058  . #
2059  . # QL          Check-Item          Spec          Check
2060  # -----+-----+-----+-----+
2061  # CMD 出力装置   Message          STATUS CHECK 結果 OK      G  N
2062  #
2063  # =====
2064  . # 3.11. PWI 立ち上げ／初期化 [EWO, SORBET, AM2P, MEFIST0]
2065  # =====
2066  #
2067  . # <<<< Power on PWI >>>>
2068  . #
2069  . DH. PWI_ON1_EWO_SOR_AM2P          # DH. MC_EACH_EXE 0x0096
2070  #:          << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
2071  #:          0x00 MD2F. PME_EWO_ON
2072  #:          0x09 MD2F. PME_SORBET_ON
2073  #:          0x0A MD2F. PME_AM2P_ON
2074  #:          0x0B EWO. WPTP_ON
2075  #:          0x0C EWO. SCP_ON
2076  #:          0x0D MD2F. DPU_SW_ERR_CLR
2077  #:          0x0E MD2U. BUF_CLCT 0x10 0x00

```

文 書 番 号	シート No.	版 数
	57	1

2078	#:	0x0F MD2U. EWO_HW_INIT	
2079	#		
2080	# < CHECK >		
2081	CHECK MD2F. PME_EWO_STS	=ON	
2082	CHECK MD2F. PME_SORBET_STS	=ON	
2083	CHECK MD2F. PME_AM2P_STS	=ON	
2084	CHECK MD2F. MS1_LINK_STAT	=ON	
2085	CHECK MD2F. MS1_LINK_ERR	=na	
2086	CHECK MD2F. MS1_DAT_CLCT	=ON	
2087	CHECK MD2F. MS3_LINK_STAT	=ON	
2088	CHECK MD2F. MS3_LINK_ERR	=na	
2089	CHECK EWO. WPTP_ONOF	=ON	
2090	CHECK EWO. WPTP_GAIN	=LOW	
2091	CHECK EWO. WPTP_BIAS_ONOF	=ON	
2092	CHECK EWO. WPTP_BIAS_CAL	=BIAS	
2093	CHECK EWO. WPTP_CAL_ONOF	=OFF	
2094	CHECK EWO. WPTP_CAL_IMP	=R10M	
2095	CHECK EWO. SCP_ONOF	=ON	
2096	CHECK EWO. SCP_CAL_STS	=OFF	
2097	CHECK EWO. EFD_SYNC_STS	=EWO	
2098	CHECK EWO. EFD_OBS_MODE	=STBY	
2099	CHECK EWO. EFD_FB_ONOF	=OFF	
2100	CHECK EWO. WPT1_BIAS	128	
2101	CHECK EWO. WPT2_BIAS	128	
2102	CHECK EWO. WPT1_LOOP_GAIN	0	
2103	CHECK EWO. WPT2_LOOP_GAIN	0	
2104	CHECK EWO. EFD_BIAS_SWP_1	0	
2105	CHECK EWO. EFD_BIAS_X1_CNT	1	
2106	CHECK EWO. WFC_E_AM2P_SEL	=STBY	
2107	CHECK EWO. WFC_E_INP_SEL	=DIFF	
2108	CHECK EWO. WFC_E_LPF_STS	=FC_20KHZ	
2109	CHECK EWO. WFC_E_PGA_STS	=LOW	
2110	CHECK EWO. WFC_B_OBS_STS	=STBY	
2111	CHECK EWO. WFC_B_LPF_STS	=FC_20KHZ	
2112	CHECK EWO. WFC_B_PGA_STS	=LOW	
2113	CHECK EWO. WFC_E_CAL_ATT	=OFF	
2114	CHECK EWO. WFC_E_EFD_ICAL	=OFF	
2115	CHECK EWO. WFC_B_ICAL	=OFF	
2116	CHECK EWO. TEMP_MON_ONOF	=ON	
2117	CHECK EWO. AM2P_TC_CNT	0	
2118	CHECK EWO. AM2P_TMTRG_CNT	0	
2119	CHECK EWO. AM2P_TC_ACK_CNT	0x01	
2120	CHECK EWO. AM2P_ACQ_CNT	0x01	
2121	CHECK EWO. OTR_WFCE	=NONE	
2122	CHECK EWO. OTR_WFCB	=NONE	
2123	CHECK EWO. MDP_PAC_EN_DIS	=DIS	
2124	CHECK EWO. EFD_SRAM_FL	=NOT_FULL	
2125	CHECK EWO. WFC_E_SRAM_FL	=NOT_FULL	
2126	CHECK EWO. WFC_B_SRAM_FL	=NOT_FULL	
2127	CHECK EWO. EFD_LU_STS	=NONE	
2128	CHECK EWO. WFC_E_LU_STS	=NONE	
2129	CHECK EWO. WFC_B_LU_STS	=NONE	

文 書 番 号	シート No.	版 数
	58	1

```

2130 CHECK EWO. EWO_CMD_ERR_CNT 0x00
2131 CHECK EWO. RMAP_HDR_ERR 0x00
2132 CHECK EWO. WFC_E_SRAM_ERR1 0x00
2133 CHECK EWO. WFC_E_SRAM_ERR2 0x00
2134 CHECK EWO. WFC_B_SRAM_ERR1 0x00
2135 CHECK EWO. WFC_B_SRAM_ERR2 0x00
2136 CHECK EWO. EFD_FIR_ERR =NONE
2137 CHECK EWO. WFC_B_FIR_ERR =NONE
2138 #
2139 # ===== Record of the initial vcalues =====
2140 # QL Check-Item Spec Data Check
2141 # -----+-----+-----+-----+
2142 # EWO T1 記録のみ N/A
2143 #
2144 # < CHECK >
2145 CHECK SORB. CMD_CODE 0
2146 CHECK SORB. WPT_GAIN =LOW
2147 CHECK SORB. WPT_BIAS_ON_OFF =OFF
2148 CHECK SORB. WPT_CAL_ON_OFF =OFF
2149 CHECK SORB. TNR1_ON_OFF =OFF
2150 CHECK SORB. TNR1_INP_SETUP 0
2151 CHECK SORB. TNR2_ON_OFF =OFF
2152 CHECK SORB. TNR2_INP_SETUP 0
2153 CHECK SORB. HFR_ON_OFF =OFF
2154 CHECK SORB. HFR_INP_SETUP 0
2155 CHECK SORB. VIA_TNR1 0
2156 CHECK SORB. VIA_TNR2 0
2157 CHECK SORB. HF1_RANGE 0
2158 CHECK SORB. HF2_RANGE 0
2159 CHECK SORB. HFR_FREQ_STEP 0
2160 CHECK SORB. SPW_LINK 1
2161 CHECK SORB. SPW_ERROR =NORMAL
2162 CHECK SORB. CAL_SRC_SETUP 0
2163 CHECK SORB. CAL_BAND_A 0
2164 CHECK SORB. CAL_BAND_B 0
2165 CHECK SORB. CAL_BAND_C 0
2166 CHECK SORB. CAL_BAND_D 0
2167 CHECK SORB. CAL_BAND_HF1 0
2168 CHECK SORB. CAL_BAND_HF2 0
2169 #
2170 # << PWI Observation Start 0x00D3 >>
2171 # DH. PWI_OBS_START # DH. MC_EACH_EXE 0x00D3
2172 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
2173 #: 0x00 EWO. WFCE_OBS_START
2174 #: 0x01 EWO. WFCB_OBS_START
2175 #: 0x02 EWO. EFD_OBS_START
2176 #: 0x03 SORB. OBS_SEQ
2177 #: 0x08 MD2U. BUF_CLCT 0x10 0x01
2178 #: 0x0E EWO. RAM_FUL_CLR
2179 #
2180 # < CHECK >
2181 CHECK EWO. EFD_OBS_MODE =OBS

```

文 書 番 号	シート No.	版 数
TPP-FS8238-31K	59	1

2182	CHECK EWO.WFC_E_AM2P_SEL	=WFC_OBS	
2183	CHECK EWO.WFC_B_OBS_STS	=OBS	
2184	CHECK EWO.MDP_PAC_EN_DIS	=ENA	
2185	CHECK MD2F.MS3_DAT_CLCT	=ON	
2186	CHECK SORB.TNR1_ON_OFF	=ON	
2187	CHECK SORB.TNR1_INP_SETUP	0x1	
2188	CHECK SORB.TNR2_ON_OFF	=ON	
2189	CHECK SORB.VIA_TNR1	0x0	
2190	#		
2191	# QL	Check-Item	Spec
2192	#		Check
2193	# SORBET	COMMAND COUNTER	Increment G N
2194	#	HFR FREQ. STEP	Increment G N
2195	#		
2196	#	COMMAND CODE	0x518F または 0x53BF G N
2197	#		
2198	MD2U.EWO_WFCE_FREQ_SEL	0x01	
2199	#		
2200	# # PWI 側で確認		
2201	# QL	Check-Item	Spec
2202	#		
2203	# EWO	WFC(E) LPF FREQ	FC_120KHZ
2204	#		
2205	MD2U.EWO_EFD_MODE	0xFF 0xFF	
2206	#		
2207	DH.PWI_ON2_MEF	# DH.MC_EACH_EXE 0x008A	
2208	#:	<< MACRO >> 内容について以下に示す (DH.DUMMY は省略)	
2209	#:	0x00 MD2F.PME_MEFISTO_ON	
2210	#:	0x03 MD2F.DPU_SW_ERR_CLR	
2211	#		
2212	# < CHECK >		
2213	CHECK MD2F.PME_MEFISTO_STS	=ON	
2214	CHECK MD2F.MS4_LINK_STAT	=ON	
2215	CHECK MD2F.MS4_LINK_ERR	=na	
2216	#		
2217	# << PWI-MEFISTO Initialize 0x00D2 >>		
2218	DH.PWI_MEF_INIT	# DH.MC_EACH_EXE 0x00D2	
2219	#:	<< MACRO >> 内容について以下に示す (DH.DUMMY は省略)	
2220	#:	0x00 MEF1.INIT_A	
2221	#:	0x02 MEF1.INIT_A	
2222	#:	0x04 MEF1.INIT_B	
2223	#:	0x06 MEF1.INIT_C	
2224	#		
2225	# < CHECK >		
2226	CHECK MEF1.BDAC1	0x8000	
2227	CHECK MEF1.BDAC2	0x8000	
2228	CHECK MEF1.SDAC1	0x8000	
2229	CHECK MEF1.SDAC2	0x8000	
2230	CHECK MEF1.ADC_HV_NEG	-145 145	
2231	CHECK MEF1.ADC_HV_POS	-145 145	
2232	#		
2233	# QL	Check-Item	Spec
			Check

文 書 番 号	シート No.	版 数
	60	1

```

2234 . # -----+-----+-----+-----
2235 . # CMD 出力装置      Message          STATUS CHECK 結果 OK      G      N
2236 . #
2237 . # =====
2238 . #   3. 12. MAST 立ち上げ／初期化
2239 . # =====
2240 . #
2241 . MD2F. PME_MAST_ON
2242 . #
2243 . MD2F. DPU_SW_ERR_CLR
2244 . #
2245 . # < CHECK >
2246 . CHECK MD2F. PME_MAST_STS              =ON
2247 . CHECK MD2F. MSO_LINK_STAT             =ON
2248 . #
2249 . # <初期チェック>
2250 . #
2251 . # < CHECK >
2252 . CHECK MWE. STAT_STBY                  =OFF
2253 . CHECK MWE. STAT_MST_PSU               =MAIN
2254 . CHECK MWE. STAT_WPT_PSU               =MAIN
2255 . CHECK MWE. STAT_SAFE_MODE             =OFF
2256 . CHECK MWE. STAT_OVLD_CONT             =DIS
2257 . CHECK MWE. STAT_TEMP_CONT            =DIS
2258 . CHECK MWE. STAT_UNBL_CONT            =DIS
2259 . CHECK MWE. MST_MS_CONT                =ENA
2260 . CHECK MWE. MST_LEN_CONT              =DIS
2261 . CHECK MWE. WPT_MS_CONT                =ENA
2262 . CHECK MWE. WPT_LEN_CONT              =DIS
2263 . CHECK MWE. STAT_WPT_RATE              =RST
2264 . CHECK MWE. STAT_MST_STBY              =OFF
2265 . CHECK MWE. MST_SC_RET                 =OFF
2266 . CHECK MWE. MST_SC_EXT                 =OFF
2267 . CHECK MWE. MST_SC_RLSE                =OFF
2268 . CHECK MWE. MST_SC_DRV                 =OFF
2269 . CHECK MWE. CONT_MTR2_OVHT             =NON
2270 . CHECK MWE. CONT_MTR2_OVLD            =NON
2271 . CHECK MWE. MST_SC_MS_RET              =FULL
2272 . CHECK MWE. CONT_LEN_DET_SC            =NO
2273 . CHECK MWE. CONT_MS_RL_SC              =OFF
2274 . CHECK MWE. MST_MGF_RET                =OFF
2275 . CHECK MWE. MST_MGF_EXT                =OFF
2276 . CHECK MWE. MST_MGF_RLSE              =OFF
2277 . CHECK MWE. MST_MGF_DRV                =OFF
2278 . CHECK MWE. CONT_MTR1_OVHT             =NON
2279 . CHECK MWE. CONT_MTR1_OVLD            =NON
2280 . CHECK MWE. MST_MGF_MS_RET              =FULL
2281 . CHECK MWE. CONT_LEN_DET_MGF           =NO
2282 . CHECK MWE. CONT_MS_RL_MGF             =OFF
2283 . CHECK MWE. STAT_WPT_STBY              =OFF
2284 . CHECK MWE. WPT_S1_RET                 =OFF
2285 . CHECK MWE. WPT_S1_EXT                 =OFF

```

文 書 番 号	シート No.	版 数
	61	1

2286 CHECK MWE.WPT_S1_RELS =OFF
2287 CHECK MWE.WPT_S1_DRV =OFF
2288 CHECK MWE.WPT_S1_MS_HRM =CLS
2289 CHECK MWE.WPT_S1_MS_EXT =NON
2290 CHECK MWE.CONT_LEN_DET_S1 =NO
2291 CHECK MWE.CONT_MS_RL_S1 =OFF
2292 CHECK MWE.WPT_S2_RET =OFF
2293 CHECK MWE.WPT_S2_EXT =OFF
2294 CHECK MWE.WPT_S2_RELS =OFF
2295 CHECK MWE.WPT_S2_DRV =OFF
2296 CHECK MWE.WPT_S2_MS_HRM =CLS
2297 CHECK MWE.WPT_S2_MS_EXT =NON
2298 CHECK MWE.CONT_LEN_DET_S2 =NO
2299 CHECK MWE.CONT_MS_RL_S2 =OFF
2300 #
2301 . # < MWE STBY ON >
2302 . MWE.MWE_STBY_ON
2303 . # STBY ON から 5 秒以上経過後、ステータスを QL にてチェックする。
2304 #
2305 . # < CHECK >
2306 CHECK MWE.STAT_STBY =ON
2307 CHECK MWE.MWE_CMND_ADRS 0x0A10
2308 CHECK MWE.MWE_CMND_CODE 0x0010
2309 . #
2310 . # QL Check-Item Spec Data Check
2311 # -----+-----+-----+-----+-----
2312 # MW-E MST_SC_MTR_TEMP 取得のみ _____°C N/A
2313 # MST_SC_DRV_TEMP 取得のみ _____°C N/A
2314 # MST_MGF_MTR_TEMP 取得のみ _____°C N/A
2315 # MST_MGF_DRV_TEMP 取得のみ _____°C N/A
2316 # WPT_S1_MTR_TEMP 取得のみ _____°C N/A
2317 # WPT_S1_CHAS_TEMP 取得のみ _____°C N/A
2318 # WPT_S2_MTR_TEMP 取得のみ _____°C N/A
2319 # WPT_S2_CHAS_TEMP 取得のみ _____°C N/A
2320 # MWE_FPGA_TEMP 取得のみ _____°C N/A
2321 # MWE_TR_TEMP 取得のみ _____°C N/A
2322 #
2323 . # ※エラークリアは後でまとめて実施。
2324 #
2325 . # QL Check-Item Spec Check
2326 . # -----+-----+-----+-----+-----
2327 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2328 . #
2329 # =====
2330 . # 3.13. MOTOR-PSU チェック
2331 # =====
2332 . #
2333 . PCD.ON_OFF_ENA
2334 . #
2335 . # < CHECK >
2336 CHECK PCD.ON_OFF_ENA_DIS =ENA
2337 . #

```

2338 . PCD.PSU_Motor_A_ON
2339 . #
2340 . # < CHECK >
2341 . CHECK PCD.PSU_MORTOR_A_ON_OFF =ON
2342 . #
2343 . # QL          Check-Item          Spec          Data          Check
2344 . # -----+-----+-----+-----+-----
2345 . # PCS1          PCD.PSU_MOTOR_A_I    取得のみ          _____ N/A
2346 . #
2347 . PCD.PSU_Motor_A_OFF
2348 . #
2349 . # < CHECK >
2350 . CHECK PCD.PSU_MORTOR_A_ON_OFF =OFF
2351 . #
2352 . PCD.PSU_Motor_B_ON
2353 . #
2354 . # < CHECK >
2355 . CHECK PCD.PSU_MORTOR_B_ON_OFF =ON
2356 . #
2357 . # QL          Check-Item          Spec          Data          Check
2358 . # -----+-----+-----+-----+-----
2359 . # PCS1          PCD.PSU_MOTOR_B_I    取得のみ          _____ N/A
2360 . #
2361 . PCD.PSU_Motor_B_OFF
2362 . #
2363 . # < CHECK >
2364 . CHECK PCD.PSU_MORTOR_B_ON_OFF =OFF
2365 . #
2366 . # QL          Check-Item          Spec          Check
2367 . # -----+-----+-----+-----
2368 . # CMD 出力装置  Message          STATUS CHECK 結果 OK    G    N
2369 . #
2370 . # =====
2371 . # 3.14. MDM 立ち上げ／初期化
2372 . # =====
2373 . #
2374 . PCD.MDM_E_ON
2375 . #
2376 . MD2F.DPU_SW_ERR_CLR
2377 . #
2378 . # < CHECK >
2379 . CHECK PCD.MDM_E_ON_OFF =ON
2380 . CHECK MD2F.MS7_LINK_STAT =ON
2381 . CHECK MD2F.MS7_DAT_CLCT =ON
2382 . #
2383 . # QL          Check-Item          Spec          Check
2384 . # -----+-----+-----+-----
2385 . # CMD 出力装置  Message          STATUS CHECK 結果 OK    G    N
2386 . #
2387 . # =====
2388 . # 3.15. MSASI 立ち上げ／初期化
2389 . # =====

```

```

2390 . #
2391 . PCD.MSASI_ON
2392 . #
2393 . MD2F.DPU_SW_ERR_CLR
2394 . #
2395 . # < CHECK >
2396 . CHECK PCD.MSASI_ON_OFF =ON
2397 . CHECK MD2F.MS6_LINK_STAT =ON
2398 . CHECK MD2F.MS6_DAT_CLCT =OFF
2399 . #
2400 . PCD.ON_OFF_DIS
2401 . #
2402 . # < CHECK >
2403 . CHECK PCD.ON_OFF_ENA_DIS =DIS
2404 . #
2405 . MD1F.DPU_SW_ERR_CLR
2406 . MD2F.DPU_SW_ERR_CLR
2407 . #
2408 . # < CHECK >
2409 . CHECK MD1F.MS0_LINK_ERR =na
2410 . CHECK MD1F.MS1_LINK_ERR =na
2411 . CHECK MD1F.MS2_LINK_ERR =na
2412 . CHECK MD1F.MS3_LINK_ERR =na
2413 . CHECK MD1F.MS4_LINK_ERR =na
2414 . CHECK MD1F.MS5_LINK_ERR =na
2415 . CHECK MD1F.MS6_LINK_ERR =na
2416 . CHECK MD1F.MS7_LINK_ERR =na
2417 . CHECK MD1F.SLOT_ERR =na
2418 . CHECK MD2F.MS0_LINK_ERR =na
2419 . CHECK MD2F.MS1_LINK_ERR =na
2420 . CHECK MD2F.MS2_LINK_ERR =na
2421 . CHECK MD2F.MS3_LINK_ERR =na
2422 . CHECK MD2F.MS4_LINK_ERR =na
2423 . CHECK MD2F.MS5_LINK_ERR =na
2424 . CHECK MD2F.MS6_LINK_ERR =na
2425 . CHECK MD2F.MS7_LINK_ERR =na
2426 . CHECK MD2F.SLOT_ERR =na
2427 . #
2428 . # QL          Check-Item          Spec          Check
2429 . # -----+-----+-----+-----+
2430 . # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2431 . #
2432 . # =====
2433 . # 3.16. SPM チェック
2434 . # =====
2435 . #
2436 . # QL          Check-Item          Spec          Data          Check
2437 . # -----+-----+-----+-----+
2438 . # TCIU          DH.SPM1_LV1_CNT      取得のみ          _____      N/A
2439 . #               DH.SPM1_LV2_CNT      取得のみ          _____      N/A
2440 . #               DH.SPM1_LV3_CNT      取得のみ          _____      N/A
2441 . #               DH.SPM1_LV4_CNT      取得のみ          _____      N/A

```

文 書 番 号	シート No.	版 数
	64	1

```

2442 # DH. SPM1_ERR_CNT 取得のみ _____ N/A
2443 # DH. SPM2_LV1_CNT 取得のみ _____ N/A
2444 # DH. SPM2_LV2_CNT 取得のみ _____ N/A
2445 # DH. SPM2_LV3_CNT 取得のみ _____ N/A
2446 # DH. SPM2_LV4_CNT 取得のみ _____ N/A
2447 # DH. SPM2_ERR_CNT 取得のみ _____ N/A
2448 #
2449 # =====
2450 # 3.17. 消費電力取得
2451 # =====
2452 #
2453 # 【記録】
2454 # 電圧/電流値の取得を実施する。
2455 # 結果を<データシート 3.17-1>に記録すること。
2456 #
2457 # *****
2458 # 4. L/M-Mode Check
2459 # *****
2460 # =====
2461 # 4.1. MDP L/M-Mode 設定
2462 # =====
2463 #
2464 # N/A
2465 #
2466 # =====
2467 # 4.2. ENA L/M-Mode check
2468 # =====
2469 #
2470 # ENA status が以下となっていることを確認する。
2471 #
2472 # < CHECK >
2473 CHECK ENA.CMD_ERR_FLG =FALSE
2474 CHECK ENA.HV_SAFTY_DIS =ENA
2475 CHECK ENA.HV_ON =OFF
2476 CHECK ENA.HV_ON_ENA =DIS
2477 CHECK ENA.HV_SHUTDOWN =NON
2478 CHECK ENA.HV_SHUTDOWN_MODE =NON
2479 CHECK ENA.ENA_TBL_WRT_IFE_END =END
2480 CHECK ENA.CMD_CTR2 0x82
2481 #
2482 # QL Check-Item Spec Check
2483 # -----+-----+-----+-----
2484 # MPPE-ENA2 PSYNCCTR カウントアップする G N
2485 # BASECTR カウントアップする G N
2486 #
2487 # < mission packet 生成 (HK) >
2488 #
2489 # MD1U.ENA_PRI0_HK_SET 0x05
2490 #
2491 # < CHECK >
2492 CHECK ENA.ENA_HK_INTVAL =sec_16
2493 #

```

文 書 番 号	シ-ト No.	版 数
	65	1

```
2494 . # < mass accumulation mode >
2495 . MD1U. ENA_PROCESS_MODE_SET 0x00 0x88 0x10 0x08 0x02 0xff 0xff 0x00
2496 &                                0x00 0x20 0x00 0x00 0x20 0x00 0x22 0x00
2497 &                                0x00 0x00 0x00
2498 #
2499 . CALL dcsn-f310-1 # 変更手順 1 (付録 2)
2500 #
2501 #
2502 #
2503 #
2504 #
2505 #
2506 #
2507 #
2508 #
2509 #
2510 #
2511 #
2512 #
2513 #
2514 #
2515 #
2516 #
2517 #
2518 #
2519 #
2520 #
2521 #
2522 #
2523 #
2524 #
2525 #
2526 #
2527 #
2528 #
2529 #
2530 #
2531 #
2532 #
2533 #
2534 #
2535 #
2536 #
2537 #
2538 #
2539 #
2540 #
2541 #
2542 #
2543 #
2544 #
2545 #
```

文 書 番 号	シ-ト No.	版 数
	66	1

2546	#
2547	#
2548	#
2549	#
2550	#
2551	#
2552	#
2553	#
2554	#
2555	#
2556	#
2557	#
2558	#
2559	#
2560	#
2561	#
2562	#
2563	#
2564	#
2565	#
2566	#
2567	#
2568	#
2569	#
2570	#
2571	#
2572	#
2573	#
2574	#
2575	#
2576	#
2577	#
2578	#
2579	#
2580	#
2581	#
2582	#
2583	#
2584	#
2585	#
2586	#
2587	#
2588	#
2589	#
2590	#
2591	#
2592	#
2593	#
2594	#
2595	#
2596	#
2597	#

文 書 番 号	シ-ト No.	版 数
	67	1

2598	#
2599	#
2600	#
2601	#
2602	#
2603	#
2604	#
2605	#
2606	#
2607	#
2608	#
2609	#
2610	#
2611	#
2612	#
2613	#
2614	#
2615	#
2616	#
2617	#
2618	#
2619	#
2620	#
2621	#
2622	#
2623	#
2624	#
2625	#
2626	#
2627	#
2628	#
2629	#
2630	#
2631	#
2632	#
2633	#
2634	#
2635	#
2636	#
2637	#
2638	#
2639	#
2640	#
2641	#
2642	#
2643	#
2644	#
2645	#
2646	#
2647	#
2648	#
2649	#

文 書 番 号	シ-ト No.	版 数
	68	1

```

2650  #
2651  #
2652  #
2653  #
2654  #
2655  #
2656  #
2657  #
2658  #
2659  #
2660  #
2661  #
2662  #
2663  #
2664  #
2665  #
2666  #
2667  #
2668  #
2669  #
2670  #
2671  #
2672  #
2673  #
2674  . DH. DR_REC_OFF
2675  #
2676  . # < CHECK >
2677  CHECK DH. DR_REC_ON_OFF          =OFF
2678  #
2679  . # < counter accumulation mode >
2680  . MD1U. ENA_PROCESS_MODE_SET 0x01 0x18 0x10 0x01 0x02 0xff 0xff 0x00
2681  &                                0x00 0x20 0x00 0x00 0x20 0x00 0x22 0x00
2682  &                                0x00 0x00 0x00
2683  #
2684  . # < CHECK >
2685  CHECK ENA. ENA_PROCESS_MODE          =CNT
2686  CHECK ENA. OBS_MODE                  =CTR
2687  CHECK ENA. SV_WAVE1_TBL              2
2688  CHECK ENA. SV_WAVE2A_TBL             2
2689  CHECK ENA. SV_WAVE2B_TBL             2
2690  CHECK ENA. SV_LENS_TBL               2
2691  CHECK ENA. ENA_PHASE_NUMID           =n_16
2692  CHECK ENA. ENA_ENGY_NUMID            =n_8
2693  CHECK ENA. ENA_CH_NUMID              =n_1
2694  CHECK ENA. ENA_MASS_NUMID            =n_1
2695  CHECK ENA. ENA_ACCUM_NUMID           =sp_4
2696  CHECK ENA. ENA_TRG_ENADIS           =DIS
2697  . #
2698  . DH. DR_REC_ON
2699  #
2700  . # < CHECK >
2701  CHECK DH. DR_REC_ON_OFF              =ON

```

文 書 番 号	シ-ト No.	版 数
	69	1

```

2702 . #
2703 . # << 1 分以上待つ >>
2704 . #
2705 . DH. DR_REC_OFF
2706 . #
2707 . # < CHECK >
2708 . CHECK DH. DR_REC_ON_OFF =OFF
2709 . #
2710 . # < TOF accumulation mode >
2711 . MD1U. ENA_PROCESS_MODE_SET 0x02 0x18 0x01 0x01 0x02 0xff 0xff 0x00
2712 & 0x00 0x20 0x00 0x00 0x20 0x00 0x22 0x00
2713 & 0x00 0x00 0x00
2714 . #
2715 . # < CHECK >
2716 . CHECK ENA. ENA_PROCESS_MODE =TOF
2717 . CHECK ENA. OBS_MODE =COINCI
2718 . CHECK ENA. SV_WAVE1_TBL 2
2719 . CHECK ENA. SV_WAVE2A_TBL 2
2720 . CHECK ENA. SV_WAVE2B_TBL 2
2721 . CHECK ENA. SV_LENS_TBL 2
2722 . CHECK ENA. ENA_PHASE_NUMID =n_1
2723 . CHECK ENA. ENA_ENGY_NUMID =n_8
2724 . CHECK ENA. ENA_CH_NUMID =n_1
2725 . CHECK ENA. ENA_MASS_NUMID =n_1
2726 . CHECK ENA. ENA_TRG_ENADIS =DIS
2727 . #
2728 . DH. DR_REC_ON
2729 . #
2730 . # < CHECK >
2731 . CHECK DH. DR_REC_ON_OFF =ON
2732 . #
2733 . # << 1 分以上待つ >>
2734 . #
2735 . DH. DR_REC_OFF
2736 . #
2737 . # < CHECK >
2738 . CHECK DH. DR_REC_ON_OFF =OFF
2739 . #
2740 . # < engineering mode >
2741 . MD1U. ENA_PROCESS_MODE_SET 0x03 0x00 0x00 0x00 0x00 0xff 0xff 0x00
2742 & 0x00 0x24 0x00 0x00 0x20 0x00 0x22 0x00
2743 & 0x00 0x00 0x00
2744 . #
2745 . # < CHECK >
2746 . CHECK ENA. ENA_PROCESS_MODE =ENG
2747 . CHECK ENA. ENA_PROCPCKT_INSPIN_NUMID =n_16
2748 . CHECK ENA. ENA_ACCUM_NUMID =sp_4
2749 . #
2750 . DH. DR_REC_ON
2751 . #
2752 . # < CHECK >
2753 . CHECK DH. DR_REC_ON_OFF =ON

```

文 書 番 号	シート No.	版 数
	70	1

```

2754 . #
2755 . # << 1 分以上待つ >>
2756 . #
2757 . DH. DR_REC_OFF
2758 . #
2759 . # < CHECK >
2760 . CHECK DH. DR_REC_ON_OFF =OFF
2761 . #
2762 . # < mass accumulation mode >
2763 . MD1U. ENA_PROCESS_MODE_SET 0x00 0x88 0x10 0x08 0x02 0xff 0xff 0x00
2764 . & 0x00 0x20 0x00 0x00 0x20 0x00 0x22 0x00
2765 . & 0x00 0x00 0x00
2766 . #
2767 . # < CHECK >
2768 . CHECK ENA. ENA_PROCESS_MODE =MASS
2769 . CHECK ENA. OBS_MODE =COINCI
2770 . CHECK ENA. SV_WAVE1_TBL 2
2771 . CHECK ENA. SV_WAVE2A_TBL 2
2772 . CHECK ENA. SV_WAVE2B_TBL 2
2773 . CHECK ENA. SV_LENS_TBL 2
2774 . CHECK ENA. ENA_PHASE_NUMID =n_16
2775 . CHECK ENA. ENA_ENGY_NUMID =n_8
2776 . CHECK ENA. ENA_CH_NUMID =n_8
2777 . CHECK ENA. ENA_MASS_NUMID =n_8
2778 . CHECK ENA. ENA_ACCUM_NUMID =sp_4
2779 . CHECK ENA. ENA_TRG_ENADIS =DIS
2780 . #
2781 . DH. DR_REC_ON
2782 . #
2783 . # < CHECK >
2784 . CHECK DH. DR_REC_ON_OFF =ON
2785 . #
2786 . # << 1 分以上待つ >>
2787 . #
2788 . DH. DR_REC_OFF
2789 . #
2790 . # < CHECK >
2791 . CHECK DH. DR_REC_ON_OFF =OFF
2792 . #
2793 . # QL Check-Item Spec Check
2794 . # -----+-----+-----+-----
2795 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2796 . CALL dcsn-f310-2 # 変更手順 2 (付録 3)
2797 . # =====
2798 . # 4.3. MEA1 L/M-Mode check
2799 . # =====
2800 . #
2801 . # MEA1 テレメトリ表示の確認 (LINK 確立から 8 秒以上経過後に実施)
2802 . # Confirmation of the MEA1 telemetry indication
2803 . # (More than 8 minutes after from LINK establishment)
2804 . #
2805 . # < CHECK >

```

文 書 番 号	シート No.	版 数
	71	1

```

2806 CHECK MEA1.HK_I_P5V <110
2807 CHECK MEA1.HK_I_P12V <17
2808 CHECK MEA1.HK_MCP 0
2809 CHECK MEA1.I_MCP 0
2810 CHECK MEA1.HK_TMP_FPG <30
2811 CHECK MEA1.HK_TMP_MCP <30
2812 CHECK MEA1.Status_reg 0x80
2813 #
2814 . # SET MODE
2815 . MD1U.MEA1_DATA_MODE 0x00 0x01
2816 . #
2817 . # < CHECK >
2818 CHECK MEA1.S_DATA_MODE_SE =SCI
2819 CHECK MEA1.S_DATA_MODE 1
2820 #
2821 . # <<MCP_ON>>
2822 #
2823 . # High Voltage 出力 Enable
2824 . # MDP send a CMD MEA1 HV Enable (unlock HV MCP and Spheres)
2825 . MEA1.HV_EN
2826 . #
2827 . # < CHECK >
2828 CHECK MEA1.l1_cmd_exe =0x1D00
2829 CHECK MEA1.l2_cmd_exe =0xFFFF
2830 #
2831 . # MDP send a MCP ON 0x1C at 0V (param : 0x0E)
2832 . MEA1.MCP_ON 0x0E
2833 . #
2834 . # < CHECK >
2835 CHECK MEA1.l1_cmd_exe =0x1C0E
2836 CHECK MEA1.l2_cmd_exe =0x1D00
2837 CHECK MEA1.HK_MCP 0 2
2838 CHECK MEA1.Status_reg 0xC0
2839 #
2840 . # <<Analyzer ON>>
2841 #
2842 . # Analyzer の scan 設定のため、下記 6 コマンドを送信
2843 . # MDP send a 6 commands to switch on the scan of the analyzer
2844 #
2845 . # -1st: switch on the scan, command 0x25 0x00
2846 . MEA1.Scan_ON
2847 . #
2848 . # < CHECK >
2849 CHECK MEA1.l1_cmd_exe =0x2500
2850 CHECK MEA1.l2_cmd_exe =0x1C0E
2851 #
2852 . # -2nd: choose the semi auto-command (Sun pulse synchronization)
2853 . MEA1.Md_S_Auto
2854 . #
2855 . # < CHECK >
2856 CHECK MEA1.l1_cmd_exe =0x3100
2857 CHECK MEA1.l2_cmd_exe =0x2500

```

文 書 番 号	シート No.	版 数
	72	1

```

2858 . #
2859 . # -3rd: choose the mode ROM4 with Adress=04
2860 . MEA1.mode_ROM_3 0x02
2861 . #
2862 . # < CHECK >
2863 . CHECK MEA1.l1_cmd_exe =0x0202
2864 . CHECK MEA1.l2_cmd_exe =0x3100
2865 . #
2866 . # <<Geometry Factor>>
2867 . #
2868 . # MDP send Geometry Factor command
2869 . # --GF = 0.8
2870 . MEA1.G_Factor 0x01
2871 . #
2872 . # < CHECK >
2873 . CHECK MEA1.l1_cmd_exe 0x0801
2874 . CHECK MEA1.l2_cmd_exe 0x0202
2875 . CHECK MEA1.Status_reg 0xD0
2876 . #
2877 . # --GF = 0.27
2878 . MEA1.G_Factor 0x04
2879 . #
2880 . # < CHECK >
2881 . CHECK MEA1.l1_cmd_exe 0x0804
2882 . CHECK MEA1.l2_cmd_exe 0x0801
2883 . CHECK MEA1.Status_reg 0xC2
2884 . #
2885 . # <<Test Ampli A111>>
2886 . #
2887 . # MDP Start the data mission read during 3 minutes
2888 . # -Check the Science Data value = 0 (no count)
2889 . #
2890 . # MDP send Ampli Test Mode ON
2891 . MEA1.Md_Tst_ON
2892 . #
2893 . # < CHECK >
2894 . CHECK MEA1.l1_cmd_exe 0x3900
2895 . CHECK MEA1.l2_cmd_exe 0x0804
2896 . CHECK MEA1.Status_reg 0xC3
2897 . #
2898 . # -Check the Science Data value (count) after 8 sec
2899 . #
2900 . DH.DR_REC_ON
2901 . #
2902 . # < CHECK >
2903 . CHECK DH.DR_REC_ON_OFF =ON
2904 . #
2905 . # << 1 分以上待つ >>
2906 . #
2907 . DH.DR_REC_OFF
2908 . #
2909 . # < CHECK >

```

文 書 番 号	シート No.	版 数
	73	1

```

2910 CHECK DH.DR_REC_ON_OFF =OFF
2911 #
2912 . # QL Check-Item Spec Check
2913 # -----+-----+-----+
2914 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2915 #
2916 # =====
2917 . # 4.4. MEA2 L/M-Mode check
2918 # =====
2919 #
2920 . # MEA2 テレメトリ表示の確認 (LINK 確立から 8 秒以上経過後に実施)
2921 . # Confirmation of the MEA2 telemetry indication
2922 . # (More than 8 minutes after from LINK establishment)
2923 #
2924 . # < CHECK >
2925 CHECK MEA2.HK_I_P5V <110
2926 CHECK MEA2.HK_I_P12V <17
2927 CHECK MEA2.HK_MCP 0
2928 CHECK MEA2.I_MCP 0
2929 CHECK MEA2.HK_TMP_FPG <30
2930 CHECK MEA2.HK_TMP_MCP <30
2931 CHECK MEA2.Status_reg 0x80
2932 #
2933 . # SET MODE
2934 . MD1U.MEA2_DATA_MODE 0x00 0x01
2935 . #
2936 . # < CHECK >
2937 CHECK MEA2.S_DATA_MODE_SE =SCI
2938 CHECK MEA2.S_DATA_MODE 1
2939 . #
2940 . # <<MCP_ON>>
2941 #
2942 . # MDP send a CMD MEA2 HV Enable (unlock HV MCP and Spheres)
2943 . MEA2.HV_EN
2944 . #
2945 . # < CHECK >
2946 CHECK MEA2.I1_cmd_exe =0x1D00
2947 CHECK MEA2.I2_cmd_exe =0xFFFF
2948 . #
2949 . # MDP send a MCP ON 0x00 at 0V (param : 0x0E)
2950 . MEA2.MCP_ON 0x0E
2951 . #
2952 . # < CHECK >
2953 CHECK MEA2.I1_cmd_exe =0x1C0E
2954 CHECK MEA2.I2_cmd_exe =0x1D00
2955 CHECK MEA2.HK_MCP 0 2
2956 CHECK MEA2.Status_reg 0xC0
2957 . #
2958 . # <<Analyzer ON>>
2959 #
2960 . # Analyzer の scan 設定のため、下記 6 コマンドを送信
2961 . # MDP send a 6 commands to switch on the scan of the analyzer

```

文 書 番 号	シート No.	版 数
	74	1

```

2962 . #
2963 . # -1st: switch on the scan, command 0x25 0x00
2964 . MEA2.Scan_ON
2965 . #
2966 . # < CHECK >
2967 . CHECK MEA2.l1_cmd_exe =0x2500
2968 . CHECK MEA2.l2_cmd_exe =0x1C0E
2969 . #
2970 . # -2nd: choose the semi auto-command (Sun pulse synchronization)
2971 . MEA2.Md_S_Auto
2972 . #
2973 . # < CHECK >
2974 . CHECK MEA2.l1_cmd_exe =0x3100
2975 . CHECK MEA2.l2_cmd_exe =0x2500
2976 . #
2977 . # -3rd: choose the mode ROM4 with Adress=04
2978 . MEA2.mode_ROM_3 0x02
2979 . #
2980 . # < CHECK >
2981 . CHECK MEA2.l1_cmd_exe =0x0202
2982 . CHECK MEA2.l2_cmd_exe =0x3100
2983 . #
2984 . # <<Geometry Factor>>
2985 . #
2986 . # MDP send Geometry Factor command
2987 . # --GF = 0.8
2988 . MEA2.G_Factor 0x01
2989 . #
2990 . # < CHECK >
2991 . CHECK MEA2.l1_cmd_exe 0x0801
2992 . CHECK MEA2.l2_cmd_exe 0x0202
2993 . CHECK MEA2.Status_reg 0xD0
2994 . #
2995 . # --GF = 0.27
2996 . MEA2.G_Factor 0x04
2997 . #
2998 . # < CHECK >
2999 . CHECK MEA2.l1_cmd_exe 0x0804
3000 . CHECK MEA2.l2_cmd_exe 0x0801
3001 . CHECK MEA2.Status_reg 0xC2
3002 . #
3003 . # <<Test Ampli A111>>
3004 . #
3005 . # MDP Start the data mission read during 3 minutes
3006 . # -Check the Science Data value = 0 (no count)
3007 . #
3008 . # MDP send Ampli Test Mode ON
3009 . MEA2.Md_Tst_ON
3010 . #
3011 . # < CHECK >
3012 . CHECK MEA2.l1_cmd_exe 0x3900
3013 . CHECK MEA2.l2_cmd_exe 0x0804

```

文 書 番 号	シート No.	版 数
	75	1

```

3014 CHECK MEA2.Status_reg 0xC3
3015 . #
3016 . # -Check the Science Data value (count) after 8 sec
3017 . #
3018 . DH.DR_REC_ON
3019 . #
3020 . # < CHECK >
3021 CHECK DH.DR_REC_ON_OFF =ON
3022 . #
3023 . # << 1 分以上待つ >>
3024 . #
3025 . DH.DR_REC_OFF
3026 . #
3027 . # < CHECK >
3028 CHECK DH.DR_REC_ON_OFF =OFF
3029 . #
3030 . # QL Check-Item Spec Check
3031 . # -----+-----+-----+-----
3032 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
3033 . #
3034 . # =====
3035 . # 4.5. MSA L/M-Mode check
3036 . # =====
3037 . #
3038 . # MSA status が以下となっていることを確認する。
3039 . # なっていなければ待つ（数分程度かかる場合がある）。
3040 . #
3041 . # < CHECK >
3042 CHECK MSA.CPU_FPGA 0x32
3043 CHECK MSA.HV_DAC_VHV3 0
3044 CHECK MSA.HV_DAC_HV1 0
3045 CHECK MSA.HV_DAC_HV2 0
3046 CHECK MSA.HV_DAC_VHV1 0
3047 CHECK MSA.HV_DAC_VHV2 0
3048 CHECK MSA.HV_DAC_VHV4 0
3049 CHECK MSA.HV_DAC_AMP1 0
3050 CHECK MSA.HV_DAC_AMP2 0
3051 CHECK MSA.HV_DAC_AMP3 0
3052 CHECK MSA.ADC_HV_VHV1 0 16
3053 CHECK MSA.ADC_HV_VHV3 0 16
3054 CHECK MSA.ADC_HV_VHV4 0
3055 CHECK MSA.ADC_HV_HV1 0 16
3056 CHECK MSA.ADC_HV_HV2 0 1
3057 CHECK MSA.ADC_CFD_LEFA 0xfff
3058 CHECK MSA.ADC_CFD_LEFB 0xfff
3059 CHECK MSA.ADC_CFD_LEFS 0xfff
3060 CHECK MSA.ADC_CFD_STC 0xfff
3061 CHECK MSA.ADC_CFD_STE 0xfff
3062 CHECK MSA.CPU_AMP_ENA_1 =DIS
3063 CHECK MSA.HV_ERR =ERR
3064 CHECK MSA.HV_ON_OPTICS =OFF
3065 CHECK MSA.HV_ON_BOARD =OFF

```

文 書 番 号	シート No.	版 数
	76	1

```

3066 CHECK MSA. HV_SAFE =ON
3067 CHECK MSA. CPU_ENA_OPTICS =DIS
3068 CHECK MSA. CPU_ENA_BOARD =DIS
3069 CHECK MSA. CPU_SAFE_ENA =DIS
3070 #
3071 #
3072 . # < MSA AMPSW CAL command Check >
3073 #
3074 . # HK rate of 4s
3075 . MSA. HK_RATE 0x01000040
3076 #
3077 . # Discriminator Level Setting
3078 #
3079 . # Ch1
3080 . MSA. CFD_DAC 0x30300B00 # command : 0D0130300B00
3081 . # QL Check-Item Spec Data Check
3082 # -----+-----+-----+-----+
3083 # MPPE-MSA1 ADC_CFD_LEFA 0xB00~0xC00 _____ G N
3084 #
3085 . # Ch2
3086 . MSA. CFD_DAC 0x28280B00 # command : 0D0128280B00
3087 . # QL Check-Item Spec Data Check
3088 # -----+-----+-----+-----+
3089 # MPPE-MSA1 ADC_CFD_LEFB 0xB00~0xC00 _____ G N
3090 #
3091 . # Ch3
3092 . MSA. CFD_DAC 0x24240B00 # command : 0D0124240B00
3093 . # QL Check-Item Spec Data Check
3094 # -----+-----+-----+-----+
3095 # MPPE-MSA1 ADC_CFD_LEFS 0xB00~0xC00 _____ G N
3096 #
3097 . # Ch4
3098 . MSA. CFD_DAC 0x22220B00 # command : 0D0122220B00
3099 . # QL Check-Item Spec Data Check
3100 # -----+-----+-----+-----+
3101 # MPPE-MSA1 ADC_CFD_STC 0xB00~0xC00 _____ G N
3102 #
3103 . # Ch5
3104 . MSA. CFD_DAC 0x21210B00 # command : 0D0121210B00
3105 . # QL Check-Item Spec Data Check
3106 # -----+-----+-----+-----+
3107 # MPPE-MSA1 ADC_CFD_STE 0xB00~0xC00 _____ G N
3108 #
3109 . # Measurement_Enable
3110 . MSA. CPU_AMP_ENA 0x01000100 # command : 000901000100
3111 #
3112 #
3113 . # AMPSW CAL ON
3114 . MSA. CAL_SET 0x8050125310 # command : fa8050125310
3115 #
3116 . # < MSA CPU HV command/telemetry Check >
3117 #

```

```

3118 . # HV_sw_enable
3119 . MSA.HV_ENA 0x0A000000      # command : 07000a000000
3120 . #
3121 . # < CHECK >
3122 . CHECK MSA.CPU_ENA_BOARD      =ENA
3123 . #
3124 . # HV_Board_PwrON
3125 . MSA.HV_ON 0x0A000000      # command : 05000a000000
3126 . #
3127 . # < CHECK >
3128 . CHECK MSA.HV_ON_BOARD      =ON
3129 . CHECK MSA.HV_ERR           =OK
3130 . #
3131 . #####
3132 . # Wait for at least 1 minute
3133 . #####
3134 . #
3135 . # HV_OFF
3136 . MSA.HV_OFF 0x0A000000      # command : 09000a000000
3137 . #
3138 . # < CHECK >
3139 . CHECK MSA.HV_ON_BOARD      =OFF
3140 . #
3141 . # HV_sw_disable
3142 . MSA.HV_DIS 0x0A000000      # command : 08000a000000
3143 . #
3144 . # < CHECK >
3145 . CHECK MSA.CPU_ENA_BOARD      =DIS
3146 . CHECK MSA.HV_DAC_HV1         0
3147 . CHECK MSA.HV_DAC_HV2         0
3148 . CHECK MSA.HV_DAC_VHV1        0
3149 . CHECK MSA.HV_DAC_VHV2        0
3150 . CHECK MSA.HV_DAC_VHV4        0
3151 . CHECK MSA.HV_DAC_AMP1         0
3152 . CHECK MSA.HV_DAC_AMP2         0
3153 . CHECK MSA.HV_DAC_AMP3         0
3154 . CHECK MSA.ADC_HV_VHV1        0 16
3155 . CHECK MSA.ADC_HV_VHV3        0
3156 . CHECK MSA.ADC_HV_VHV4        0
3157 . CHECK MSA.ADC_HV_HV1         0
3158 . CHECK MSA.ADC_HV_HV2         0
3159 . #
3160 . # QL          Check-Item      Spec      Data      Check
3161 . # -----+-----+-----+-----+-----
3162 . # MPPE-MSA1    ADC_HV_VHV2      取得のみ      _____ N/A
3163 . #              ADC_HV_OL        取得のみ      _____ N/A
3164 . #              ADC_HV_AMP1       取得のみ      _____ N/A
3165 . #              ADC_HV_AMP2       取得のみ      _____ N/A
3166 . #              ADC_HV_AMP3       取得のみ      _____ N/A
3167 . #
3168 . DH.DR_REC_ON
3169 . #

```

文 書 番 号	シート No.	版 数
	78	1

```

3170 . # < CHECK >
3171 CHECK DH. DR_REC_ON_OFF          =ON
3172 #
3173 #
3174 . # << 1 分以上待つ >>
3175 #
3176 . #
3177 . DH. DR_REC_OFF
3178 #
3179 . # < CHECK >
3180 CHECK DH. DR_REC_ON_OFF          =OFF
3181 #
3182 . # QL          Check-Item          Spec          Check
3183 # -----+-----+-----+-----+
3184 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
3185 #
3186 # =====
3187 . # 4. 6. MIA L/M-Mode check
3188 # =====
3189 #
3190 . # MIA status が以下となっていることを確認する。
3191 . # なっていない場合は待つ（数分程度かかる場合がある）。
3192 #
3193 . # < CHECK >
3194 CHECK MIA. RMAP_ST1          0x00 0xff
3195 CHECK MIA. EDAC_1BIT_ERR          =NON
3196 CHECK MIA. EDAC_2BITS_ERR          =NON
3197 CHECK MIA. EMER1_ST          =NON
3198 CHECK MIA. EMER2_ST          =NON
3199 CHECK MIA. MODE          0
3200 CHECK MIA. SPIN_ID1          0 3
3201 CHECK MIA. SECT0_PHASE_H          0x00
3202 CHECK MIA. SECT0_PHASE_L          0x80
3203 CHECK MIA. CAL_ON_OFF          =OFF
3204 CHECK MIA. CAL_H          0x0b
3205 CHECK MIA. CAL_L          0x05
3206 CHECK MIA. DT_ED          =DIS
3207 CHECK MIA. DEADTIME          0x3c
3208 CHECK MIA. SERIAL_ED          =DIS
3209 CHECK MIA. SYNC          =SNC
3210 CHECK MIA. ACTIVATE_ED          =DIS
3211 CHECK MIA. ACTIVATE_H          0x00
3212 CHECK MIA. ACTIVATE_L          0x00
3213 CHECK MIA. SHUTDOWN_H          0x00
3214 CHECK MIA. SHUTDOWN_L          0x00
3215 CHECK MIA. PHSCAN_ON_OFF          =OFF
3216 CHECK MIA. PHSCAN_MODE_H          0x80
3217 CHECK MIA. PHSCAN_MODE_L          0x10
3218 CHECK MIA. HVSCAN_ED          =DIS
3219 CHECK MIA. HVSCAN_ON_OFF          =OFF
3220 CHECK MIA. HVSCAN_MODE_H          0x98
3221 CHECK MIA. HVSCAN_MODE_M          0xc0

```

文 書 番 号	シート No.	版 数
	79	1

3222	CHECK MIA. HVSCAN_MODE_L	0x03
3223	CHECK MIA. HK_SEL	0x00
3224	CHECK MIA. SELECT_HK1	0x00 0xff
3225	CHECK MIA. SELECT_HK2	0x00 0xff
3226	CHECK MIA. SVG_ED	=DIS
3227	CHECK MIA. SVG_ON_OFF	=OFF
3228	CHECK MIA. SVG	0 40
3229	CHECK MIA. SVG_TGT	0x80
3230	CHECK MIA. SVG_LVL	0x00
3231	CHECK MIA. SVS_ED	=DIS
3232	CHECK MIA. SVS_ON_OFF	=OFF
3233	CHECK MIA. SVS	-40 0
3234	CHECK MIA. SVS_TGT	0xB8
3235	CHECK MIA. SVS_LVL	0x00
3236	CHECK MIA. MHV_ED	=DIS
3237	CHECK MIA. MHV_ON_OFF	=OFF
3238	CHECK MIA. MHV	-40 0
3239	CHECK MIA. MHV_TGT	0xa8
3240	CHECK MIA. MHV_LVL	0x00
3241	CHECK MIA. MCP_I	-2 2
3242	CHECK MIA. SECT1_8RAM	0x00
3243	CHECK MIA. SECT9_16RAM	0x01
3244	CHECK MIA. SECT17_24RAM	0x01
3245	CHECK MIA. SECT25_32RAM	0x00
3246	CHECK MIA. SECT33_40RAM	0x00
3247	CHECK MIA. SECT41_48RAM	0x02
3248	CHECK MIA. SECT49_56RAM	0x02
3249	CHECK MIA. SECT57_64RAM	0x00
3250	CHECK MIA. SAFETY_ED	=DIS
3251	CHECK MIA. SAFETY_ON_OFF	=ON
3252	CHECK MIA. ASIC_T	10 40
3253	CHECK MIA. IF_T	10 40
3254	CHECK MIA. ASIC_SEL	=NON
3255	CHECK MIA. SEL_THRES	0xff
3256	CHECK MIA. ASIC_CONFIG_ERR	=NON
3257	CHECK MIA. ROM_SAVE_ED	=DIS
3258	CHECK MIA. RAM_LOAD_ED	=DIS
3259	CHECK MIA. ASIC_PARAM1	0x00
3260	CHECK MIA. ASIC_PARAM2	0x00
3261	CHECK MIA. ASIC_PARAM3	0x00
3262	CHECK MIA. ASIC_PARAM4	0x00
3263	CHECK MIA. ASIC_CALCH	0x00
3264	CHECK MIA. ASIC_CALLVL	0xff
3265	CHECK MIA. ASIC_THRES	0x14
3266	CHECK MIA. ASIC_OFCH1H	0x00
3267	CHECK MIA. ASIC_OFCH1L	0x00
3268	CHECK MIA. ASIC_OFCH2H	0x00
3269	CHECK MIA. ASIC_OFCH2L	0x00
3270	CHECK MIA. ASIC_OFCH3H	0x00
3271	CHECK MIA. ASIC_OFCH3L	0x00
3272	CHECK MIA. ASIC_OFCH4H	0x00
3273	CHECK MIA. ASIC_OFCH4L	0x00

文 書 番 号	シート No.	版 数
	80	1

3274	CHECK MIA. ASIC_DCAL	=DIS			
3275	CHECK MIA. ASIC_DIV10	=DIS			
3276	CHECK MIA. ASIC_I	40 60			
3277	CHECK MIA. S_DATA_MODE_SA	=SNP			
3278	CHECK MIA. S_DATA_MODE_SE	=SCI			
3279	CHECK MIA. S_DATA_MODE	0 8			
3280	CHECK MIA. S_ERNG	0xffffffff			
3281	CHECK MIA. S_HK_MON_MHV	=DIS			
3282	CHECK MIA. S_MHV_LIM_L	0x00			
3283	CHECK MIA. S_MHV_LIM_U	0xff			
3284	CHECK MIA. S_HK_MON_SVS	=DIS			
3285	CHECK MIA. S_SVS_LIM_L	0x00			
3286	CHECK MIA. S_SVS_LIM_U	0xff			
3287	CHECK MIA. S_HK_MON_SVG	=DIS			
3288	CHECK MIA. S_SVG_LIM_L	0x00			
3289	CHECK MIA. S_SVG_LIM_U	0xff			
3290	CHECK MIA. S_HK_MON_MCP_I	=DIS			
3291	CHECK MIA. S_MCP_I_LIM_L	0x00			
3292	CHECK MIA. S_MCP_I_LIM_U	0xff			
3293	CHECK MIA. S_HK_MON_ASIC_T	=DIS			
3294	CHECK MIA. S_ASIC_T_LIM_L	0x00			
3295	CHECK MIA. S_ASIC_T_LIM_U	0xff			
3296	CHECK MIA. S_HK_MON_IF_T	=DIS			
3297	CHECK MIA. S_IF_T_LIM_L	0x00			
3298	CHECK MIA. S_IF_T_LIM_U	0xff			
3299	CHECK MIA. S_HK_MON_ASIC_SEL	=DIS			
3300	CHECK MIA. CMD_ANS1	0			
3301	CHECK MIA. CMD_ANS2	0			
3302	CHECK MIA. CMD_ANS3	0			
3303	CHECK MIA. CMD_ANS4	0			
3304	CHECK MIA. CMD_CNTR	0			
3305	CHECK MIA. ECMD_CNTR	0			
3306	CHECK MIA. S_CMD_CTR	0			
3307	CHECK MIA. S_CNT1	0x00cae3			
3308	#				
3309	# QL	Check-Item	Spec	Data	Check
3310	#				
3311	# MPPE-MIA2	MIA. SVG	取得のみ *1		N/A
3312	#	MIA. SVS	取得のみ *1		N/A
3313	#	MIA. MHV	取得のみ *1		N/A
3314	#	MIA. MCP_I	取得のみ *1		N/A
3315	#	MIA. ASIC_T	取得のみ *1		N/A
3316	#	MIA. ASIC_I	取得のみ *1		N/A
3317	#	MIA. IF_T	取得のみ *1		N/A
3318	#				
3319	#	*1 上記 CHECK にて Spec 範囲内である事を確認済。			
3320	#				
3321	#	<高圧 ACTIVATE>			
3322	#				
3323	#	< CHECK >			
3324	CHECK MIA. SAFETY_ED	=DIS			
3325	CHECK MIA. SAFETY_ON_OFF	=ON			

文 書 番 号	シ-ト No.	版 数
	81	1

```

3326 . #
3327 . MIA. ACTIVAT_ENA
3328 . #
3329 . # < CHECK >
3330 . CHECK MIA. ACTIVATE_ED =ENA
3331 . #
3332 . MIA. ACTIVATE 0x0200
3333 . # < 1 分程度待つ:HVPS SVG/SVS/MHV の TGT=LVL になるまで>
3334 . #
3335 . # < CHECK >
3336 . CHECK MIA. RMAP_ST1 0x00 0xff
3337 . CHECK MIA. EDAC_1BIT_ERR =NON
3338 . CHECK MIA. EDAC_2BITS_ERR =NON
3339 . CHECK MIA. EMER1_ST =NON
3340 . CHECK MIA. EMER2_ST =NON
3341 . CHECK MIA. MODE 1
3342 . CHECK MIA. SPIN_ID1 0 3
3343 . CHECK MIA. SECT0_PHASE_H 0x00
3344 . CHECK MIA. SECT0_PHASE_L 0x80
3345 . CHECK MIA. CAL_ON_OFF =OFF
3346 . CHECK MIA. CAL_H 0x0b
3347 . CHECK MIA. CAL_L 0x05
3348 . CHECK MIA. DT_ED =DIS
3349 . CHECK MIA. DEADTIME 0x3c
3350 . CHECK MIA. SERIAL_ED =DIS
3351 . CHECK MIA. SYNC =SNC
3352 . CHECK MIA. ACTIVATE_ED =DIS
3353 . CHECK MIA. ACTIVATE_H 0x02
3354 . CHECK MIA. ACTIVATE_L 0x00
3355 . CHECK MIA. SHUTDOWN_H 0x00
3356 . CHECK MIA. SHUTDOWN_L 0x00
3357 . CHECK MIA. PHSCAN_ON_OFF =OFF
3358 . CHECK MIA. PHSCAN_MODE_H 0x80
3359 . CHECK MIA. PHSCAN_MODE_L 0x10
3360 . CHECK MIA. HVSCAN_ED =DIS
3361 . CHECK MIA. HVSCAN_ON_OFF =OFF
3362 . CHECK MIA. HVSCAN_MODE_H 0x98
3363 . CHECK MIA. HVSCAN_MODE_M 0xc0
3364 . CHECK MIA. HVSCAN_MODE_L 0x03
3365 . CHECK MIA. HK_SEL 0x00
3366 . CHECK MIA. SELECT_HK1 0x00 0xff
3367 . CHECK MIA. SELECT_HK2 0x00 0xff
3368 . CHECK MIA. SVG_ED =DIS
3369 . CHECK MIA. SVG_ON_OFF =ON
3370 . CHECK MIA. SVG 0 20
3371 . CHECK MIA. SVG_TGT 0x80
3372 . CHECK MIA. SVG_LVL 0x80
3373 . CHECK MIA. SVS_ED =DIS
3374 . CHECK MIA. SVS_ON_OFF =ON
3375 . CHECK MIA. SVS -20 0
3376 . CHECK MIA. SVS_TGT 0xB8
3377 . CHECK MIA. SVS_LVL 0xB8

```

文 書 番 号	シート No.	版 数
	82	1

3378	CHECK MIA. MHV_ED	=DIS
3379	CHECK MIA. MHV_ON_OFF	=ON
3380	CHECK MIA. MHV	-20 0
3381	CHECK MIA. MHV_TGT	0xa8
3382	CHECK MIA. MHV_LVL	0xa8
3383	CHECK MIA. MCP_I	-2 2
3384	CHECK MIA. SECT1_8RAM	0x03
3385	CHECK MIA. SECT9_16RAM	0x03
3386	CHECK MIA. SECT17_24RAM	0x03
3387	CHECK MIA. SECT25_32RAM	0x03
3388	CHECK MIA. SECT33_40RAM	0x03
3389	CHECK MIA. SECT41_48RAM	0x03
3390	CHECK MIA. SECT49_56RAM	0x03
3391	CHECK MIA. SECT57_64RAM	0x03
3392	CHECK MIA. SAFETY_ED	=DIS
3393	CHECK MIA. SAFETY_ON_OFF	=ON
3394	CHECK MIA. ASIC_T	10 40
3395	CHECK MIA. IF_T	10 40
3396	CHECK MIA. CMD_ANS1	0x54
3397	CHECK MIA. CMD_ANS2	0x00
3398	CHECK MIA. CMD_ANS3	0x02
3399	CHECK MIA. CMD_ANS4	0x00
3400	CHECK MIA. CMD_CNTR	2
3401	CHECK MIA. ECMD_CNTR	0
3402	CHECK MIA. S_CMD_CTR	0
3403	CHECK MIA. S_CNT1	0x00cae3
3404	CHECK MIA. ASIC_SEL	=NON
3405	CHECK MIA. SEL_THRES	0xff
3406	CHECK MIA. ASIC_CONFIG_ERR	=NON
3407	CHECK MIA. ROM_SAVE_ED	=DIS
3408	CHECK MIA. RAM_LOAD_ED	=DIS
3409	CHECK MIA. ASIC_PARAM1	0x00
3410	CHECK MIA. ASIC_PARAM2	0x00
3411	CHECK MIA. ASIC_PARAM3	0x00
3412	CHECK MIA. ASIC_PARAM4	0x00
3413	CHECK MIA. ASIC_CALCH	0x00
3414	CHECK MIA. ASIC_CALLVL	0xff
3415	CHECK MIA. ASIC_THRES	0x14
3416	CHECK MIA. ASIC_OFCH1H	0x00
3417	CHECK MIA. ASIC_OFCH1L	0x00
3418	CHECK MIA. ASIC_OFCH2H	0x00
3419	CHECK MIA. ASIC_OFCH2L	0x00
3420	CHECK MIA. ASIC_OFCH3H	0x00
3421	CHECK MIA. ASIC_OFCH3L	0x00
3422	CHECK MIA. ASIC_OFCH4H	0x00
3423	CHECK MIA. ASIC_OFCH4L	0x00
3424	CHECK MIA. ASIC_DCAL	=DIS
3425	CHECK MIA. ASIC_DIV10	=DIS
3426	CHECK MIA. S_DATA_MODE_SA	=SNP
3427	CHECK MIA. S_DATA_MODE_SE	=SCI
3428	CHECK MIA. S_DATA_MODE	0 7
3429	CHECK MIA. S_ERNG	0xffffffff

文 書 番 号	シート No.	版 数
	83	1

```

3430 CHECK MIA. S_HK_MON_MHV =DIS
3431 CHECK MIA. S_MHV_LIM_L 0x00
3432 CHECK MIA. S_MHV_LIM_U 0xff
3433 CHECK MIA. S_HK_MON_SVS =DIS
3434 CHECK MIA. S_SVS_LIM_L 0x00
3435 CHECK MIA. S_SVS_LIM_U 0xff
3436 CHECK MIA. S_HK_MON_SVG =DIS
3437 CHECK MIA. S_SVG_LIM_L 0x00
3438 CHECK MIA. S_SVG_LIM_U 0xff
3439 CHECK MIA. S_HK_MON_MCP_I =DIS
3440 CHECK MIA. S_MCP_I_LIM_L 0x00
3441 CHECK MIA. S_MCP_I_LIM_U 0xff
3442 CHECK MIA. S_HK_MON_ASIC_T =DIS
3443 CHECK MIA. S_ASIC_T_LIM_L 0x00
3444 CHECK MIA. S_ASIC_T_LIM_U 0xff
3445 CHECK MIA. S_HK_MON_IFT =DIS
3446 CHECK MIA. S_IFT_LIM_L 0x00
3447 CHECK MIA. S_IFT_LIM_U 0xff
3448 CHECK MIA. S_HK_MON_ASIC_SEL =DIS
3449 #
3450 . # QL Check-Item Spec Data Check
3451 . # -----+-----+-----+-----+-----
3452 . # MPPE-MIA2 MIA. SVG (SVG MON) 取得のみ *1 _____ N/A
3453 . # MIA. SVS (SVS MON) 取得のみ *1 _____ N/A
3454 . # MIA. MHV (MHV MON) 取得のみ *1 _____ N/A
3455 . # MIA. MCP_I (MHV MCP_I) 取得のみ *1 _____ N/A
3456 . # MIA. ASIC-T 取得のみ *1 _____ N/A
3457 . # MIA. ASIC_I 取得のみ *1 _____ N/A
3458 . # MIA. IF-T 取得のみ *1 _____ N/A
3459 . #
3460 . # *1 上記 CHECK にて Spec 範囲内である事を確認済。
3461 . #
3462 . MD1U. ERRCLR
3463 . MD2U. ERRCLR
3464 . DH. DR_REC_ON
3465 . #
3466 . # < CHECK >
3467 CHECK DH. DR_REC_ON_OFF =ON
3468 . #
3469 . # <<<< 固定データ出力・本物データ出力固定カウント入力 >>>>>
3470 . MD1U. MIA_CNT1_SET 0x00 0x01 0x01 0xff 0xff 0xff 0x01
3471 . #
3472 . # < CHECK >
3473 CHECK MIA. S_CNT1 0x02CAE3
3474 . #
3475 . # MIA mission data 記録のため 90 秒待
3476 . #
3477 MD1U. MIA_ROM_READ
3478 . #
3479 . # MIA mission data 記録のため 90 秒待
3480 . #
3481 . # <<<< 本物データ出力本物データ入力に戻す >>>>>

```

文 書 番 号	シート No.	版 数
	84	1

```

3482 . MD1U.MIA_CNT1_SET 0x00 0x01 0x00 0x00 0x00 0x00 0x00
3483 . #
3484 . # < CHECK >
3485 . CHECK MIA.S_CNT1 0x01CAE3
3486 . #
3487 . # MIA mission data 記録のため30秒待
3488 . #
3489 . #
3490 . # <CAL 1.953kHz>
3491 . MIA.CAL_MODE_SET 0x0B05
3492 . MIA.CAL_ON
3493 . #
3494 . # < CHECK >
3495 . CHECK MIA.CAL_ON_OFF =ON
3496 . CHECK MIA.CAL_H 0x0B
3497 . CHECK MIA.CAL_L 0x05
3498 . CHECK MIA.ASIC_CALCH 0x00 0x3F
3499 . #
3500 . # MIA mission data 記録のため5分待ち(CAL_CH 0x00~0x3Fの間データ REC)
3501 . #
3502 . # ***** MIA MODE 0 *****
3503 . MIA.MODE_SET 0x0000
3504 . #
3505 . # < CHECK >
3506 . CHECK MIA.MODE 0
3507 . #
3508 . # MIA mission data 記録のため4分待ち
3509 . #
3510 . MIA.CAL_OFF
3511 . #
3512 . # < CHECK >
3513 . CHECK MIA.CAL_ON_OFF =OFF
3514 . #
3515 . MIA.CAL_ON
3516 . #
3517 . # < CHECK >
3518 . CHECK MIA.CAL_ON_OFF =ON
3519 . #
3520 . # ***** MIA MODE 2 *****
3521 . MIA.MODE_SET 0x0200
3522 . #
3523 . # < CHECK >
3524 . CHECK MIA.MODE 2
3525 . #
3526 . # MIA mission data 記録のため5分待ち(CAL_CH 0x00~0x3Fの間データ REC)
3527 . #
3528 . # ***** MIA MODE 1 *****
3529 . MIA.MODE_SET 0x0100
3530 . #
3531 . # < CHECK >
3532 . CHECK MIA.MODE 1
3533 . #

```

文 書 番 号	シート No.	版 数
	85	1

```

3534 . # MDP MIA SW CHECK
3535 . MD1U.MIA_DATA_MODE 0x00 0x01
3536 . #
3537 . # < CHECK >
3538 . CHECK MIA.S_DATA_MODE 1
3539 . #
3540 . MIA.CAL_OFF
3541 . #
3542 . # < CHECK >
3543 . CHECK MIA.CAL_ON_OFF =OFF
3544 . #
3545 . DH.DR_REC_OFF
3546 . #
3547 . # < CHECK >
3548 . CHECK DH.DR_REC_ON_OFF =OFF
3549 . #
3550 . # QL Check-Item Spec Check
3551 . # -----+-----+-----+-----
3552 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
3553 . #
3554 . # =====
3555 . # 4.7. HEP-E L/M-Mode check
3556 . # =====
3557 . #
3558 . # <Power ON 初期ステータス確認>
3559 . #
3560 . # < CHECK >
3561 . CHECK HEPE.TEMP_MONITOR1 -0.459 43.209
3562 . CHECK HEPE.TEMP_MONITOR2 3.425 48.380
3563 . CHECK HEPE.ELE_ION_FL =ELE
3564 . CHECK HEPE.STBY_OBS_F =SBY
3565 . CHECK HEPE.HV_ELE_CTL =DIS
3566 . CHECK HEPE.HE_ELE_DAC =OFF
3567 . LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3568 . #
3569 . # QL Check-Item Spec Check
3570 . # -----+-----+-----+-----
3571 . # MPPE-HEPE1 HV_MONITOR1 0x0000 or 0x0100 G N
3572 . #
3573 . # ASIC 内部レジスタ 1~16 及び ASIC1~9 Disable ビット設定
3574 . MD1U.HEPE_ASIC_INIT
3575 . #
3576 . # < CHECK >
3577 . SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3578 . WAIT_SEC 2
3579 . LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3580 . #
3581 . # ASIC Gr1 レジスタ書き込みコマンド
3582 . HEPE.REG_WR_GR1
3583 . #
3584 . # < CHECK >
3585 . SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}

```

文 書 番 号	シート No.	版 数
	86	1

```

3586 WAIT_SEC 2
3587 LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3588 #
3589 #
3590 . # 4sec 待機 (処理時間)
3591 #
3592 #
3593 . # ASIC Gr2 レジスタ書き込みコマンド
3594 . HEPE.REG_WR_GR2
3595 #
3596 . # < CHECK >
3597 SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3598 WAIT_SEC 2
3599 LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 4 ) & 0xF
3600 #
3601 . # 4sec 待機 (処理時間)
3602 #
3603 . # PEAK TIME 設定
3604 . HEPE.PEAK 0x0003
3605 #
3606 . # VFP_Gr1 設定
3607 . HEPE.ASIC_GR1_VFP 0x0009
3608 #
3609 . # VFP_Gr2 設定
3610 . HEPE.ASIC_GR2_VFP 0x0009
3611 #
3612 . # TH_BACK 設定
3613 . HEPE.TH_BACK 0x3FFE
3614 #
3615 . # < CHECK >
3616 SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3617 WAIT_SEC 2
3618 LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3619 CHECK HEPE.PEAK_TIME_VAL 0x0003
3620 CHECK HEPE.ASIC_GR1_VFP_VAL 0x0009
3621 CHECK HEPE.ASIC_GR2_VFP_VAL 0x0009
3622 CHECK HEPE.TH_BACK_VAL 0x3FFE
3623 #
3624 . # LOAD コマンド送信
3625 . HEPE.LOAD_START
3626 #
3627 . # < CHECK >
3628 SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3629 WAIT_SEC 2
3630 LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 7 ) & 0xF
3631 #
3632 . # 温度補正テーブル初期化
3633 . MD1U.HEPE_TEMP_INIT
3634 #
3635 . # 設定終了まで 30 秒待ち
3636 . # SI にてステータス確認実施
3637 #

```

文 書 番 号	シート No.	版 数
	87	1

```

3638 . # 外部温度モニタ設定レジスタ設定
3639 . HEPE.TEMP_MONITOR_SEL 0x0000
3640 . #
3641 . # < CHECK >
3642 . SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3643 . WAIT_SEC 2
3644 . LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3645 . #
3646 . # 温度補正_内部/外部コマンド送信(外部切替)
3647 . HEPE.TEMP_CORRECT_EXT
3648 . #
3649 . # < CHECK >
3650 . SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3651 . WAIT_SEC 2
3652 . LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3653 . CHECK HEPE.TEMP_CORRECT =EXT
3654 . #
3655 . # 4sec 待機 (処理時間)
3656 . #
3657 . # 温度補正_内部/外部コマンド送信(内部切替)
3658 . HEPE.TEMP_CORRECT_INT
3659 . #
3660 . # < CHECK >
3661 . SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3662 . CHECK HEPE.TEMP_CORRECT =INT
3663 . #
3664 . # ELE-HV 電源 ON コマンド受信 ENA/DIS コマンド(HV-ELE-CTL_ENA/DIS=ENA)
3665 . HEPE.HV_ELE_CTL_ENA
3666 . #
3667 . # < CHECK >
3668 . CHECK HEPE.HV_ELE_CTL =ENA
3669 . #
3670 . # ELE-HV REF 電源 ON/OFF スイッチ切替コマンド(ON/OFF=ON)
3671 . HEPE.HV_ELE_REF_ON
3672 . #
3673 . # < CHECK >
3674 . CHECK HEPE.HE_ELE_DAC =ON
3675 . #
3676 . # ELE-HV 出力電圧設定(DAC 設定値=80h 設定)
3677 . HEPE.HV_ELE_DAC 0x0080
3678 . #
3679 . # 35sec 後に以下を確認 (処理時間)
3680 . #
3681 . # < CHECK >
3682 . CHECK HEPE.HV_ELE_DAC_VAL 0x0080
3683 . CHECK HEPE.HV_MONITOR1 95 105
3684 . LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3685 . #
3686 . DH.DR_REC_ON
3687 . #
3688 . # < CHECK >
3689 . CHECK DH.DR_REC_ON_OFF =ON

```

文 書 番 号	シート No.	版 数
	88	1

```

3690  #
3691  . # FIFO_RESET コマンド送信
3692  . HEPE. FIFO_RESET
3693  #
3694  . # < CHECK >
3695  SYSTEM test {HEPE. CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3696  WAIT_SEC 2
3697  LET HEPE_C_BUF_CNT = ({HEPE. CMD_BUF_WR_CNT} + 1 ) & 0xF
3698  #
3699  . # READ_BACK コマンド送信
3700  . HEPE. READ_BACK
3701  #
3702  . # < CHECK >
3703  SYSTEM test {HEPE. CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3704  WAIT_SEC 2
3705  LET HEPE_C_BUF_CNT = ({HEPE. CMD_BUF_WR_CNT} + 1 ) & 0xF
3706  #
3707  . # 4sec 待機(処理時間)
3708  #
3709  . # FIFO_RESET コマンド送信
3710  . HEPE. FIFO_RESET
3711  #
3712  . # < CHECK >
3713  SYSTEM test {HEPE. CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3714  #
3715  . # CAL_DAC 設定
3716  . HEPE. CAL_DAC 0x0010
3717  #
3718  . # < CHECK >
3719  CHECK HEPE. CAL_DAC_VAL 0x0010
3720  #
3721  . # CAL_ASIC_ID 設定 Gr1
3722  . HEPE. CAL_ASIC_ID 0x0002
3723  #
3724  . # < CHECK >
3725  CHECK HEPE. CAL_ASIC_ID_VAL 0x0002
3726  LET HEPE_C_BUF_CNT = ({HEPE. CMD_BUF_WR_CNT} + 1 ) & 0xF
3727  #
3728  . # 60sec 待機 (処理時間)
3729  #
3730  . # FIFO_RESET コマンド送信
3731  . HEPE. FIFO_RESET
3732  #
3733  . # < CHECK >
3734  SYSTEM test {HEPE. CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3735  #
3736  . # CAL_ASIC_ID 設定 Gr1
3737  . HEPE. CAL_ASIC_ID 0x0004
3738  #
3739  . # < CHECK >
3740  CHECK HEPE. CAL_ASIC_ID_VAL 0x0004
3741  LET HEPE_C_BUF_CNT = ({HEPE. CMD_BUF_WR_CNT} + 1 ) & 0xF

```

文 書 番 号	シート No.	版 数
	89	1

```

3742  #
3743  . # 60sec 待機 (処理時間)
3744  #
3745  . # FIFO_RESET コマンド送信
3746  . HEPE.FIFO_RESET
3747  #
3748  . # < CHECK >
3749  SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3750  #
3751  . # CAL_ASIC_ID 設定 Gr2
3752  . HEPE.CAL_ASIC_ID 0x0006
3753  #
3754  . # < CHECK >
3755  CHECK HEPE.CAL_ASIC_ID_VAL 0x0006
3756  LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3757  #
3758  . # 60sec 待機 (処理時間)
3759  #
3760  . # FIFO_RESET コマンド送信
3761  . HEPE.FIFO_RESET
3762  #
3763  . # < CHECK >
3764  SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3765  WAIT_SEC 2
3766  LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3767  #
3768  . # SSD モード切替コマンド送信 (SSD/OFF=SSD)
3769  . HEPE.SSD_ON
3770  #
3771  . # < CHECK >
3772  SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3773  #
3774  . # 測定モード切替コマンド送信 (STBY/START=START)
3775  . HEPE.OBS_START
3776  #
3777  . # < CHECK >
3778  CHECK HEPE.SSD_OFF_FL =OBS
3779  CHECK HEPE.STBY_OBS_F =OBS
3780  #
3781  #
3782  . # 300sec 待機 (処理時間)
3783  #
3784  . #
3785  . HEPE.HV_ELE_REF_OFF
3786  . DH.DR_REC_OFF
3787  #
3788  . # < CHECK >
3789  CHECK DH.DR_REC_ON_OFF =OFF
3790  #
3791  . MD1U.TLMH_TRG_SET 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00
3792  . MD2U.TLMH_TRG_SET 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00
3793  #

```

文 書 番 号	シート No.	版 数
	90	1

3794	. # QL	Check-Item	Spec	Check
3795	#	-----+-----+-----+-----		
3796	#	CMD 出力装置 Message	STATUS CHECK 結果 OK	G N
3797	#			
3798	#	=====		
3799	. #	4.8. HEP-I L/M-Mode check		
3800	#	=====		
3801	#			
3802	. #	＜初期ステータス確認＞		
3803	#			
3804	. #	＜ CHECK ＞		
3805		CHECK HEPI.TEMP_MONITOR1	3.526 48.013	
3806		CHECK HEPI.TEMP_MONITOR2	1.060 45.286	
3807		CHECK HEPI.ELE_ION_FL	=ION	
3808		CHECK HEPI.STBY_OBS_F	=SBY	
3809		CHECK HEPI.HV_ION_H_CTL	=DIS	
3810		CHECK HEPI.HV_ION_L_CTL	=DIS	
3811		CHECK HEPI.HE_ION_H_DAC	=OFF	
3812		CHECK HEPI.HE_ION_DAC	=OFF	
3813		LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1) & 0xF		
3814	#			
3815	. # QL	Check-Item	Spec	Check
3816	#	-----+-----+-----+-----		
3817	#	MPPE-HEPI1 HV_MONITOR1	0x0000 or 0x0100	G N
3818	#	HV_MONITOR2	0x0000 or 0x0100	G N
3819	#	HV_MONITOR3	0x0000 or 0x0100	G N
3820	. #			
3821	. #	ASIC 内部 Resister1～16 及び ASIC1～9 Disable-Bit 設定		
3822	. MD1U.	HEPI_ASIC_INIT		
3823	#			
3824	. #	＜ CHECK ＞		
3825		SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}		
3826		WAIT_SEC 2		
3827		LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1) & 0xF		
3828	#			
3829	. #	ASIC Gr1 レジスタ書込みコマンド		
3830	. HEPI.	REG_WR_GR1		
3831	#			
3832	. #	＜ CHECK ＞		
3833		SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}		
3834		WAIT_SEC 2		
3835		LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1) & 0xF		
3836	#			
3837	. #	WAIT 4sec (Writing ASIC Gr1 register)		
3838	#			
3839	. #	ASIC Gr2 レジスタ書込みコマンド		
3840	. HEPI.	REG_WR_GR2		
3841	#			
3842	. #	＜ CHECK ＞		
3843		SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}		
3844	#			
3845	. #	WAIT 4sec (Writing ASIC Gr2 register)		

文 書 番 号	シート No.	版 数
	91	1

```

3846  #
3847  . # PEAK TIME Setting
3848  . HEPI.PEAK 0x0003
3849  #
3850  . # VFP_Gr1 設定
3851  . HEPI.ASIC_GR1_VFP 0x0009
3852  #
3853  . # VFP_Gr2 設定
3854  . HEPI.ASIC_GR2_VFP 0x0009
3855  #
3856  . # TH_ANTI 設定
3857  . HEPI.TH_ANTI 0x0001
3858  #
3859  . # TH_BACK 設定
3860  . HEPI.TH_BACK 0x3FFE
3861  #
3862  . # < CHECK >
3863  CHECK HEPI.PEAK_TIME_VAL          0x0003
3864  CHECK HEPI.ASIC_GR1_VFP_VAL        0x0009
3865  CHECK HEPI.ASIC_GR2_VFP_VAL        0x0009
3866  CHECK HEPI.TH_ANTI_VAL              0x0001
3867  CHECK HEPI.TH_BACK_VAL              0x3FFE
3868  LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3869  #
3870  . # LOAD コマンド送信
3871  . HEPI.LOAD_START
3872  #
3873  . # < CHECK >
3874  SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
3875  WAIT_SEC 2
3876  LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 7 ) & 0xF
3877  #
3878  . # 温度補正テーブル設定
3879  . MD1U.HEPI_TEMP_INIT
3880  #
3881  #
3882  . # 設定終了まで 30 秒待ち
3883  . # SI にてステータス確認実施
3884  #
3885  #
3886  . # 外部温度モニタ設定レジスタ設定
3887  . HEPI.TEMP_MONITOR_SEL 0x00BE
3888  #
3889  . # < CHECK >
3890  SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
3891  WAIT_SEC 2
3892  LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3893  #
3894  . # 温度補正_内部/外部コマンド送信(外部切替)
3895  . HEPI.TEMP_CORRECT_EXT
3896  #
3897  . # < CHECK >

```

文 書 番 号	シート No.	版 数
	92	1

```

3898 SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
3899 WAIT_SEC 2
3900 LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3901 CHECK HEPI.TEMP_CORRECT =EXT
3902 #
3903 . # WAIT 4sec (処理時間)
3904 #
3905 . # 温度補正_内部/外部コマンド送信(内部切替)
3906 . HEPI.TEMP_CORRECT_INT
3907 #
3908 . # < CHECK >
3909 SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
3910 WAIT_SEC 2
3911 LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3912 CHECK HEPI.TEMP_CORRECT =INT
3913 #
3914 . # ION-HV-LOW 電源 ON コマンド受信 ENA/DIS コマンド (HV-ION-L-CTL_ENA/DIS=ENA)
3915 . HEPI.HV_ION_L_CTL_ENA
3916 #
3917 . # < CHECK >
3918 CHECK HEPI.HV_ION_L_CTL =ENA
3919 #
3920 . # ION-HV-LOW REF 電源 ON/OFF スイッチ切替コマンド (ON/OFF=ON)
3921 . HEPI.HV_ION_REF_ON
3922 #
3923 . # < CHECK >
3924 CHECK HEPI.HE_ION_DAC =ON
3925 #
3926 . # ION-HV-ch1 出力電圧設定 (Ch1 DAC 設定値=80h 設定)
3927 . HEPI.HV_ION_DAC_CH1 0x0080
3928 #
3929 . # 45sec 待機後に以下を確認
3930 #
3931 . # < CHECK >
3932 CHECK HEPI.HV_ION_DAC1_VAL 0x0080
3933 CHECK HEPI.HV_MONITOR1 50 90
3934 #
3935 #
3936 . # ION-HV-HIGH 電源 ON コマンド受信 ENA/DIS コマンド (HV-ION-H-CTL_ENA/DIS=ENA)
3937 . HEPI.HV_ION_H_CTL_ENA
3938 #
3939 . # < CHECK >
3940 CHECK HEPI.HV_ION_H_CTL =ENA
3941 #
3942 . # ION-HV-HIGH REF 電源 ON/OFF SW 切替コマンド (ON/OFF=ON)
3943 . HEPI.HV_ION_H_REF_ON
3944 #
3945 . # < CHECK >
3946 CHECK HEPI.HE_ION_H_DAC =ON
3947 #
3948 . # ION-HV-ch2 出力電圧設定 (Ch2 DAC 設定値=01h 設定)
3949 . HEPI.HV_ION_DAC_CH2 0x0001

```

文 書 番 号	シート No.	版 数
	93	1

```

3950  #
3951  . # < CHECK >
3952  CHECK HEPI.HV_ION_DAC2_VAL          0x0001
3953  #
3954  . # WAIT 4sec (処理時間)
3955  #
3956  . # < CHECK >
3957  CHECK HEPI.HV_MONITOR2              0 14
3958  #
3959  . # ION-HV-ch3 出力電圧設定 (Ch3 DAC 設定値=01h 設定)
3960  . HEPI.HV_ION_DAC_CH3 0x0001
3961  #
3962  . # < CHECK >
3963  CHECK HEPI.HV_ION_DAC3_VAL          0x0001
3964  LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3965  #
3966  . # WAIT 4sec (処理時間)
3967  #
3968  . # < CHECK >
3969  CHECK HEPI.HV_MONITOR3              0 35
3970  #
3971  . # FIFO_RESET コマンド送信
3972  . HEPI.FIFO_RESET
3973  #
3974  . # < CHECK >
3975  SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
3976  WAIT_SEC 2
3977  LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3978  . #
3979  . DH.DR_REC_ON
3980  #
3981  . # < CHECK >
3982  CHECK DH.DR_REC_ON_OFF              =ON
3983  #
3984  . # READ_BACK コマンド送信
3985  . HEPI.READ_BACK
3986  #
3987  . # < CHECK >
3988  SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
3989  #
3990  . # CAL_DAC 設定
3991  . HEPI.CAL_DAC 0x0010
3992  . #
3993  . # < CHECK >
3994  CHECK HEPI.CAL_DAC_VAL              0x0010
3995  #
3996  . # CAL_ASIC_ID 設定 Gr1
3997  . HEPI.CAL_ASIC_ID 0x0001
3998  . #
3999  . # < CHECK >
4000  CHECK HEPI.CAL_ASIC_ID_VAL          0x0001
4001  LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF

```

文 書 番 号	シート No.	版 数
	94	1

```

4002  #
4003  . # WAIT 60sec (Calibration Gr1)
4004  #
4005  . # FIFO_RESET コマンド送信
4006  . HEPI.FIFO_RESET
4007  #
4008  . # < CHECK >
4009  SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4010  WAIT_SEC 2
4011  LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4012  #
4013  . # CAL_ASIC_ID 設定 Gr2
4014  . HEPI.CAL_ASIC_ID 0x0003
4015  #
4016  . # < CHECK >
4017  CHECK HEPI.CAL_ASIC_ID_VAL 0x0003
4018  LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4019  #
4020  . # WAIT 60sec (Calibration Gr2)
4021  #
4022  . # FIFO_RESET コマンド送信
4023  . HEPI.FIFO_RESET
4024  #
4025  . # < CHECK >
4026  SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4027  WAIT_SEC 2
4028  LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4029  #
4030  . # SSD モード切替コマンド送信 (SSD/OFF=SSD)
4031  . HEPI.SSD_ON
4032  #
4033  . # < CHECK >
4034  SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4035  #
4036  . # 測定モード切替コマンド送信 (STBY/START=START)
4037  . HEPI.OBS_START
4038  #
4039  . # < CHECK >
4040  CHECK HEPI.OBS_STBY_START_RCV =OBS
4041  CHECK HEPI.SSD_OFF_FL =OBS
4042  CHECK HEPI.STBY_OBS_F =OBS
4043  #
4044  . # 300sec 待機 (処理時間)
4045  #
4046  . # SSD モード切替コマンド送信 (SSD/OFF=OFF)
4047  . HEPI.SSD_OFF
4048  #
4049  . # < CHECK >
4050  CHECK HEPI.SSD_OFF_FL =OFF
4051  CHECK HEPI.STBY_OBS_F =SBY
4052  #
4053  . # TOF モード切替コマンド送信 (TOF/OFF=TOF)

```

文 書 番 号	シート No.	版 数
	95	1

```

4054 . HEPI.TOF_ON
4055 . #
4056 . # < CHECK >
4057 . CHECK HEPI.TOF_OFF_FL =OBS
4058 . CHECK HEPI.STBY_OBS_F =OBS
4059 . LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4060 . #
4061 . # TOF_CODE, PAR 設定 1
4062 . HEPI.TOF_CMD 0x0008 0x0001 0x0000 0x0000
4063 . #
4064 . # < CHECK >
4065 . SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4066 . WAIT_SEC 2
4067 . LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4068 . #
4069 . # TOF_COM コマンド送信 1
4070 . HEPI.TOF_COM
4071 . #
4072 . # < CHECK >
4073 . SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4074 . WAIT_SEC 2
4075 . LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4076 . #
4077 . # TOF_CODE, PAR 設定 2
4078 . HEPI.TOF_CMD 0x0009 0x0001 0x0000 0x0000
4079 . #
4080 . # < CHECK >
4081 . SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4082 . WAIT_SEC 2
4083 . LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4084 . #
4085 . # TOF_COM コマンド送信 2
4086 . HEPI.TOF_COM
4087 . #
4088 . # < CHECK >
4089 . SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4090 . WAIT_SEC 2
4091 . LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4092 . #
4093 . # TOF_CODE, PAR 設定 3
4094 . HEPI.TOF_CMD 0x000A 0x0001 0x0001 0x0001
4095 . #
4096 . # < CHECK >
4097 . SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4098 . WAIT_SEC 2
4099 . LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4100 . #
4101 . # TOF_COM コマンド送信 3
4102 . HEPI.TOF_COM
4103 . #
4104 . # < CHECK >
4105 . SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}

```

```

4106 . #
4107 . # 4sec 待機（処理時間）
4108 . #
4109 . # SSD モード切替コマンド送信 (SSD/OFF=OFF)
4110 . HEPI.SSD_OFF
4111 . #
4112 . # < CHECK >
4113 . CHECK HEPI.SSD_OFF_FL =OFF
4114 . #
4115 . # TOF モード切替コマンド送信 (TOF/OFF=TOF)
4116 . HEPI.TOF_ON
4117 . #
4118 . # < CHECK >
4119 . CHECK HEPI.TOF_OFF_FL =OBS
4120 . CHECK HEPI.STBY_OBS_F =OBS
4121 . LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4122 . #
4123 . # FIFO_RESET コマンド送信
4124 . HEPI.FIFO_RESET
4125 . #
4126 . # < CHECK >
4127 . SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4128 . #
4129 . # 120sec 待機（処理時間）
4130 . #
4131 . # SSD モード切替コマンド送信 (SSD/OFF=SSD)
4132 . HEPI.SSD_ON
4133 . #
4134 . # < CHECK >
4135 . CHECK HEPI.SSD_OFF_FL =OBS
4136 . #
4137 . # 120sec 待機（処理時間）
4138 . #
4139 . HEPI.HV_ION_REF_OFF
4140 . DH.DR_REC_OFF
4141 . #
4142 . # < CHECK >
4143 . CHECK DH.DR_REC_ON_OFF =OFF
4144 . #
4145 . # QL          Check-Item          Spec          Check
4146 . # -----+-----+-----+-----+
4147 . # CMD 出力装置  Message          STATUS CHECK 結果 OK   G   N
4148 . #
4149 . # =====
4150 . # 4. 9. PWI (EW0/AM2P/SORBET/MEFIST0/WPT/SC) L/M-Mode check
4151 . # =====
4152 . #
4153 . DH.DR_REC_ON
4154 . #
4155 . # < CHECK >
4156 . CHECK DH.DR_REC_ON_OFF =ON
4157 . #

```

文 書 番 号	シート No.	版 数
	97	1

```

4158 . EWO. CAL_ATT_20_SEL
4159 #
4160 . # < CHECK >
4161 CHECK EWO. WFC_E_CAL_ATT =LVL_20DB
4162 . #
4163 . EWO. WPTP_AC_CAL_SEL
4164 #
4165 . # < CHECK >
4166 CHECK EWO. WPTP_CAL_IMP =R1k
4167 . #
4168 . MD2U. EWO_WFCE_INP_SEL 0x02
4169 #
4170 . # < CHECK >
4171 CHECK EWO. WFC_E_INP_SEL =WPT_MONO
4172 . #
4173 . EWO. WPTP_LGAIN_SEL
4174 #
4175 . # < CHECK >
4176 CHECK EWO. WPTP_GAIN =LOW
4177 . #
4178 . EWO. SCAL_WPT_AC_ON
4179 #
4180 . # < CHECK >
4181 CHECK EWO. WPTP_CAL_ONOF =ON
4182 . #
4183 . MD2U. EWO_AM2P_SUSPND 0x01
4184 #
4185 . # < CHECK >
4186 CHECK EWO. AM2P_SUSPND =SPD
4187 . #
4188 . MD2U. EWO_WFC_CAL_ON 0x0074 0x07
4189 #
4190 . # QL          Check-Item          Spec          Check
4191 # -----+-----+-----+-----+
4192 # EWO          CALIBRATION STAGE      increment      G  N
4193 #              AM2P TC WORD COUNT     increment      G  N
4194 #              AM2P ACK COUNT          increment      G  N
4195 #              AM2P TM TRG COUNT       increment      G  N
4196 #              AM2P ACQUI COUNT        increment      N/A
4197 #
4198 . # < CHECK >
4199 CHECK EWO. AM2P_TC_WORD      0x0074
4200 #
4201 . #          CALIBRATION STAGE が、ゼロにもどることを確認
4202 . #
4203 . MD2U. EWO_AM2P_SUSPND 0x00
4204 #
4205 . # < CHECK >
4206 CHECK EWO. AM2P_SUSPND      =RUN
4207 . #
4208 . DH. DR_REC_OFF
4209 #

```

文 書 番 号	シート No.	版 数
	98	1

4210	. # < CHECK >		
4211	CHECK DH. DR_REC_ON_OFF	=OFF	
4212	. #		
4213	EWO. SCAL_ALL_OFF		
4214	. #		
4215	. # < CHECK >		
4216	CHECK EWO. WPTP_CAL_ONOF	=OFF	
4217	CHECK EWO. SCP_CAL_STS	=OFF	
4218	. #		
4219	EWO. CAL_ATT_OPEN		
4220	. #		
4221	. # < CHECK >		
4222	CHECK EWO. WFC_E_CAL_ATT	=OFF	
4223	. #		
4224	MD2U. EWO_WFCE_INP_SEL 0x04		
4225	. #		
4226	. # < CHECK >		
4227	CHECK EWO. WFC_E_INP_SEL	=DIFF	
4228	. #		
4229	EWO. WPTP_IMP_H_SEL		
4230	. #		
4231	. # < CHECK >		
4232	CHECK EWO. WPTP_CAL_IMP	=R10M	
4233	. #		
4234	. # DR 記録開始		
4235	DH. DR_REC_ON		
4236	. #		
4237	. # < CHECK >		
4238	CHECK DH. DR_REC_ON_OFF	=ON	
4239	. #		
4240	. #		
4241	. # < Calibration Sequence >		
4242	SORB. CAL_SEQ		
4243	. #		
4244	. # QL	Check-Item	Spec
4245	. #		Check
4246	. #	-----+-----+-----+-----	
4247	. #	Change of the Following HKs may not be able to be confirmed depending on	
4248	. #	the derivery timing of the telemetry.	
4249	. #	The function should be checked by the mission data	
4250	. #	after the reproduction of the data recorder.	
4251	. #	after the reproduction of the data recorder.	
4252	. #	after the reproduction of the data recorder.	
4253	. #	after the reproduction of the data recorder.	
4254	. #	after the reproduction of the data recorder.	
4255	. #	after the reproduction of the data recorder.	
4256	. #	after the reproduction of the data recorder.	
4257	. #	after the reproduction of the data recorder.	
4258	. #	after the reproduction of the data recorder.	
4259	. #	after the reproduction of the data recorder.	
4260	. #	after the reproduction of the data recorder.	
4261	. #	after the reproduction of the data recorder.	

文 書 番 号	シート No.	版 数
	99	1

```

4262 # HFR FREQ. STEP Increment N/A
4263 # CALIBRATION SOURCE SETUP 0x0/0x3/0x7/0x9 N/A
4264 # CALIBRATION HF1 0x0/0x1 N/A
4265 # CALIBRATION HF2 0x0/0x1 N/A
4266 #
4267 . # <<<<<<<< Collectin data for about 5minutes >>>>>>>>
4268 #
4269 . # DR 記録停止
4270 . DH. DR_REC_OFF
4271 #
4272 . # < CHECK >
4273 CHECK DH. DR_REC_ON_OFF =OFF
4274 #
4275 . # <Observation Sequence>
4276 . SORB. OBS_SEQ
4277 #
4278 . # QL Check-Item Spec Check
4279 # -----+-----+-----+-----
4280 # SORBET COMMAND COUNTER Increment G N
4281 # HFR FREQ. STEP Increment G N
4282 # COMMAND CODE 0X518F または 0x53BF G N
4283 . #
4284 . DH. DR_REC_ON
4285 #
4286 . # < CHECK >
4287 CHECK DH. DR_REC_ON_OFF =ON
4288 . #
4289 . #
4290 . DH. PWI_AM2P_DIFF_SP_MODE_HF # DH. MC_EACH_EXE 0x00DA
4291 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
4292 #: 0x00 MD2U. EWO_AM2P_FREQ_PRST 0x01
4293 #: 0x01 MD2U. EWO_AM2P_INP_PRST 0x04
4294 #: 0x02 MD2U. EWO_AM2P_MEF_MODE_PRST 0x2115
4295 #: 0x03 MD2U. EWO_AM2P_MEF_BIAS1_SET 0x800080
4296 #: 0x04 MD2U. EWO_AM2P_MEF_BIAS2_SET 0x800080
4297 #: 0x05 MD2U. EWO_AM2P_TC_SET 0x1004 0x07
4298 . #
4299 . MD2U. EWO_AM2P_INT_SET 0xFFFF 0x01
4300 #
4301 . # PWI 側での確認をまつ
4302 . #
4303 . DH. PWI_AM2P_DIFF_WR_MODE_HF # DH. MC_EACH_EXE 0x00DC
4304 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
4305 #: 0x00 MD2U. EWO_AM2P_FREQ_PRST 0x01
4306 #: 0x01 MD2U. EWO_AM2P_INP_PRST 0x01
4307 #: 0x02 MD2U. EWO_AM2P_MEF_MODE_PRST 0x212a
4308 #: 0x03 MD2U. EWO_AM2P_MEF_BIAS1_SET 0x800080
4309 #: 0x04 MD2U. EWO_AM2P_MEF_BIAS2_SET 0x800080
4310 #: 0x05 MD2U. EWO_AM2P_TC_SET 0x3005 0x07
4311 . #
4312 . MD2U. EWO_AM2P_INT_SET 0xFFFF 0x01
4313 #

```

文 書 番 号	シート No.	版 数
	100	1

```

4314 . # PWI 側での確認をまつ
4315 . #
4316 . DH. PWI_AM2P_B_MUTUAL_MODE_HF # DH. MC_EACH_EXE 0x00D7
4317 #:      << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
4318 #:      0x00 MD2U. EWO_AM2P_FREQ_PRST 0x01
4319 #:      0x01 MD2U. EWO_AM2P_INP_PRST 0x04
4320 #:      0x02 MD2U. EWO_AM2P_MEF_MODE_PRST 0x212a
4321 #:      0x03 MD2U. EWO_AM2P_MEF_BIAS1_SET 0x800080
4322 #:      0x04 MD2U. EWO_AM2P_MEF_BIAS2_SET 0x800080
4323 #:      0x05 MD2U. EWO_AM2P_TC_SET 0x5005 0x07
4324 . #
4325 . MD2U. EWO_AM2P_INT_SET 0xFFFF 0x01
4326 . #
4327 . # PWI 側での確認をまつ
4328 . #
4329 . DH. PWI_AM2P_C_MUTUAL_MODE_HF # DH. MC_EACH_EXE 0x00D4
4330 #:      << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
4331 #:      0x00 MD2U. EWO_AM2P_FREQ_PRST 0x01
4332 #:      0x01 MD2U. EWO_AM2P_INP_PRST 0x01
4333 #:      0x02 MD2U. EWO_AM2P_MEF_MODE_PRST 0x212a
4334 #:      0x03 MD2U. EWO_AM2P_MEF_BIAS1_SET 0x800080
4335 #:      0x04 MD2U. EWO_AM2P_MEF_BIAS2_SET 0x800080
4336 #:      0x05 MD2U. EWO_AM2P_TC_SET 0xd004 0x07
4337 . #
4338 . MD2U. EWO_AM2P_INT_SET 0xFFFF 0x01
4339 . #
4340 . # PWI 側での確認をまつ
4341 . #
4342 . #
4343 . # << AM2P 定常運転中止 >>
4344 . MD2U. EWO_AM2P_INT_SET 0x0000 0x00
4345 . #
4346 . #
4347 . #
4348 . # Execution of the main2-7361(MEFIST0 open impedance procedure)
4349 . # :付録-1 の手順(main2-7361)を実施する。
4350 . #
4351 . #
4352 . #
4353 . DH. PWI_EWO_WPTS1_SWEEP # DH. MC_EACH_EXE 0x00D9
4354 . #
4355 . # Waiting for 30seconds
4356 . # Change of the Bias sweep status on HK is occasionally missing
4357 . # depending on the telemetry timing.
4358 . #
4359 . DH. PWI_EWO_WPTS2_SWEEP # DH. MC_EACH_EXE 0x00DF
4360 . #
4361 . # Waiting for 30seconds
4362 . # Change of the Bias sweep status on HK is occasionally missing
4363 . # depending on the telemetry timing.
4364 . #
4365 . # DR 記録停止

```

文 書 番 号	シート No.	版 数
	101	1

```

4366 . DH.DR_REC_OFF
4367 #
4368 . # < CHECK >
4369 CHECK DH.DR_REC_ON_OFF =OFF
4370 #
4371 . # QL          Check-Item          Spec          Check
4372 . # -----+-----+-----+-----
4373 . # CMD 出力装置  Message          STATUS CHECK 結果 OK    G    N
4374 #
4375 # =====
4376 . # 4.10. MGF CHECKOUT (含磁場キャンセル)
4377 # =====
4378 # -----
4379 . # 4.10.1. DR REC START
4380 # -----
4381 #
4382 #
4383 . # <Data Recorder  Record start>
4384 . #
4385 . DH.DR_REC_ON
4386 #
4387 WAIT_SEC 8
4388 #
4389 . # < CHECK >
4390 CHECK DH.DR_REC_ON_OFF =ON
4391 . #
4392 #
4393 . # QL          Check-Item          Spec          Check
4394 . # -----+-----+-----+-----
4395 . # CMD 出力装置  Message          STATUS CHECK 結果 OK    G    N
4396 #
4397 # -----
4398 . # 4.10.2. MGF-0 CHECKOUT START / STOP, DR REPRO
4399 # -----
4400 . #
4401 . # 磁場キャンセルの電流を調整し、MGF-0 の磁場出力 (HK) が測定レンジ内であること
4402 . # を確認 (PI 側作業)
4403 . # adjust the current of magnetic compensation coil and confirm that the
4404 . # MGF-0 value is within measurement range by MGF PI team
4405 . #
4406 . # <START MGF-0 CHECKOUT MISSION DATA, M-MODE=8Hz>
4407 . #
4408 . MD1U.MGFO_TLM_PARM 0x01010108010c
4409 . #
4410 WAIT_SEC 8
4411 . #
4412 . # < CHECK >
4413 CHECK MGFO.TLM_OUT_STAT_L =OUTPUT
4414 CHECK MGFO.TLM_OUT_RATE_L =SPIN
4415 CHECK MGFO.TLM_OUT_STAT_M =OUTPUT
4416 CHECK MGFO.TLM_OUT_RATE_M =F8HZ
4417 CHECK MGFO.TLM_OUT_STAT_H =OUTPUT

```

文 書 番 号	シート No.	版 数
	102	1

```

4418 CHECK MGFO.TLM_OUT_RATE_H =CHECKOUT
4419 #
4420 #
4421 . # PI の指示により次へ進む。
4422 # Proceed by the order from MGF PI team
4423 #
4424 . # <CHANGE MGF-O M-MODE DATA 4Hz>
4425 . #
4426 . MD1U.MGFO_TLM_PARM 0x01010104010c
4427 #
4428 WAIT_SEC 8
4429 #
4430 # < CHECK >
4431 CHECK MGFO.TLM_OUT_STAT_L =OUTPUT
4432 CHECK MGFO.TLM_OUT_RATE_L =SPIN
4433 CHECK MGFO.TLM_OUT_STAT_M =OUTPUT
4434 CHECK MGFO.TLM_OUT_RATE_M =F4HZ
4435 CHECK MGFO.TLM_OUT_STAT_H =OUTPUT
4436 CHECK MGFO.TLM_OUT_RATE_H =CHECKOUT
4437 #
4438 . # PI の指示により次へ進む。
4439 # Proceed by the order from MGF PI team
4440 #
4441 . # <MGF-O STOP CHECKOUT MISSION DATA>
4442 . #
4443 . MD1U.MGFO_TLM_PARM 0x01010108000c
4444 #
4445 WAIT_SEC 8
4446 #
4447 # < CHECK >
4448 CHECK MGFO.TLM_OUT_STAT_L =OUTPUT
4449 CHECK MGFO.TLM_OUT_RATE_L =SPIN
4450 CHECK MGFO.TLM_OUT_STAT_M =OUTPUT
4451 CHECK MGFO.TLM_OUT_RATE_M =F8HZ
4452 CHECK MGFO.TLM_OUT_STAT_H =STOP
4453 CHECK MGFO.TLM_OUT_RATE_H =CHECKOUT
4454 #
4455 #
4456 . # QL          Check-Item          Spec          Check
4457 # -----+-----+-----+-----+
4458 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
4459 #
4460 # -----
4461 . # 4. 10. 3.  DR Repro
4462 # -----
4463 . #
4464 . DH.DR_RECNTNOTIFY
4465 . #
4466 . DH.DR_REPQUE_REG 73 0xFFFFFFFF 0xFFFFFFFF # MGF-O H0 Data Packet
4467 . DH.DR_REPQUE_REG 74 0xFFFFFFFF 0xFFFFFFFF # MGF-I H0 Data Packet
4468 . #
4469 . # < CHECK >

```

文 書 番 号	シート No.	版 数
	103	1

```

4470 CHECK DH.DR_REP_QUE_NUM 2
4471 . #
4472 . DH.DR_REPQUENOTIFY
4473 . #
4474 . # <DR Repro start>
4475 . DH.DR_REP_ON
4476 . #
4477 . # < CHECK >
4478 CHECK DH.DR_REPRO_ON_OFF =ON
4479 . #
4480 . # < REPRO 終了まで待機 >
4481 . #
4482 . # < CHECK >
4483 CHECK DH.DR_REPRO_ON_OFF =OFF
4484 . #
4485 . #
4486 . # 4.10.2 の手順を PI の指示に従い繰り返す。
4487 . # Procedure in 4.10.2 is repeated by the request from PI team.
4488 . #
4489 . #
4490 . # -----
4491 . # 4.10.4 MGF-0 L/M-Mode check (Normal Function-D sequence)
4492 . # -----
4493 . #
4494 . #
4495 . # <MGF-0 Procedure>
4496 . # Boot from EEPROM Page 1 (Test counter) and produce TMR warnings >
4497 . # <<BOOT EEPROM PAGE 1>>
4498 . # MDP Macro - first Byte after 0x is Parameter Byte No.0(Command count)
4499 . # MDP Multi CMD consists of the following MGFO CMDs
4500 . # +-----+
4501 . # | CMD No. | CMD Name | CMD data |
4502 . # +-----+
4503 . # | 0 | MGFO.MAG_CONFIG_REG | 0x0010 |
4504 . # | 1 | MGFO.BOOT_OP_REG | 0x0009 |
4505 . # +-----+
4506 . # The 2 commands of MDP macro command must be executed within 2 seconds
4507 . MD1U.MGFO_MULTI_CMD 0x020000101A0009000000000000000000000000000000
4508 . #
4509 . # < CHECK >
4510 CHECK MGFO.PRAM_STAT 0
4511 CHECK MGFO.MDATRAM_STAT 0
4512 CHECK MGFO.EXTSYNC_OFF 0
4513 CHECK MGFO.RELAY_ON_OFF 1
4514 CHECK MGFO.OPEN_LOOP 0
4515 CHECK MGFO.EXCIT_ON 1
4516 CHECK MGFO.EEPROM_PAGE 1
4517 CHECK MGFO.BOOT_OP 1
4518 CHECK MGFO.BOOT_MAIN 1
4519 CHECK MGFO.CS_OK 1
4520 CHECK MGFO.RELAY1_STAT 1
4521 CHECK MGFO.SYNC_PRESENT 1

```

文 書 番 号	シート No.	版 数
	104	1

```

4522 CHECK MGFO.LINK_DISCONNECT 0
4523 CHECK MGFO.LINK_EERR 0
4524 CHECK MGFO.LINK_CERR 0
4525 CHECK MGFO.LINK_PERR 0
4526 CHECK MGFO.RMAP_ERR_FLAGS 0
4527 CHECK MGFO.VALID_WCMDS 1
4528 CHECK MGFO.INVALID_CMDS 0
4529 CHECK MGFO.SYNC_OFFSET 0xa1fe
4530 CHECK MGFO.SOFTWARE_VER 0x0206
4531 CHECK MGFO.EEPROM_CHECKSUM 0x16D5
4532 CHECK MGFO.CALC_CHECKSUM 0x16D5
4533 CHECK MGFO.ELECTR_TEMP -30 70
4534 CHECK MGFO.OS_TEMP -60 180
4535 CHECK MGFO.IS_TEMP -60 180
4536 CHECK MGFO.CURRENT_P12V 65 79
4537 CHECK MGFO.CURRENT_M12V -40 -29
4538 CHECK MGFO.CURRENT_3V3 32 39
4539 CHECK MGFO.CURRENT_1V5 55 77
4540 CHECK MGFO.VOLTAGE_PANA 7.3 7.9
4541 CHECK MGFO.VOLTAGE_MANA -7.9 -7.5
4542 CHECK MGFO.VOLTAGE_P12V 11.3 12.5
4543 CHECK MGFO.VOLTAGE_M12V -12.5 -11.3
4544 CHECK MGFO.VOLTAGE_3V3 3.1 3.5
4545 CHECK MGFO.VOLTAGE_P5V 4.7 5.3
4546 CHECK MGFO.VOLTAGE_M5V -5.3 -4.7
4547 CHECK MGFO.VOLTAGE_1V5 1.37 1.63
4548 CHECK MGFO.VOLTAGE_EXCIT 7.8 8.4
4549 #
4550 . # <<<<<<<< 2 分間データ収集 >>>>>>>>
4551 . # <<<<<<<< 2-minutes data recording >>>>>>>>
4552 . #
4553 . #
4554 . # -----
4555 . # 4.10.5. MGF-I CHECKOUT START / STOP, DR REPRO
4556 . # -----
4557 . #
4558 . # 磁場キャンセラの電流を調整し、MGF-I の磁場出力 (HK) が測定レンジ内であること
4559 . # を確認 (PI 側作業)
4560 . # adjust the current of magnetic compensation coil and confirm that the
4561 . # MGF-I value is within measurement range by MGF PI team
4562 . #
4563 . # <START MGF-I CHECKOUT MISSION DATA, M-MODE=8Hz>
4564 . #
4565 . MD2U.MGFI_TLM_PARM 0x01010108010c
4566 . #
4567 . WAIT_SEC 8
4568 . #
4569 . # < CHECK >
4570 . CHECK MGFI.TLM_OUT_STAT_L =OUTPUT
4571 . CHECK MGFI.TLM_OUT_RATE_L =SPIN
4572 . CHECK MGFI.TLM_OUT_STAT_M =OUTPUT
4573 . CHECK MGFI.TLM_OUT_RATE_M =F8HZ

```

文 書 番 号	シート No.	版 数
	105	1

```

4574 CHECK MGFI.TLM_OUT_STAT_H           =OUTPUT
4575 CHECK MGFI.TLM_OUT_RATE_H           =CHECKOUT
4576 #
4577 . # PI の指示により次へ進む。
4578 # Proceed by the order from MGF PI team
4579 #
4580 . # <CHANGE MGF-I M-MODE DATA 4Hz>
4581 . #
4582 . MD2U.MGFI_TLM_PARM 0x01010104010c
4583 #
4584 WAIT_SEC 8
4585 #
4586 # < CHECK >
4587 CHECK MGFI.TLM_OUT_STAT_L           =OUTPUT
4588 CHECK MGFI.TLM_OUT_RATE_L           =SPIN
4589 CHECK MGFI.TLM_OUT_STAT_M           =OUTPUT
4590 CHECK MGFI.TLM_OUT_RATE_M           =F4HZ
4591 CHECK MGFI.TLM_OUT_STAT_H           =OUTPUT
4592 CHECK MGFI.TLM_OUT_RATE_H           =CHECKOUT
4593 #
4594 . # PI の指示により次へ進む。
4595 # Proceed by the order from MGF PI team
4596 . # <MGF-I STOP CHECKOUT MISSION DATA>
4597 #
4598 . MD2U.MGFI_TLM_PARM 0x01010108000c
4599 #
4600 WAIT_SEC 8
4601 #
4602 # < CHECK >
4603 CHECK MGFI.TLM_OUT_STAT_L           =OUTPUT
4604 CHECK MGFI.TLM_OUT_RATE_L           =SPIN
4605 CHECK MGFI.TLM_OUT_STAT_M           =OUTPUT
4606 CHECK MGFI.TLM_OUT_RATE_M           =F8HZ
4607 CHECK MGFI.TLM_OUT_STAT_H           =STOP
4608 CHECK MGFI.TLM_OUT_RATE_H           =CHECKOUT
4609 #
4610 #
4611 #
4612 #
4613 #
4614 #
4615 #
4616 #
4617 . # QL          Check-Item          Spec          Check
4618 # -----+-----+-----+-----
4619 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
4620 #
4621 # -----
4622 . # 4.10.6. DR Repro
4623 # -----
4624 . #
4625 . DH.DR_RECNTNOTIFY

```

文 書 番 号	シート No.	版 数
	106	1

```

4626 . #
4627 . DH. DR_REPQUE_REG 73 0xFFFFFFFF 0xFFFFFFFF # MGF-O H0 Data Packet
4628 . DH. DR_REPQUE_REG 74 0xFFFFFFFF 0xFFFFFFFF # MGF-I H0 Data Packet
4629 . #
4630 . # < CHECK >
4631 . CHECK DH. DR_REP_QUE_NUM                2
4632 . #
4633 . DH. DR_REPQUENOTIFY
4634 . #
4635 . # <DR Repro start>
4636 . DH. DR_REP_ON
4637 . #
4638 . # < CHECK >
4639 . CHECK DH. DR_REPRO_ON_OFF                =ON
4640 . #
4641 . # < REPRO 終了まで待機 >
4642 . #
4643 . # < CHECK >
4644 . CHECK DH. DR_REPRO_ON_OFF                =OFF
4645 . #
4646 . # PI の指示に従い、4. 10. 5 を繰り返す。
4647 . # Procedure 4. 10. 5 is repeated by the order from PI team.
4648 . #
4649 . #
4650 . # -----
4651 . # 4. 10. 7. DR Rec OFF
4652 . # -----
4653 . #
4654 . # <DR Record stop>
4655 . DH. DR_REC_OFF
4656 . #
4657 . WAIT_SEC 8
4658 . #
4659 . # < CHECK >
4660 . CHECK DH. DR_REC_ON_OFF                =OFF
4661 . #
4662 . #
4663 . #
4664 . #
4665 . #
4666 . # QL          Check-Item                Spec                Check
4667 . # -----+-----+-----+-----
4668 . # CMD 出力装置  Message                STATUS CHECK 結果 OK        G  N
4669 . #
4670 . #
4671 . # =====
4672 . # 4. 11. MAST L/M-Mode check
4673 . # =====
4674 . #
4675 . # N/A
4676 . #
4677 . # =====

```

文 書 番 号	シート No.	版 数
	107	1

```

4678 . # 4.12. MDM L/M-Mode check
4679 . # =====
4680 . #
4681 . # MDM 電源供給後 MDM はイニシャル処理を行うため、定常動作となる約 3 分後
4682 . # ステータスを QL モニタにてチェックする。
4683 . #
4684 . # < CHECK >
4685 . CHECK MDM. MDM_DAUTO =OFF
4686 . CHECK MDM. MDM_TRIG =MODE1
4687 . CHECK MDM. MDM_CAL =OFF
4688 . CHECK MDM. MDM_OBS =MODE1
4689 . CHECK MDM. MDM_MUX 0x00
4690 . CHECK MDM. MDM_SEL 0x0f
4691 . CHECK MDM. MDM_SAMP 0x1
4692 . CHECK MDM. MDM_ACLK 0x1
4693 . CHECK MDM. MDM_DISCRI2 0x06
4694 . CHECK MDM. MDM_PARA 0x00
4695 . #
4696 . # QL          Check-Item          Spec          Data          Check
4697 . # -----+-----+-----+-----+-----
4698 . # MDM          MDM_DISCRI1          0x01~0x40          _____ G N
4699 . #
4700 . # ディスクリ 1 の設定送信
4701 . MDM. MDM_DISCRI1_LSET 0x55
4702 . #
4703 . # < CHECK >
4704 . CHECK MDM. MDM_DISCRI1          0x55
4705 . #
4706 . # 自動ディスクリ設定機能確認
4707 . # 下記コマンド送信後 1 分以内に HK 画面のステータス確認を行う
4708 . MDM. MDM_DISCRI1_AUTO
4709 . #
4710 . # < CHECK >
4711 . CHECK MDM. MDM_DAUTO =ON
4712 . #
4713 . # 上記コマンドを送信して 4 分以上経過後に以下のステータス確認を行う
4714 . #
4715 . # < CHECK >
4716 . CHECK MDM. MDM_DAUTO =OFF
4717 . CHECK MDM. MDM_TRIG =MODE1
4718 . CHECK MDM. MDM_OBS =MODE1
4719 . CHECK MDM. MDM_DISCRI1 0x01 0x40
4720 . #
4721 . DH. DR_REC_ON
4722 . #
4723 . # < CHECK >
4724 . CHECK DH. DR_REC_ON_OFF =ON
4725 . #
4726 . # MDM-S に対して超音波を照射する。(現地担当者作業)
4727 . #
4728 . # 11 分以上データを取得する。REC と並行して次章以降の試験手順を実施可。
4729 . #

```

文書番号 TPP-FS8238-31K	シート No.	版数
	108	1

```

4730 . #
4731 . DH. DR_REC_OFF
4732 . #
4733 . # < CHECK >
4734 . CHECK DH. DR_REC_ON_OFF =OFF
4735 . #
4736 . # QL Check-Item Spec Check
4737 . # -----+-----+-----+-----
4738 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
4739 . #
4740 . # =====
4741 . # 4.13. MSASI L/M-Mode check
4742 . # =====
4743 . #
4744 . # < CHECK >
4745 . CHECK MSAS.RMAP_STATUS 0
4746 . CHECK MSAS.CMD_CNT 0
4747 . CHECK MSAS.MSASI_ON_OFF =RUN
4748 . CHECK MSAS.LLM_STATUS =OK
4749 . CHECK MSAS.SP_STATUS =OK
4750 . CHECK MSAS.LINK_STATUS =OK
4751 . CHECK MSAS.PARTITION_AB =A
4752 . CHECK MSAS.PACKET_CNT 0
4753 . CHECK MSAS.OBS_MODE =AUT
4754 . CHECK MSAS.AUTOOBS_STATUS =STP
4755 . CHECK MSAS.CMOS_ON_OFF =ON
4756 . CHECK MSAS.CMOS_RO =STP
4757 . CHECK MSAS.CMOS_INPUT =IMG
4758 . CHECK MSAS.G_ON_OFF =OFF
4759 . CHECK MSAS.G_CRC =OK
4760 . CHECK MSAS.G_TLM_ANGLE 0
4761 . CHECK MSAS.G_STEP 0
4762 . CHECK MSAS.HV_ON_OFF =OFF
4763 . CHECK MSAS.HV_ENA_DIS =DIS
4764 . CHECK MSAS.HV_MONI_N4K 0
4765 . CHECK MSAS.HV_MONI_P6K 0
4766 . CHECK MSAS.HT_CTRL_ON_OFF =OFF
4767 . CHECK MSAS.HT_ON_OFF =OFF
4768 . CHECK MSAS.CENTER_CTS 0
4769 . CHECK MSAS.DATA_TYPE =DUM
4770 . CHECK MSAS.SEQ_PACKET_CNT 0
4771 . CHECK MSAS.SEQ_CNT 0
4772 . CHECK MSAS.MSASI_SP_ENA_DIS =ENA
4773 . CHECK MSAS.CHANGE_DUM_FLG =OFF
4774 . CHECK MSAS.SP_AVG_TIME 8
4775 . CHECK MSAS.SHOT_TIMING 0
4776 . CHECK MSAS.SHOT_NUMBER 256
4777 . CHECK MSAS.INTEG_SPIN 0
4778 . CHECK MSAS.LINE_NUMBER 21
4779 . CHECK MSAS.TOTAL_CNT 0
4780 . CHECK MSAS.CMOS_GAIN =X1
4781 . CHECK MSAS.CMOS_BIN_MODE =XYBIN

```

文 書 番 号	シート No.	版 数
	109	1

4782	CHECK MSAS.CMOS_EXP_TIME	2000	
4783	CHECK MSAS.CMOS_ROIX_START	494	
4784	CHECK MSAS.CMOS_ROIY_START	480	
4785	CHECK MSAS.CMOS_ROIX_NUMBER	10	
4786	CHECK MSAS.CMOS_ROIY_NUMBER	10	
4787	CHECK MSAS.CMOS_ROIX_STEP	4	
4788	CHECK MSAS.CMOS_ROIY_STEP	4	
4789	CHECK MSAS.CMOS_OFFSET	111	
4790	CHECK MSAS.COM_SENT_TO_G	0	
4791	CHECK MSAS.G_TLM_CMD	0	
4792	CHECK MSAS.G_TLM_P15V	=NG	
4793	CHECK MSAS.G_TLM_M15V	=NG	
4794	CHECK MSAS.G_TLM_ADC	=NG	
4795	CHECK MSAS.G_TLM_ANG_SENS	=NG	
4796	CHECK MSAS.G_TLM_PS_SENS1	=NG	
4797	CHECK MSAS.G_TLM_PS_SENS2	=NG	
4798	CHECK MSAS.G_TLM_CMD_RES	=ERR	
4799	CHECK MSAS.HV_AUTO_N4K_L	0	
4800	CHECK MSAS.HV_AUTO_P6K_L	0	
4801	CHECK MSAS.HV_LIM_N4K	255	
4802	CHECK MSAS.HV_LIM_P6K	255	
4803	CHECK MSAS.HV_AUTO_STEP	100	
4804	CHECK MSAS.HT_MODE	=AUT	
4805	CHECK MSAS.HT_STATUS	=OUT	
4806	CHECK MSAS.OBS_CHECK_STATUS	0	
4807	CHECK MSAS.P15V_V_L	12 16	
4808	CHECK MSAS.P15V_I_L	0.01 0.3	
4809	CHECK MSAS.M15V_V_L	12 16	
4810	CHECK MSAS.M15V_I_L	0.01 0.3	
4811	CHECK MSAS.P5V_V_L	4.5 5.2	
4812	CHECK MSAS.P5V_I_L	0.1 1	
4813	CHECK MSAS.TEMP1_L	15 40	
4814	CHECK MSAS.TEMP2_L	15 40	
4815	CHECK MSAS.TEMP3_L	15 40	
4816	CHECK MSAS.TEMP4_L	15 40	
4817	CHECK MSAS.TEMP5_L	15 40	
4818	CHECK MSAS.TEMP6_L	15 40	
4819	CHECK MSAS.TEMP7_L	15 40	
4820	CHECK MSAS.TEMP8_L	15 40	
4821	#		
4822	# 以下は MSASI 側で確認		
4823	# QL	Check-Item	Spec
4824	#		
4825	# MSASI	SP_CNT	spin ごとに1カウントアップ N/A
4826	#	SP_TIME_USED	spin ごとに1カウントアップ N/A
4827	#	TIME_CODE	変化することを確認 N/A
4828	#		
4829	# Wait the check by PI team		
4830	# P I チームでの確認を待つ		
4831	#		
4832	# QL	Check-Item	Spec Data
4833	#		

文 書 番 号	シート No.	版 数
	110	1

```

4834 # MSASI          MSAS.CMD_CNT          記録のみ          N/A
4835 #
4836 . # < CHECK >
4837 LET MSASI_CNT = ({MSAS.CMD_CNT} + 10 ) & 0xFF
4838 #
4839 . # *** ショートマクロ MSASI_CMOS_SETUP 実行 ***
4840 . #
4841 . DH.MSASI_CMOS_SETUP      # DH.MC_EACH_EXE 0x00F1
4842 #:          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
4843 #:          0x00 MSAS.CMOS_SET_GAIN 0x00
4844 #:          0x01 MSAS.CMOS_SET_BIN 0x02
4845 #:          0x02 MSAS.CMOS_EXP_TIME 0x0007D0
4846 #:          0x03 MSAS.CMOS_ROIX_START 0x01F4
4847 #:          0x04 MSAS.CMOS_ROIY_START 0x01EA
4848 #:          0x05 MSAS.CMOS_ROIX_NUMBER 0x000A
4849 #:          0x06 MSAS.CMOS_ROIY_NUMBER 0x000A
4850 #:          0x07 MSAS.CMOS_ROIX_STEP 0x04
4851 #:          0x08 MSAS.CMOS_ROIY_STEP 0x04
4852 #:          0x09 MSAS.CMOS_SET_OFFSET 0x71
4853 #
4854 . # < CHECK >
4855 SYSTEM test {MSAS.CMD_CNT} = {MSASI_CNT}
4856 CHECK MSAS.CMOS_GAIN          =X1
4857 CHECK MSAS.CMOS_INPUT          =IMG
4858 CHECK MSAS.CMOS_BIN_MODE       =XYBIN
4859 CHECK MSAS.CMOS_EXP_TIME       2000
4860 CHECK MSAS.CMOS_ROIX_START     500
4861 CHECK MSAS.CMOS_ROIY_START     490
4862 CHECK MSAS.CMOS_ROIX_NUMBER    10
4863 CHECK MSAS.CMOS_ROIY_NUMBER    10
4864 CHECK MSAS.CMOS_ROIX_STEP      4
4865 CHECK MSAS.CMOS_ROIY_STEP      4
4866 CHECK MSAS.CMOS_OFFSET         113
4867 LET MSASI_CNT = ({MSAS.CMD_CNT} + 4 ) & 0xFF
4868 #
4869 . # Wait the check by PI team
4870 . # P I チームでの確認を待つ
4871 #
4872 . # *** ショートマクロ MSASI_HT_SETUP 実行 ***
4873 . #
4874 . DH.MSASI_HTR_SETUP      # DH.MC_EACH_EXE 0x00F3
4875 #:          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
4876 #:          0x00 MSAS.HT_SET_ON_TEMP 0x61
4877 #:          0x01 MSAS.HT_SET_OFF_TEMP 0x65
4878 #:          0x02 MSAS.HT_SET_MODE 0x01
4879 #:          0x03 MSAS.HT_ON
4880 #
4881 . # < CHECK >
4882 SYSTEM test {MSAS.CMD_CNT} = {MSASI_CNT}
4883 CHECK MSAS.HT_CTRL_ON_OFF      =ON
4884 CHECK MSAS.HT_ON_OFF           =ON
4885 #

```

文 書 番 号	シート No.	版 数
	111	1

4886	# QL	Check-Item	Spec	Check
4887	#	-----	-----	-----
4888	# MSASI			
4889	# USER	HT_MODE	AUT	N/A
4890	# USER	HT_ON_TEMP	36.7	N/A
4891	# USER	HT_OFF_TEMP	41.4	N/A
4892	# USER	TEMP4_L		N/A
4893	# BOTH	HT_STATUS	IN	N/A
4894	#			
4895	#	Wait the check by PI team		
4896	#	P I チームでの確認を待つ		
4897	#			
4898	#			
4899	#	MSAS.G_INIT		
4900	#			
4901	#	< CHECK >		
4902		CHECK MSAS.G_ON_OFF	=ON	
4903		LET MSASI_CNT = ({MSAS.CMD_CNT} + 8) & 0xFF		
4904	#			
4905	# QL	Check-Item	Spec	Check
4906	#	-----	-----	-----
4907	# MSASI			
4908	# BOTH	G_STEP	13	N/A
4909	# USER	G_TLM_COM	0x01	N/A
4910	# USER	G_TLM_ANGLE	(Nominal : 33000-33800)	N/A
4911	# USER	G_TLM_CMD_RES	CMD_EXE	N/A
4912	# USER	G_P5V	(4.0-5.4V)	N/A
4913	# USER	P15V_V_L	(12-16V)	N/A
4914	# USER	P15V_I_L	(0.01-0.3A)	N/A
4915	# USER	M15V_V_L	(12-16V)	N/A
4916	# USER	M15V_I_L	(0.01-0.3A)	N/A
4917	# USER	P5V_V_L	(4.5-5.2V)	N/A
4918	# USER	P5V_I_L	(0.1-1.0A)	N/A
4919	#			
4920	#			
4921	#	Wait the check by PI team		
4922	#	P I チームでの確認を待つ		
4923	#			
4924	#			
4925	#	*** ショートマクロ MSASI_HV_SETUP 実行 ***		
4926	#			
4927	#	DH.MSASI_HV_SETUP # DH.MC_EACH_EXE 0x00F2		
4928	#:	<< MACRO >> 内容について以下に示す (DH.DUMMY は省略)		
4929	#:	0x00 MSAS.HV_ENA		
4930	#:	0x01 MSAS.HV_ON		
4931	#:	0x02 MSAS.HV_SET_AUTO_P6K 0xF4		
4932	#:	0x03 MSAS.HV_SET_AUTO_N4K 0xB2		
4933	#:	0x04 MSAS.HV_SET_LIM_N4K 0xFF		
4934	#:	0x05 MSAS.HV_SET_LIM_P6K 0xFF		
4935	#:	0x06 MSAS.HV_SET_AUTO_STEP 0x0064		
4936	#:	0x07 MSAS.HV_EXEC		
4937	#			

文 書 番 号	シート No.	版 数
	113	1

```

4990 # SYSTEM CENTER_CTS N/A
4991 # USER TOTAL_CNT N/A
4992 # USER G_STEP 3-23 G N
4993 # (1 spin ごとにカウントアップ) N/A
4994 #
4995 #
4996 . # Wait the check by PI team
4997 . # P I チームでの確認を待つ
4998 #
4999 . # < CHECK >
5000 LET MSASI_CNT = ({MSAS.CMD_CNT} + 10 ) & 0xFF
5001 #
5002 . # *** ショートマクロ MSASI_SETUP_2 実行 ***
5003 . #
5004 . DH.MSASI_SETUP_2 # DH.MC_EACH_EXE 0x00F5
5005 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
5006 #: 0x00 MSAS.SET_SHOT_NUMBER 0x000008
5007 #: 0x01 MSAS.CMOS_SET_BIN 0x00
5008 #: 0x02 MSAS.CMOS_EXP_TIME 0x0007D0
5009 #: 0x03 MSAS.CMOS_ROIX_START 0x01E0
5010 #: 0x04 MSAS.CMOS_ROIY_START 0x01C2
5011 #: 0x05 MSAS.CMOS_ROIX_NUMBER 0x0050
5012 #: 0x06 MSAS.CMOS_ROIY_NUMBER 0x0050
5013 #: 0x07 MSAS.CMOS_ROIX_STEP 0x01
5014 #: 0x08 MSAS.CMOS_ROIY_STEP 0x01
5015 #: 0x09 MSAS.CMOS_SET_OFFSET 0x71
5016 #
5017 . # < CHECK >
5018 SYSTEM test {MSAS.CMD_CNT} = {MSASI_CNT}
5019 #
5020 . # QL Check-Item Spec Data Check
5021 # -----+-----+-----+-----+
5022 # MSASI
5023 # USER SHOT_NUMBER 8 N/A
5024 # USER CMOS_BIN_MODE XYBIN N/A
5025 # USER CMOS_EXP_TIME 2000 N/A
5026 # USER CMOS_ROIX_START 480 N/A
5027 # USER CMOS_ROIY_START 450 N/A
5028 # USER CMOS_ROIX_NUMBER 80 N/A
5029 # USER CMOS_ROIY_NUMBER 80 N/A
5030 # USER CMOS_ROIX_STEP 1 N/A
5031 # USER CMOS_ROIY_STEP 1 N/A
5032 # USER CMOS_OFFSET 113 N/A
5033 #
5034 #
5035 . # Wait the check by PI team
5036 . # P I チームでの確認を待つ
5037 #
5038 . # < CHECK >
5039 LET MSASI_CNT = ({MSAS.CMD_CNT} + 1 ) & 0xFF
5040 . #
5041 . DH.MSASI_OBS_START # DH.MC_EACH_EXE 0x00F4

```

文 書 番 号	シート No.	版 数
	114	1

```

5042  #:          << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
5043  #:          0x00 MD2U. BUF_CLCT_MSAS 0x22
5044  #:          0x0F MSAS. AUTO_OBS_START
5045  #
5046  . # < CHECK >
5047  SYSTEM test {MSAS.CMD_CNT} = {MSASI_CNT}
5048  CHECK MSAS. AUTOOBS_STATUS                      =RUN
5049  #
5050  . # QL          Check-Item          Spec          Data          Check
5051  # -----+-----+-----+-----+-----+
5052  # MSASI
5053  #   BOTH      PACKET_CNT          カウントアップ開始を確認          N/A
5054  #   USER      SEQ_PACKET_CNT      (1 spin 中にカウントアップ)          N/A
5055  #   USER      SEQ_CNT              0-21(1 spin ごとにカウントアップ)          N/A
5056  #   SYSTEM    CENTER_CTS          N/A
5057  #   USER      TOTAL_CNT            N/A
5058  #   USER      G_STEP                3-23(1 spin ごとにカウントアップ)          N/A
5059  #
5060  # <健全性確認試験用> 長時間積分 (50msec)
5061  . #
5062  . MSAS. CMOS_EXP_TIME 0x00C350
5063  . #
5064  . # < CHECK >
5065  CHECK MSAS. CMOS_EXP_TIME                      50000
5066  LET MSASI_CNT = ( {MSAS.CMD_CNT} + 1 ) & 0xFF
5067  #
5068  . # <<MSASI CLCT>>
5069  . DH. MSASI_OBS_START      # DH. MC_EACH_EXE 0x00F4
5070  #:          << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
5071  #:          0x00 MD2U. BUF_CLCT_MSAS 0x22
5072  #:          0x0F MSAS. AUTO_OBS_START
5073  #
5074  . # < CHECK >
5075  SYSTEM test {MSAS.CMD_CNT} = {MSASI_CNT}
5076  CHECK MSAS. AUTOOBS_STATUS                      =RUN
5077  CHECK MSAS. SEQ_CNT                          0 21
5078  #
5079  # QL          Check-Item          Spec          Data          Check
5080  # -----+-----+-----+-----+
5081  # MSASI
5082  #   BOTH      PACKET_CNT          カウントアップ開始を確認          G  N
5083  #                                     (1 spin ごとにカウントアップ)          N/A
5084  #   SYSTEM    CENTER_CTS          N/A
5085  #   USER      TOTAL_CNT            N/A
5086  #   USER      G_STEP                3-23          G  N
5087  #                                     (1 spin ごとにカウントアップ)          N/A
5088  #
5089  #
5090  . # Wait the check by PI team
5091  . # P I チームでの確認を待つ
5092  #
5093  . # MSASI コンフィギュレーション Na ランプを OFF する。

```

```

5094 . #
5095 . # MSASI ミッションデータの MDP2 からの出力終了を待つ
5096 . #
5097 . DH. DR_REC_OFF
5098 . #
5099 . # < CHECK >
5100 CHECK DH. DR_REC_ON_OFF =OFF
5101 . #
5102 . # QL Check-Item Spec Check
5103 . # -----+-----+-----+-----
5104 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
5105 . #
5106 . # =====
5107 . # 4. 14. 消費電力取得
5108 . # =====
5109 . #
5110 . # 【記録】
5111 . # 電圧/電流値の取得を実施する。
5112 . # 結果を<データシート 4. 14-1>に記録すること。
5113 . #
5114 . # main2-F311 に続く
5115 . #

```

[illegible]

```

0050 . # < CHECK >
0051 CHECK DH.DR_REC_ON_OFF =ON
0052 #
0053 . # < CHECK >
0054 LET MD1_GET = ( {MD1U.U_CmdDmc_GetCNT} + 1 ) & 0x1F
0055 LET MD2_GET = ( {MD2U.U_CmdDmc_GetCNT} + 1 ) & 0x1F
0056 LET MD1_EXE = ( {MD1U.U_CmdDmc_ExeCNT} + 1 ) & 0x1F
0057 LET MD2_EXE = ( {MD2U.U_CmdDmc_ExeCNT} + 1 ) & 0x1F
0058 . #
0059 . DH.MDP_TLMH1_STRT_16SEC # DH.MC_EACH_EXE 0x0060
0060 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
0061 #: 0x00 MD1U.TLMH_DUMP 0xFF 0x00 0x01 0x02 0x00 0x02 0x00
0062 #: 0x01 MD2U.TLMH_DUMP 0xFF 0x00 0x01 0x02 0x00 0x02 0x00
0063 #
0064 . # < CHECK >
0065 SYSTEM test {MD1U.U_CmdDmc_GetCNT} = {MD1_GET}
0066 SYSTEM test {MD2U.U_CmdDmc_GetCNT} = {MD2_GET}
0067 SYSTEM test {MD1U.U_CmdDmc_ExeCNT} = {MD1_EXE}
0068 SYSTEM test {MD2U.U_CmdDmc_ExeCNT} = {MD2_EXE}
0069 #
0070 # -----
0071 # ---- TEST of DATA COMPRESSION
0072 # -----
0073 #
0074 . # *** ARITH-0 CMP ***
0075 WAIT_SEC 60
0076 #
0077 # *** ARITH-1 CMP ***
0078 MD1U.TLM_CMP 0xFF 0x00 0x22
0079 WAIT_SEC 1
0080 MD2U.TLM_CMP 0xFF 0x00 0x22
0081 WAIT_SEC 60
0082 #
0083 # QL Check-Item Spec Check
0084 #-----+-----+-----+-----+
0085 # MD1U U_CmdDmc_GetCNT (increment: +1) N/A
0086 # U_CmdDmc_ExeCNT (increment: +1) N/A
0087 # U_TlmML_CMP_DPU1 CMP N/A
0088 # U_TlmML_CMP_HEPE/HEPI/MGFO AR1 N/A
0089 # U_TlmH_CMP_DPU1 CMP N/A
0090 # U_TlmH_CMP_HEPE/HEPI/MGFO AR1 N/A
0091 # U_App03_Load (checked by MDP)
0092 #-----+-----+-----+-----+
0093 # MD2U U_CmdDmc_GetCNT (increment: +1) N/A
0094 # U_CmdDmc_ExeCNT (increment: +1) N/A
0095 # U_TlmML_CMP_DPU2 CMP N/A
0096 # U_TlmML_CMP_* AR1 N/A
0097 # U_TlmH_CMP_DPU2 CMP N/A
0098 # U_TlmH_CMP_* AR1 N/A
0099 # U_App13_Load (checked by MDP)
0100 #
0101 #

```

0102 # *** RICE CMP ***
 0103 MD1U.TLM_CMP 0xFF 0x00 0x44
 0104 WAIT_SEC 1
 0105 MD2U.TLM_CMP 0xFF 0x00 0x44
 0106 WAIT_SEC 60

#	QL	Check-Item	Spec	Check
0107	#			
0110	# MD1U	U_CmdDmc_GetCNT	(increment: +1)	N/A
0111	#	U_CmdDmc_ExeCNT	(increment: +1)	N/A
0112	#	U_TlmML_CMP_DPU1	CMP	N/A
0113	#	U_TlmML_CMP_HEPE/HEPI/MGFO	RCE	N/A
0114	#	U_TlmH_CMP_DPU1	CMP	N/A
0115	#	U_TlmH_CMP_HEPE/HEPI/MGFO	RCE	N/A
0116	#	U_App03_Load	(checked by MDP)	
0117	#			
0118	# MD2U	U_CmdDmc_GetCNT	(increment: +1)	N/A
0119	#	U_CmdDmc_ExeCNT	(increment: +1)	N/A
0120	#	U_TlmML_CMP_DPU2	CMP	N/A
0121	#	U_TlmML_CMP_*	RCE	N/A
0122	#	U_TlmH_CMP_DPU2	CMP	N/A
0123	#	U_TlmH_CMP_*	RCE	N/A
0124	#	U_App13_Load	(checked by MDP)	

0125 #
 0126 #
 0127 # *** Arith-0 CMP ***
 0128 MD1U.TLM_CMP 0xFF 0x00 0x11
 0129 WAIT_SEC 1
 0130 MD2U.TLM_CMP 0xFF 0x00 0x11
 0131 WAIT_SEC 60

#	QL	Check-Item	Spec	Check
0132	#			
0135	# MD1U	U_CmdDmc_GetCNT	(increment: +1)	N/A
0136	#	U_CmdDmc_ExeCNT	(increment: +1)	N/A
0137	#	U_TlmML_CMP_DPU1	CMP	N/A
0138	#	U_TlmML_CMP_HEPE/HEPI/MGFO	ARO	N/A
0139	#	U_TlmH_CMP_DPU1	CMP	N/A
0140	#	U_TlmH_CMP_HEPE/HEPI/MGFO	ARO	N/A
0141	#	U_App03_Load	(checked by MDP)	
0142	#			
0143	# MD2U	U_CmdDmc_GetCNT	(increment: +1)	N/A
0144	#	U_CmdDmc_ExeCNT	(increment: +1)	N/A
0145	#	U_TlmML_CMP_DPU2	CMP	N/A
0146	#	U_TlmML_CMP_*	ARO	N/A
0147	#	U_TlmH_CMP_DPU2	CMP	N/A
0148	#	U_TlmH_CMP_*	ARO	N/A
0149	#	U_App13_Load	(checked by MDP)	

#	QL	Check-Item	Spec	Check
0151	#			
0152	#			
0153	# MD1F_OBS_HK	SAT_TIME		N/A

文 書 番 号	シート No.	版 数
	119	1

0154	# MD2F_OBS_HK	SAT_TIME		N/A
0155	#			
0156	# MD1F_OBS_HK	MS0 BUFF (MGFO)	S	N/A
0157	#	MS1 BUFF (MEA1)	S	N/A
0158	#	MS2 BUFF (MEA2)	S	N/A
0159	#	MS4 BUFF (MIA)	S	N/A
0160	#	MS5 BUFF (MSA)	S	N/A
0161	#	MS6 BUFF (HEPE)	S	N/A
0162	#	MS7 BUFF (HEPI)	S	N/A
0163	#			
0164	# MD2F_OBS_HK	MS3 BUFF (EWO)	S	N/A
0165	#	MS5 BUFF (MGFI)	S	N/A
0166	#			
0167	# MD1U_USER_HK	U_BiKl_Clct_MEA1/MEA2/MIA/MSA/HEPE/HEPI/MGFO		
0168	#	dis		
0169	#	U_BiKs_Clct_MEA1/MEA2/MIA/MSA/HEPE/HEPI/MGFO		
0170	#	ENA(if ON)		
0171	#	U_TImH_DUMP_MEA1/MEA2/MIA/MSA/HEPE/HEPI/MGF		
0172	#	ENA(if ON)		
0173	#			
0174	# MD2U_USER_HK	U_BiKl_Clct_MGFI/EWOE/AM2P/EFD/EWOB	dis	
0175	#	U_BiKs_Clct_MGFI/EWOE/AM2P/EFD/EWOB	ENA(if ON)	
0176	#	U_TImH_DUMP_MGFI/EWOE/AM2P/EFD/EWOB	ENA(if ON)	
0177	#			
0178	#	=====		
0179	#	MD1F. MS0～MS2、MS4～MS7 及び MD2F. MS3、MS5 の BUFF_SEL が全て		
0180	#	L → S → L に変化してから CHECK 文を実行のこと。		
0181	#	=====		
0182	#			
0183	#	< CHECK >		
0184	CHECK	MD1U. U_DpuNum	=DPU1	
0185	CHECK	MD1U. U_DpuRdn	=off	
0186	CHECK	MD1U. U_WdtEna	=ENA	
0187	CHECK	MD1U. U_DpuIfStatus	=nrm	
0188	CHECK	MD1U. U_HkMode	=CHK	
0189	CHECK	MD1U. U_HkRep_Ena	=ENA	
0190	CHECK	MD1U. U_TImLM_Ena	=ENA	
0191	CHECK	MD1U. U_CmdDmc_NumBuf	0	
0192	CHECK	MD1U. U_CmdDmc_GetErr	0	
0193	CHECK	MD1U. U_CmdDmc_ExeErr_DPU	0	
0194	CHECK	MD1U. U_CmdDmc_ExeErr_ENA	0	
0195	CHECK	MD1U. U_CmdDmc_ExeErr_HEPE	0	
0196	CHECK	MD1U. U_CmdDmc_ExeErr_HEPI	0	
0197	CHECK	MD1U. U_CmdDmc_ExeErr_MEA1	0	
0198	CHECK	MD1U. U_CmdDmc_ExeErr_MEA2	0	
0199	CHECK	MD1U. U_CmdDmc_ExeErr_MGFO	0	
0200	CHECK	MD1U. U_CmdDmc_ExeErr_MIA	0	
0201	CHECK	MD1U. U_CmdDmc_ExeErr_MSA	0	
0202	CHECK	MD1U. U_CmdUsr_GetErr	0	
0203	CHECK	MD1U. U_CmdUsr_ExeErr_DPU	0	
0204	CHECK	MD1U. U_CmdUsr_ExeErr_ENA	0	
0205	CHECK	MD1U. U_CmdUsr_ExeErr_HEPE	0	

文 書 番 号	シート No.	版 数
	120	1

0206	CHECK MD1U. U_CmdUsr_ExecErr_HEPI	0
0207	CHECK MD1U. U_CmdUsr_ExecErr_MEA1	0
0208	CHECK MD1U. U_CmdUsr_ExecErr_MEA2	0
0209	CHECK MD1U. U_CmdUsr_ExecErr_MGFO	0
0210	CHECK MD1U. U_CmdUsr_ExecErr_MIA	0
0211	CHECK MD1U. U_CmdUsr_ExecErr_MSA	0
0212	CHECK MD1U. U_Tlm_OpeMode	=nrm
0213	CHECK MD1U. U_TlmML_MSAS_CLCT	0
0214	CHECK MD1U. U_Hk_RepInt_DPU1	3
0215	CHECK MD1U. U_Hk_RepInt_ENA	3
0216	CHECK MD1U. U_Hk_RepInt_HEPE	3
0217	CHECK MD1U. U_Hk_RepInt_HEPI	3
0218	CHECK MD1U. U_Hk_RepInt_MEA1	3
0219	CHECK MD1U. U_Hk_RepInt_MEA2	3
0220	CHECK MD1U. U_Hk_RepInt_MGFO	3
0221	CHECK MD1U. U_Hk_RepInt_MIA	3
0222	CHECK MD1U. U_Hk_RepInt_MSA	3
0223	CHECK MD1U. U_TlmHk_CMP_DPU1	=non
0224	CHECK MD1U. U_TlmHk_CMP_ENA	=non
0225	CHECK MD1U. U_TlmHk_CMP_HEPE	=non
0226	CHECK MD1U. U_TlmHk_CMP_HEPI	=non
0227	CHECK MD1U. U_TlmHk_CMP_MEA1	=non
0228	CHECK MD1U. U_TlmHk_CMP_MEA2	=non
0229	CHECK MD1U. U_TlmHk_CMP_MGFO	=non
0230	CHECK MD1U. U_TlmHk_CMP_MIA	=non
0231	CHECK MD1U. U_TlmHk_CMP_MSA	=non
0232	CHECK MD1U. U_TlmML_CMP_DPU1	=CMP
0233	CHECK MD1U. U_TlmML_CMP_ENA	=USR
0234	CHECK MD1U. U_TlmML_CMP_HEPE	=ARO
0235	CHECK MD1U. U_TlmML_CMP_HEPI	=ARO
0236	CHECK MD1U. U_TlmML_CMP_MEA1	=USR
0237	CHECK MD1U. U_TlmML_CMP_MEA2	=USR
0238	CHECK MD1U. U_TlmML_CMP_MGFO	=ARO
0239	CHECK MD1U. U_TlmML_CMP_MIA	=USR
0240	CHECK MD1U. U_TlmML_CMP_MSA	=USR
0241	CHECK MD1U. U_TlmH_DUMP_HEPE	=dis
0242	CHECK MD1U. U_TlmH_DUMP_HEPI	=dis
0243	CHECK MD1U. U_TlmH_DUMP_MEA1	=dis
0244	CHECK MD1U. U_TlmH_DUMP_MEA2	=dis
0245	CHECK MD1U. U_TlmH_DUMP_MGFO	=dis
0246	CHECK MD1U. U_TlmH_DUMP_MIA	=dis
0247	CHECK MD1U. U_TlmH_DUMP_MSA	=dis
0248	CHECK MD1U. U_TlmH_CMP_DPU1	=CMP
0249	CHECK MD1U. U_TlmH_CMP_HEPE	=ARO
0250	CHECK MD1U. U_TlmH_CMP_HEPI	=ARO
0251	CHECK MD1U. U_TlmH_CMP_MEA1	=USR
0252	CHECK MD1U. U_TlmH_CMP_MEA2	=USR
0253	CHECK MD1U. U_TlmH_CMP_MGFO	=ARO
0254	CHECK MD1U. U_TlmH_CMP_MIA	=USR
0255	CHECK MD1U. U_TlmH_CMP_MSA	=USR
0256	CHECK MD2U. U_DpuNum	=DPU2
0257	CHECK MD2U. U_DpuRdn	=off

文 書 番 号	シート No.	版 数
	121	1

0258	CHECK MD2U. U_WdtEna	=ENA
0259	CHECK MD2U. U_DpuIfStatus	=nrm
0260	CHECK MD2U. U_HkMode	=CHK
0261	CHECK MD2U. U_HkRep_Ena	=ENA
0262	CHECK MD2U. U_TlmLM_Ena	=ENA
0263	CHECK MD2U. U_CmdDmc_NumBuf	0
0264	CHECK MD2U. U_CmdDmc_GetErr	0
0265	CHECK MD2U. U_CmdDmc_ExeErr_DPU	0
0266	CHECK MD2U. U_CmdDmc_ExeErr_EWO	0
0267	CHECK MD2U. U_CmdDmc_ExeErr_MDM	0
0268	CHECK MD2U. U_CmdDmc_ExeErr_MEF	0
0269	CHECK MD2U. U_CmdDmc_ExeErr_MGFI	0
0270	CHECK MD2U. U_CmdDmc_ExeErr_MSAS	0
0271	CHECK MD2U. U_CmdDmc_ExeErr_MWE	0
0272	CHECK MD2U. U_CmdDmc_ExeErr_SOR	0
0273	CHECK MD2U. U_CmdUsr_GetErr	0
0274	CHECK MD2U. U_CmdUsr_ExeErr_DPU	0
0275	CHECK MD2U. U_CmdUsr_ExeErr_EWO	0
0276	CHECK MD2U. U_CmdUsr_ExeErr_MDM	0
0277	CHECK MD2U. U_CmdUsr_ExeErr_MEF	0
0278	CHECK MD2U. U_CmdUsr_ExeErr_MGFI	0
0279	CHECK MD2U. U_CmdUsr_ExeErr_MSAS	0
0280	CHECK MD2U. U_CmdUsr_ExeErr_MWE	0
0281	CHECK MD2U. U_CmdUsr_ExeErr_SOR	0
0282	CHECK MD2U. U_Tlm_OpeMode	=nrm
0283	CHECK MD2U. U_TlmML_MSAS_CLCT	0
0284	CHECK MD2U. U_Hk_RepInt_DPU2	3
0285	CHECK MD2U. U_Hk_RepInt_EWO	3
0286	CHECK MD2U. U_Hk_RepInt_MEF	3
0287	CHECK MD2U. U_Hk_RepInt_MGFI	3
0288	CHECK MD2U. U_Hk_RepInt_MSAS	3
0289	CHECK MD2U. U_Hk_RepInt_MWE	3
0290	CHECK MD2U. U_TlmHk_CMP_DPU2	=non
0291	CHECK MD2U. U_TlmHk_CMP_EWO	=non
0292	CHECK MD2U. U_TlmHk_CMP_MDM	=non
0293	CHECK MD2U. U_TlmHk_CMP_MEF	=non
0294	CHECK MD2U. U_TlmHk_CMP_MGFI	=non
0295	CHECK MD2U. U_TlmHk_CMP_MSAS	=non
0296	CHECK MD2U. U_TlmHk_CMP_MWE	=non
0297	CHECK MD2U. U_TlmHk_CMP_SOR	=non
0298	CHECK MD2U. U_TlmML_CMP_DPU2	=CMP
0299	CHECK MD2U. U_TlmML_CMP_AM2P	=ARO
0300	CHECK MD2U. U_TlmML_CMP_EFD	=ARO
0301	CHECK MD2U. U_TlmML_CMP_EWOB	=ARO
0302	CHECK MD2U. U_TlmML_CMP_EWOE	=ARO
0303	CHECK MD2U. U_TlmML_CMP_MDM	=ARO
0304	CHECK MD2U. U_TlmML_CMP_MGFI	=ARO
0305	CHECK MD2U. U_TlmML_CMP_MSAS	=ARO
0306	CHECK MD2U. U_TlmML_CMP_SOR	=ARO
0307	CHECK MD2U. U_TlmH_DUMP_EFD	=dis
0308	CHECK MD2U. U_TlmH_DUMP_EWOB	=dis
0309	CHECK MD2U. U_TlmH_DUMP_EWOE	=dis

文 書 番 号	シート No.	版 数
	122	1

```

0310 CHECK MD2U.U_TlMh_DUMP_MGFI =dis
0311 CHECK MD2U.U_TlMh_CMP_DPU2 =CMP
0312 CHECK MD2U.U_TlMh_CMP_EFD =ARO
0313 CHECK MD2U.U_TlMh_CMP_EWOB =USR
0314 CHECK MD2U.U_TlMh_CMP_EWOE =USR
0315 CHECK MD2U.U_TlMh_CMP_MGFI =ARO
0316 #
0317 . # <<<< MDP: Check the TLM data contents >>>>
0318 . # -----
0319 . # * DPU-TLM-L 0x32 (50) for QL of TLM-L without REPRO
0320 . # * DPU-TLM-M 0x3C (60) for QL of TLM-M without REPRO
0321 . # -----
0322 #
0323 . # *****
0324 . # # WAIT the END of DUMP for 16sec-data (ARO) !!!!
0325 . # # [DPU1] MEA1 <5 sec
0326 . # # MEA2 <8 sec
0327 . # # MIA <13 sec
0328 . # # MSA <12 sec
0329 . # # HEP-e <9 sec
0330 . # # HEP-i <40 sec
0331 . # # MGf-O <7 sec --> ~100sec!
0332 . # # [DPU2] MGFI <7 sec
0333 . # # EFD <6 sec
0334 . # # EW0-B <110 sec
0335 . # # EW0-E <80 sec --> ~200sec!
0336 . # #
0337 . # # Originally, each data is 30-sec unit series.
0338 . # *****
0339 . #
0340 . # < CHECK >
0341 CHECK MD1F.MS0_BUFF_SEL =L
0342 CHECK MD1F.MS1_BUFF_SEL =L
0343 CHECK MD1F.MS2_BUFF_SEL =L
0344 CHECK MD1F.MS4_BUFF_SEL =L
0345 CHECK MD1F.MS5_BUFF_SEL =L
0346 CHECK MD1F.MS6_BUFF_SEL =L
0347 CHECK MD1F.MS7_BUFF_SEL =L
0348 CHECK MD2F.MS3_BUFF_SEL =L
0349 CHECK MD2F.MS5_BUFF_SEL =L
0350 # -----+-----+-----+-----
0351 # MD1U_USER_HK U_Blk_Clct_MEA1/MEA2/MIA/MSA/HEPE/HEPI/MGFO
0352 # ENA(if ON)
0353 # U_BlkS_Clct_MEA1/MEA2/MIA/MSA/HEPE/HEPI/MGFO
0354 # dis
0355 # U_TlMh_DUMP_MEA1/MEA2/MIA/MSA/HEPE/HEPI/MGF
0356 # dis
0357 # -----+-----+-----+-----
0358 # MD2U_USER_HK U_Blk_Clct_MGFI/EW0E/AM2P/EFD/EWOB ENA(if ON)
0359 # U_BlkS_Clct_MGFI/EW0E/AM2P/EFD/EWOB
0360 # U_TlMh_DUMP_MGFI/EW0E/AM2P/EFD/EWOB
0361 #

```

文 書 番 号	シート No.	版 数
	123	1

```

0362  #
0363  . # ==== 4 minutes after DR_REC_ON ====
0364  #
0365  . # < Data Recorder  Record stop >
0366  . DH.DR_REC_OFF
0367  #
0368  . # < CHECK >
0369  CHECK DH.DR_REC_ON_OFF          =OFF
0370  #
0371  . # < CHECK >
0372  LET MD1_GET = ({MD1U.U_CmdDmc_GetCNT} + 5 ) & 0x1F
0373  LET MD2_GET = ({MD2U.U_CmdDmc_GetCNT} + 5 ) & 0x1F
0374  LET MD1_EXE = ({MD1U.U_CmdDmc_ExecCNT} + 5 ) & 0x1F
0375  LET MD2_EXE = ({MD2U.U_CmdDmc_ExecCNT} + 5 ) & 0x1F
0376  #
0377  . # -----
0378  . # --- DUMP STOP (It can be skipped if it is already stopped) ---
0379  . # -----
0380  . DH.MDP_U_TLMH_TRG_STOP  # DH.MC_EACH_EXE 0x007E
0381  #:          << MACRO >> 内容について以下に示す(DH.DUMMYは省略)
0382  #:          0x00 MD2U.TLMH_TRG_STRT  0x00 0x00 0x00 0x00 0x00
0383  #:          0x01 MD1U.TLMH_TRG_STRT  0x00 0x00 0x00 0x00 0x00
0384  #:          0x03 MD2U.TLMH_TRG_SET   0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00
0385  #:          0x04 MD1U.TLMH_TRG_SET   0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00
0386  #:          0x06 MD2U.TLMH_DUMP      0x00 0x00 0x00 0x00 0x00 0x00 0x00
0387  #:          0x07 MD1U.TLMH_DUMP      0x00 0x00 0x00 0x00 0x00 0x00 0x00
0388  #:          0x09 MD2U.TLMH_DR_SET    0x00 0x00 0x00 0x00 0x00
0389  #:          0x0A MD1U.TLMH_DR_SET    0x00 0x00 0x00 0x00 0x00
0390  #:          0x0C MD2U.BUF_LONG        0xFF
0391  #:          0x0D MD1U.BUF_LONG        0xFF
0392  #
0393  . # < CHECK >
0394  CHECK MD1F.MS0_BUFF_SEL            =L
0395  CHECK MD1F.MS1_BUFF_SEL            =L
0396  CHECK MD1F.MS2_BUFF_SEL            =L
0397  CHECK MD1F.MS4_BUFF_SEL            =L
0398  CHECK MD1F.MS5_BUFF_SEL            =L
0399  CHECK MD1F.MS6_BUFF_SEL            =L
0400  CHECK MD1F.MS7_BUFF_SEL            =L
0401  CHECK MD2F.MS3_BUFF_SEL            =L
0402  CHECK MD2F.MS5_BUFF_SEL            =L
0403  SYSTEM test {MD1U.U_CmdDmc_GetCNT} = {MD1_GET}
0404  SYSTEM test {MD2U.U_CmdDmc_GetCNT} = {MD2_GET}
0405  SYSTEM test {MD1U.U_CmdDmc_ExecCNT} = {MD1_EXE}
0406  SYSTEM test {MD2U.U_CmdDmc_ExecCNT} = {MD2_EXE}
0407  #
0408  . # QL          Check-Item          Spec          Check
0409  # -----+-----+-----+-----+
0410  # MD1U_USER_HK  U_BiKL_Clct_MEA1/MEA2/MIA/MSA/HEPE/HEPI/MGFO
0411  #                                     ENA(if ON)

```

文 書 番 号	シート No.	版 数
TPP-FS8238-31K	124	1

```

0412 # U_BlkS_Clct_MEA1/MEA2/MIA/MSA/HEPE/HEPI/MGFO
0413 # dis
0414 # U_TlmH_DUMP_MEA1/MEA2/MIA/MSA/HEPE/HEPI/MGF
0415 # dis
0416 # -----+-----+-----+-----
0417 # MD2U_USER_HK U_BlkL_Clct_MGFI/EWOE/AM2P/EPD/EWOB ENA(if ON)
0418 # U_BlkS_Clct_MGFI/EWOE/AM2P/EPD/EWOB dis
0419 # U_TlmH_DUMP_MGFI/EWOE/AM2P/EPD/EWOB dis
0420 #
0421 # QL Check-Item Spec Check
0422 # -----+-----+-----+-----
0423 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0424 #
0425 # *****
0426 # 6. RF -> RD 切り替え(XTWT A OFF)
0427 # *****
0428 #
0429 # < Uplink OFF >
0430 #
0431 # 【局管制設定】
0432 # HOLD TONE OFF
0433 # CMD MOD OFF
0434 # Uplink OFF
0435 # 監視制御端末【X-TCRMOD】の IF を OFF する。
0436 # 監視制御端末【SUMMARY】の X-TX および X-RX の ATT の値を最大値(79dB)に戻す。
0437 #
0438 # 【地上装置設定】
0439 # RD LINK ON
0440 #
0441 # 【衛星管制設定】
0442 # RF/LINE 切換え: LINE
0443 # プリチェック : ON
0444 # センドベリファイ : 強制 TYPE-B
0445 # サクセスベリファイ: ON
0446 #
0447 # 【局管制設定】
0448 # SUMMARY 画面にて、X-TLM: 32kbps_RD を選択実行する。
0449 # SUMMARY 画面にて、BIT/VIT/FRM が全て ON となることを確認する。
0450 #
0451 # < CHECK >
0452 CHECK DH. XTRP_A_CARR_LOCK =OFF
0453 CHECK DH. XTRP_B_CARR_LOCK =OFF
0454 #
0455 TCIU. XTRP_B_TX_OFF
0456 #
0457 # < CHECK >
0458 CHECK DH. XTRP_B_TX_ON_OFF =OFF
0459 #
0460 TCIU. XTWT A_B_HV_OFF # 【有効寿命・特性値管理手順書(TPP-7S8238-02K)へ記録】
0461 #
0462 # < CHECK >
0463 CHECK ACS_SYS. TWTA_B_FIL_ON_OFF =ON

```

文 書 番 号	シート No.	版 数
	125	1

```

0464 CHECK ACS_SYS.TWTA_B_ARU_STATUS =DIS
0465 . #
0466 . TCIU.XTWTA_B_FIL_OFF1 # 【有効寿命・特性値管理手順書(TPP-7S8238-02K)へ記録】
0467 . #
0468 . # < CHECK >
0469 CHECK ACS_SYS.TWTA_B_FIL_ON_OFF =OFF
0470 . #
0471 . PCD.ON_OFF_ENA
0472 . #
0473 . # < CHECK >
0474 CHECK PCD.ON_OFF_ENA_DIS =ENA
0475 . #
0476 . PCD.EPC_B_OFF
0477 . #
0478 . # < CHECK >
0479 CHECK PCD.EPC_B_ON_OFF =OFF
0480 . #
0481 . PCD.ON_OFF_DIS
0482 . #
0483 . # < CHECK >
0484 CHECK PCD.ON_OFF_ENA_DIS =DIS
0485 . #
0486 . # QL Check-Item Spec Check
0487 . # -----+-----+-----+-----
0488 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0489 . #
0490 . # *****
0491 . # 7. DR Repro
0492 . # *****
0493 . #
0494 . DH.DR_RECNTNOTIFY
0495 . #
0496 . DH.DR_REPQUE_REG 41 0xFFFFFFFF 0xFFFFFFFF # MDP ONE-SHOT Packet
0497 . DH.DR_REPQUE_REG 44 0xFFFFFFFF 0xFFFFFFFF # MPPE/ENA Data Packet
0498 . DH.DR_REPQUE_REG 45 0xFFFFFFFF 0xFFFFFFFF # MDM Data Packet
0499 . DH.DR_REPQUE_REG 46 0xFFFFFFFF 0xFFFFFFFF # MSASI Data Packet (A)
0500 . DH.DR_REPQUE_REG 47 0xFFFFFFFF 0xFFFFFFFF # MSASI Data Packet (B)
0501 . DH.DR_REPQUE_REG 50 0xFFFFFFFF 0xFFFFFFFF # DPU L Data Packet
0502 . DH.DR_REPQUE_REG 51 0xFFFFFFFF 0xFFFFFFFF # MPPE-L L Data Packet
0503 . DH.DR_REPQUE_REG 52 0xFFFFFFFF 0xFFFFFFFF # MPPE-H L Data Packet
0504 . DH.DR_REPQUE_REG 53 0xFFFFFFFF 0xFFFFFFFF # MGF-O L Data Packet
0505 . DH.DR_REPQUE_REG 54 0xFFFFFFFF 0xFFFFFFFF # MGF-I L Data Packet
0506 . DH.DR_REPQUE_REG 55 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-E L Data Packet
0507 . DH.DR_REPQUE_REG 56 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-B L Data Packet
0508 . DH.DR_REPQUE_REG 57 0xFFFFFFFF 0xFFFFFFFF # PWI/EFD L Data Packet
0509 . DH.DR_REPQUE_REG 58 0xFFFFFFFF 0xFFFFFFFF # PWI/AM2P L Data Packet
0510 . DH.DR_REPQUE_REG 59 0xFFFFFFFF 0xFFFFFFFF # PWI/SORBET L Data Packet
0511 . DH.DR_REPQUE_REG 60 0xFFFFFFFF 0xFFFFFFFF # DPU M Data Packet
0512 . DH.DR_REPQUE_REG 61 0xFFFFFFFF 0xFFFFFFFF # MPPE-L M Data Packet
0513 . DH.DR_REPQUE_REG 62 0xFFFFFFFF 0xFFFFFFFF # MPPE-H M Data Packet
0514 . DH.DR_REPQUE_REG 63 0xFFFFFFFF 0xFFFFFFFF # MGF-O M Data Packet
0515 . DH.DR_REPQUE_REG 64 0xFFFFFFFF 0xFFFFFFFF # MGF-I M Data Packet

```

文 書 番 号	シート No.	版 数
	126	1

```

0516 DH. DR_REPQUE_REG 65 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-E M Data Packet
0517 DH. DR_REPQUE_REG 66 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-B M Data Packet
0518 DH. DR_REPQUE_REG 67 0xFFFFFFFF 0xFFFFFFFF # PWI/EFD M Data Packet
0519 DH. DR_REPQUE_REG 69 0xFFFFFFFF 0xFFFFFFFF # PWI/SORBET M Data Packet
0520 DH. DR_REPQUE_REG 70 0xFFFFFFFF 0xFFFFFFFF # DPU H0 Data Packet
0521 DH. DR_REPQUE_REG 71 0xFFFFFFFF 0xFFFFFFFF # MPPE-L H0 Data Packet
0522 DH. DR_REPQUE_REG 73 0xFFFFFFFF 0xFFFFFFFF # MGF-O H0 Data Packet
0523 DH. DR_REPQUE_REG 74 0xFFFFFFFF 0xFFFFFFFF # MGF-I H0 Data Packet
0524 DH. DR_REPQUE_REG 75 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-E H0 Data Packet
0525 DH. DR_REPQUE_REG 77 0xFFFFFFFF 0xFFFFFFFF # PWI/EFD H0 Data Packet
0526 DH. DR_REPQUE_REG 81 0xFFFFFFFF 0xFFFFFFFF # MPPE-L H1 Data Packet
0527 DH. DR_REPQUE_REG 82 0xFFFFFFFF 0xFFFFFFFF # MPPE-H H1 Data Packet
0528 DH. DR_REPQUE_REG 83 0xFFFFFFFF 0xFFFFFFFF # MGF-O H1 Data Packet
0529 DH. DR_REPQUE_REG 84 0xFFFFFFFF 0xFFFFFFFF # MGF-I H1 Data Packet
0530 DH. DR_REPQUE_REG 85 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-E H1 Data Packet
0531 DH. DR_REPQUE_REG 86 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-B H1 Data Packet
0532 DH. DR_REPQUE_REG 87 0xFFFFFFFF 0xFFFFFFFF # PWI/EFD H1 Data Packet
0533 DH. DR_REPQUE_REG 91 0xFFFFFFFF 0xFFFFFFFF # MPPE-L H2 Data Packet
0534 DH. DR_REPQUE_REG 92 0xFFFFFFFF 0xFFFFFFFF # MPPE-H H2 Data Packet
0535 DH. DR_REPQUE_REG 93 0xFFFFFFFF 0xFFFFFFFF # MGF-O H2 Data Packet
0536 DH. DR_REPQUE_REG 94 0xFFFFFFFF 0xFFFFFFFF # MGF-I H2 Data Packet
0537 DH. DR_REPQUE_REG 95 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-E H2 Data Packet
0538 DH. DR_REPQUE_REG 96 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-B H2 Data Packet
0539 DH. DR_REPQUE_REG 97 0xFFFFFFFF 0xFFFFFFFF # PWI/EFD H2 Data Packet
0540 DH. DR_REPQUE_REG 101 0xFFFFFFFF 0xFFFFFFFF # MPPE-L H3 Data Packet
0541 DH. DR_REPQUE_REG 102 0xFFFFFFFF 0xFFFFFFFF # MPPE-H H3 Data Packet
0542 DH. DR_REPQUE_REG 103 0xFFFFFFFF 0xFFFFFFFF # MGF-O H3 Data Packet
0543 DH. DR_REPQUE_REG 104 0xFFFFFFFF 0xFFFFFFFF # MGF-I H3 Data Packet
0544 DH. DR_REPQUE_REG 105 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-E H3 Data Packet
0545 DH. DR_REPQUE_REG 106 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-B H3 Data Packet
0546 DH. DR_REPQUE_REG 107 0xFFFFFFFF 0xFFFFFFFF # PWI/EFD H3 Data Packet
0547 DH. DR_REPQUE_REG 111 0xFFFFFFFF 0xFFFFFFFF # MPPE-L H4 Data Packet
0548 DH. DR_REPQUE_REG 112 0xFFFFFFFF 0xFFFFFFFF # MPPE-H H4 Data Packet
0549 DH. DR_REPQUE_REG 113 0xFFFFFFFF 0xFFFFFFFF # MGF-O H4 Data Packet
0550 DH. DR_REPQUE_REG 114 0xFFFFFFFF 0xFFFFFFFF # MGF-I H4 Data Packet
0551 DH. DR_REPQUE_REG 115 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-E H4 Data Packet
0552 DH. DR_REPQUE_REG 116 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-B H4 Data Packet
0553 DH. DR_REPQUE_REG 117 0xFFFFFFFF 0xFFFFFFFF # PWI/EFD H4 Data Packet
0554 DH. DR_REPQUE_REG 121 0xFFFFFFFF 0xFFFFFFFF # MPPE-L H5 Data Packet
0555 DH. DR_REPQUE_REG 122 0xFFFFFFFF 0xFFFFFFFF # MPPE-H H5 Data Packet
0556 DH. DR_REPQUE_REG 123 0xFFFFFFFF 0xFFFFFFFF # MGF-O H5 Data Packet
0557 DH. DR_REPQUE_REG 124 0xFFFFFFFF 0xFFFFFFFF # MGF-I H5 Data Packet
0558 DH. DR_REPQUE_REG 125 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-E H5 Data Packet
0559 DH. DR_REPQUE_REG 126 0xFFFFFFFF 0xFFFFFFFF # PWI/EWO-B H5 Data Packet
0560 DH. DR_REPQUE_REG 127 0xFFFFFFFF 0xFFFFFFFF # PWI/EFD H5 Data Packet
0561 #
0562 #
0563 . # < CHECK >
0564 CHECK DH. DR_REP_QUE_NUM 65
0565 . #
0566 . DH. DR_REPQUENOTIFY
0567 #

```

文 書 番 号	シート No.	版 数
	127	1

```

0568 . # < CHECK >
0569 LET MD1_GET = ( {MD1U. U_CmdDmc_GetCNT} + 1 ) & 0x1F
0570 LET MD2_GET = ( {MD2U. U_CmdDmc_GetCNT} + 1 ) & 0x1F
0571 LET MD1_EXE = ( {MD1U. U_CmdDmc_ExeCNT} + 1 ) & 0x1F
0572 LET MD2_EXE = ( {MD2U. U_CmdDmc_ExeCNT} + 1 ) & 0x1F
0573 . #
0574 . MD1U. HK_SET 0xFF 0x06
0575 . MD2U. HK_SET 0xFF 0x06
0576 . #
0577 . # < CHECK >
0578 SYSTEM test {MD1U. U_CmdDmc_GetCNT} = {MD1_GET}
0579 SYSTEM test {MD2U. U_CmdDmc_GetCNT} = {MD2_GET}
0580 SYSTEM test {MD1U. U_CmdDmc_ExeCNT} = {MD1_EXE}
0581 SYSTEM test {MD2U. U_CmdDmc_ExeCNT} = {MD2_EXE}
0582 . #
0583 . # QL                      Check-Item                      Spec                      Check
0584 . # -----+-----+-----+-----+
0585 . # MD1U_USER_HK   U_Hk_RepInt_DPU1/MEA1/MEA2/MIA/MSA/ENA/HEPE/HEPI/MGFO
0586 . #                                     6 (2^6 = 64sec)
0587 . # -----+-----+-----+-----+
0588 . # MD2U_USER_HK   U_Hk_RepInt_DPU2/MGFI/MDM/MSAS/EWOE/AM2P/EFD/EWOB/SOR
0589 . #                                     6 (2^6 = 64sec)
0590 . #
0591 . # Down Link Rate Change (64kbps)
0592 . #
0593 . DH. DLINK_RATE_CHNG 0x0E
0594 . #
0595 . # 【局管制 設定】
0596 . # SUMMARY 画面にて、X-TLM: 64kbps_RD を選択実行する。
0597 . # SUMMARY 画面にて、BIT/VIT/FRM が全て ON となることを確認する。
0598 . #
0599 . # < CHECK >
0600 CHECK DH. DWNLNK_RATE                      =BPS65536
0601 . #
0602 . DH. DR_REP_ON
0603 . #
0604 . # < CHECK >
0605 CHECK DH. DR_REPRO_ON_OFF                  =ON
0606 . #
0607 . # < REPRO 終了まで待機 >
0608 . #
0609 . # < CHECK >
0610 CHECK DH. DR_REPRO_ON_OFF                  =OFF
0611 . #
0612 . DH. OPE_MODE_32KBPS_CAT0x26 0x03 0x000000000000000000000000
0613 . #
0614 . # 【局管制 設定】
0615 . # SUMMARY 画面にて、X-TLM: 32kbps_RD を選択実行する。
0616 . # SUMMARY 画面にて、BIT/VIT/FRM が全て ON となることを確認する。
0617 . #
0618 . # < CHECK >
0619 CHECK DH. CATEGORY_TBL_NO                  0x26

```

```

0620 CHECK DH.DWNLNK_RATE =BPS32768
0621 LET MD1_GET = ({MD1U.U_CmdDmc_GetCNT} + 1 ) & 0x1F
0622 LET MD2_GET = ({MD2U.U_CmdDmc_GetCNT} + 1 ) & 0x1F
0623 LET MD1_EXE = ({MD1U.U_CmdDmc_ExecCNT} + 1 ) & 0x1F
0624 LET MD2_EXE = ({MD2U.U_CmdDmc_ExecCNT} + 1 ) & 0x1F
0625 . #
0626 . MD1U.HK_SET 0xFF 0x03
0627 . MD2U.HK_SET 0xFF 0x03
0628 . #
0629 . # < CHECK >
0630 SYSTEM test {MD1U.U_CmdDmc_GetCNT} = {MD1_GET}
0631 SYSTEM test {MD2U.U_CmdDmc_GetCNT} = {MD2_GET}
0632 SYSTEM test {MD1U.U_CmdDmc_ExecCNT} = {MD1_EXE}
0633 SYSTEM test {MD2U.U_CmdDmc_ExecCNT} = {MD2_EXE}
0634 . #
0635 . # QL Check-Item Spec Check
0636 . # -----+-----+-----+-----
0637 . # MD1U_USER_HK U_Hk_RepInt_DPU1/MEA1/MEA2/MIA/MSA/ENA/HEPE/HEPI/MGFO
0638 . # 3 (2^3 = 8sec)
0639 . # -----+-----+-----+-----
0640 . # MD2U_USER_HK U_Hk_RepInt_DPU2/MGFI/MDM/MSAS/EWOE/AM2P/EFD/EWOB/SOR
0641 . # 3 (2^3 = 8sec)
0642 . #
0643 . # QL Check-Item Spec Check
0644 . # -----+-----+-----+-----
0645 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0646 . #
0647 . # *****
0648 . # 8. 機器立ち下げ
0649 . # *****
0650 . # =====
0651 . # 8.1. SI 系立ち下げ
0652 . # =====
0653 . #
0654 . # < MSASI_OFF >
0655 . #
0656 . MSAS.TO_POWER_OFF
0657 . #
0658 . # < CHECK >
0659 CHECK MSAS.HV_ON_OFF =OFF
0660 CHECK MSAS.HV_MONI_N4K =0.00
0661 CHECK MSAS.HV_MONI_P6K =0.00
0662 CHECK MSAS.HT_CTRL_ON_OFF =OFF
0663 CHECK MSAS.HT_ON_OFF =OFF
0664 CHECK MSAS.G_ON_OFF =OFF
0665 . #
0666 . # QL Check-Item Spec Data Check
0667 . # -----+-----+-----+-----+-----
0668 . # P15V_V_L 12-16V _____V G N
0669 . # P15V_I_L 0.01-0.3A _____A G N
0670 . # M15V_V_L 12-16V _____V G N
0671 . # M15V_I_L 0.01-0.3A _____A G N

```

文 書 番 号	シート No.	版 数
	129	1

```

0672 # P5V_V_L 4. 5-5. 2V _____V G N
0673 # P5V_I_L 0. 1-1. 0A _____A G N
0674 #
0675 . # # MSASI_OFF
0676 . DH. MSASI_OFF # DH. MC_EACH_EXE 0x00AA
0677 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0678 #: 0x00 PCD. ON_OFF_ENA
0679 #: 0x01 PCD. MSASI_OFF
0680 #: 0x02 PCD. ON_OFF_DIS
0681 #
0682 . # < CHECK >
0683 CHECK PCD. MSASI_ON_OFF =OFF
0684 CHECK MD2F. MS6_LINK_STAT =OFF
0685 CHECK MD2F. MS6_DAT_CLCT =OFF
0686 #
0687 . # < MDM_OFF >
0688 . #
0689 . DH. MDM_OFF # DH. MC_EACH_EXE 0x00AB
0690 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0691 #: 0x00 MD2F. MDM_CLCT_STOP
0692 #: 0x0D PCD. ON_OFF_ENA
0693 #: 0x0E PCD. MDM_E_OFF
0694 #: 0x0F PCD. ON_OFF_DIS
0695 #
0696 . # < CHECK >
0697 CHECK PCD. MDM_E_ON_OFF =OFF
0698 CHECK MD2F. MS7_LINK_STAT =OFF
0699 CHECK MD2F. MS7_DAT_CLCT =OFF
0700 #
0701 . # < MGF OFF >
0702 . #
0703 . DH. MGF_OFF # DH. MC_EACH_EXE 0x00AC
0704 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0705 #: 0x00 MD1F. MGFO_CLCT_STOP
0706 #: 0x01 MD2F. MGFI_CLCT_STOP
0707 #: 0x0C MD1F. PME_MGFO_OFF
0708 #: 0x0D PCD. ON_OFF_ENA
0709 #: 0x0E PCD. PSU_MGF_I_OFF
0710 #: 0x0F PCD. ON_OFF_DIS
0711 #
0712 . # < CHECK >
0713 CHECK MD1F. MS0_DAT_CLCT =OFF
0714 CHECK MD2F. MS5_DAT_CLCT =OFF
0715 CHECK MD1F. PME_MGFO_STS =OFF
0716 CHECK PCD. PSU_MGF_I_ON_OFF =OFF
0717 CHECK MD1F. MS0_LINK_STAT =OFF
0718 CHECK MD2F. MS5_LINK_STAT =OFF
0719 #
0720 . # < MWE STBY OFF >
0721 . #
0722 . MWE. MWE_STBY_OFF
0723 . #

```

文 書 番 号	シート No.	版 数
	130	1

```

0724 . #
0725 . # < CHECK >
0726 CHECK MWE. STAT_STBY =OFF
0727 . #
0728 . # < MWE_OFF >
0729 . #
0730 . # # MWE の POWER を OFF にする
0731 . MD2F. PME_MAST_OFF
0732 . #
0733 . # < CHECK >
0734 CHECK MD2F. PME_MAST_STS =OFF
0735 CHECK MD2F. MSO_LINK_STAT =OFF
0736 . #
0737 . # < PWI OFF >
0738 . #
0739 . DH. PWI_OFF1_AM2P_MEF_PRE # DH. MC_EACH_EXE 0x008B
0740 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0741 #: 0x00 MD2U. EWO_AM2P_INT_SET 0x0000 0x00
0742 #: 0x0D MD2F. PME_MEFISTO_OFF
0743 #: 0x0E EWO. SCP_OFF
0744 #: 0x0F EWO. WPTP_OFF
0745 . #
0746 . # < CHECK >
0747 CHECK MD2F. MS4_LINK_STAT =OFF
0748 CHECK MD2F. PME_MEFISTO_STS =OFF
0749 CHECK EWO. SCP_ONOF =OFF
0750 CHECK EWO. WPTP_ONOF =OFF
0751 . #
0752 . DH. PWI_OFF2_AM2P_SOR_EWO # DH. MC_EACH_EXE 0x00AD
0753 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0754 #: 0x00 MD2F. PME_AM2P_OFF
0755 #: 0x01 MD2F. SORB_CLCT_STOP
0756 #: 0x02 MD2F. PME_SORBET_OFF
0757 #: 0x03 MD2U. BUF_CLCT 0x10 0x00
0758 #: 0x0F MD2F. PME_EWO_OFF
0759 . #
0760 . # < CHECK >
0761 CHECK MD2F. MS3_DAT_CLCT =OFF
0762 CHECK MD2F. PME_SORBET_STS =OFF
0763 CHECK MD2F. PME_AM2P_STS =OFF
0764 CHECK MD2F. PME_EWO_STS =OFF
0765 . #
0766 . # < PME_OFF >
0767 . #
0768 . # # PME-PSU_OFF
0769 . DH. PME_OFF # DH. MC_EACH_EXE 0x00AE
0770 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0771 #: 0x00 PCD. ON_OFF_ENA
0772 #: 0x01 PCD. PSU_PME_OFF
0773 #: 0x02 PCD. ON_OFF_DIS
0774 . #
0775 . # < CHECK >

```

文 書 番 号	シート No.	版 数
	131	1

```

0776 CHECK PCD.PSU_PME_ON_OFF          =OFF
0777 #
0778 . # < HEP1_OFF >
0779 . #
0780 . # # HEP-ion OFF
0781 . DH.HEP_i_OFF # DH.MC_EACH_EXE 0x00A7
0782 #:          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
0783 #:          0x00 MD1F.HEP1_CLCT_STOP
0784 #:          0x0D PCD.ON_OFF_ENA
0785 #:          0x0E PCD.PSU_HEP_i_OFF
0786 #:          0x0F PCD.ON_OFF_DIS
0787 #
0788 . # < CHECK >
0789 CHECK PCD.PSU_HEP_I_ON_OFF          =OFF
0790 CHECK MD1F.MS7_LINK_STAT            =OFF
0791 CHECK MD1F.MS7_DAT_CLCT            =OFF
0792 #
0793 . # < HEPE_OFF >
0794 . #
0795 . # # HEP-ele OFF
0796 . DH.HEP_e_OFF # DH.MC_EACH_EXE 0x00A9
0797 #:          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
0798 #:          0x00 MD1F.HEPE_CLCT_STOP
0799 #:          0x0D PCD.ON_OFF_ENA
0800 #:          0x0E PCD.PSU_HEP_e_OFF
0801 #:          0x0F PCD.ON_OFF_DIS
0802 #
0803 . # < CHECK >
0804 CHECK PCD.PSU_HEP_E_ON_OFF          =OFF
0805 CHECK MD1F.MS6_LINK_STAT            =OFF
0806 CHECK MD1F.MS6_DAT_CLCT            =OFF
0807 #
0808 . # < MIA_OFF >
0809 . #
0810 . # # MIA SHUTDOWN
0811 . MIA.SHUTDOWN 0x0000
0812 #
0813 . # < CHECK >
0814 CHECK MIA.SVG_ED                    =DIS
0815 CHECK MIA.SVG_ON_OFF                =OFF
0816 CHECK MIA.SVG                      0 40
0817 CHECK MIA.SVG_TGT                   0x00
0818 CHECK MIA.SVG_LVL                   0x00
0819 CHECK MIA.SVS_ED                    =DIS
0820 CHECK MIA.SVS_ON_OFF                =OFF
0821 CHECK MIA.SVS                      -40 0
0822 CHECK MIA.SVS_TGT                   0x00
0823 CHECK MIA.SVS_LVL                   0x00
0824 CHECK MIA.MHV_ED                    =DIS
0825 CHECK MIA.MHV_ON_OFF                =OFF
0826 CHECK MIA.MHV                      -40 0
0827 CHECK MIA.MHV_TGT                   0x00

```

文 書 番 号	シート No.	版 数
	132	1

```

0828 CHECK MIA.MHV_LVL 0x00
0829 CHECK MIA.MCP_I -2 2
0830 #
0831 . # # MIA OFF
0832 . DH.MIA_OFF # DH.MC_EACH_EXE 0x00A5
0833 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
0834 #: 0x00 MD1F.MIA_CLCT_STOP
0835 #: 0x0D PCD.ON_OFF_ENA
0836 #: 0x0E PCD.PSU_MIA_OFF
0837 #: 0x0F PCD.ON_OFF_DIS
0838 #
0839 . # < CHECK >
0840 CHECK PCD.PSU_MIA_ON_OFF =OFF
0841 CHECK MD1F.MS4_LINK_STAT =OFF
0842 CHECK MD1F.MS4_DAT_CLCT =OFF
0843 #
0844 . # # < MSA_OFF >
0845 #
0846 . # # HV_OFF
0847 . MSA.HV_OFF 0x0A000000 # command : 09000a000000
0848 #
0849 . # # HV_sw_disable
0850 . MSA.HV_DIS 0x0A000000 # command : 08000a000000
0851 #
0852 . # < CHECK >
0853 CHECK MSA.CPU_ENA_BOARD =DIS
0854 CHECK MSA.HV_DAC_HV1 00
0855 CHECK MSA.HV_DAC_HV2 00
0856 CHECK MSA.HV_DAC_VHV1 00
0857 CHECK MSA.HV_DAC_VHV2 00
0858 CHECK MSA.HV_DAC_VHV4 00
0859 CHECK MSA.HV_DAC_AMP1 00
0860 CHECK MSA.HV_DAC_AMP2 00
0861 CHECK MSA.HV_DAC_AMP3 00
0862 . #
0863 . # # MSA OFF
0864 . DH.MSA_OFF # DH.MC_EACH_EXE 0x00A6
0865 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
0866 #: 0x00 MD1F.MSA_CLCT_STOP
0867 #: 0x0D PCD.ON_OFF_ENA
0868 #: 0x0E PCD.PSU_MSA_OFF
0869 #: 0x0F PCD.ON_OFF_DIS
0870 #
0871 . # < CHECK >
0872 CHECK PCD.PSU_MSA_ON_OFF =OFF
0873 CHECK MD1F.MS5_LINK_STAT =OFF
0874 CHECK MD1F.MS5_DAT_CLCT =OFF
0875 #
0876 . # < MEA2_OFF >
0877 . #
0878 . MEA2.HV_DIS
0879 #

```

文 書 番 号	シート No.	版 数
	133	1

```

0880 . # < CHECK >
0881 CHECK MEA2.l1_cmd_exe                      =0x1A00
0882 CHECK MEA2.Status_reg                      0x83
0883 #
0884 . # # MEA2 OFF
0885 . DH.MEA2_OFF # DH.MC_EACH_EXE 0x00A4
0886 #:      << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
0887 #:      0x00 MD1F.MEA2_CLCT_STOP
0888 #:      0x0D PCD.ON_OFF_ENA
0889 #:      0x0E PCD.PSU_MEA2_OFF
0890 #:      0x0F PCD.ON_OFF_DIS
0891 #
0892 . # < CHECK >
0893 CHECK PCD.PSU_MEA2_ON_OFF                    =OFF
0894 CHECK MD1F.MS2_LINK_STAT                    =OFF
0895 CHECK MD1F.MS2_DAT_CLCT                    =OFF
0896 #
0897 . # < MEA1_OFF >
0898 . #
0899 . MEA1.HV_DIS
0900 #
0901 . # < CHECK >
0902 CHECK MEA1.l1_cmd_exe                      =0x1A00
0903 CHECK MEA1.Status_reg                      0x83
0904 #
0905 . # # MEA1 OFF
0906 . DH.MEA1_OFF # DH.MC_EACH_EXE 0x00A3
0907 #:      << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
0908 #:      0x00 MD1F.MEA1_CLCT_STOP
0909 #:      0x0D PCD.ON_OFF_ENA
0910 #:      0x0E PCD.PSU_MEA1_OFF
0911 #:      0x0F PCD.ON_OFF_DIS
0912 #
0913 . # < CHECK >
0914 CHECK PCD.PSU_MEA1_ON_OFF                    =OFF
0915 CHECK MD1F.MS1_LINK_STAT                    =OFF
0916 CHECK MD1F.MS1_DAT_CLCT                    =OFF
0917 #
0918 #
0919 . # < ENA_OFF >
0920 . #
0921 . ENA.SHUTDOWN 0x00
0922 #
0923 . # < CHECK >
0924 CHECK ENA.HV_MAIN_LVLSET                    259.163 # 0x00
0925 CHECK ENA.HV_STARTMCP_LVLSET                -912.18 # 0x00
0926 CHECK ENA.HV_STOPMCP_LVLSET                -1095.1 # 0x00
0927 CHECK ENA.HV_DEF_LVLSET                    -158.482 # 0x00
0928 CHECK ENA.HV_ON                            =OFF
0929 CHECK ENA.HV_SHUTDOWN_MODE                 =NML
0930 . #
0931 . ENA.CLEAR_FLG 0x03

```

```

0932  #
0933  . # < CHECK >
0934  CHECK ENA. HV_SHUTDOWN_MODE                =NON
0935  . #
0936  . DH. ENA_OFF    # DH. MC_EACH_EXE 0x00A8
0937  #:              << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0938  #:              0x00 MD1F. ENA_CLCT_STOP
0939  #:              0x0D PCD. ON_OFF_ENA
0940  #:              0x0E PCD. PSU_ENA_OFF
0941  #:              0x0F PCD. ON_OFF_DIS
0942  #
0943  . # < CHECK >
0944  CHECK PCD. PSU_ENA_ON_OFF                    =OFF
0945  CHECK MD1F. MS3_LINK_STAT                    =OFF
0946  CHECK MD1F. MS3_DAT_CLCT                    =OFF
0947  . #
0948  #
0949  . # QL          Check-Item                    Spec                    Check
0950  # -----+-----+-----+-----+
0951  # CMD 出力装置  Message                    STATUS CHECK 結果 OK    G    N
0952  #
0953  # =====
0954  . # 8.2. MDP 立ち下げ
0955  # =====
0956  #
0957  . # <<< MDP-DPU RST/OFF >>>
0958  . DH. MDP_DPU12_CPU_OFF    # DH. MC_EACH_EXE 0x0B1
0959  #:              << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0960  #:              0x00 DH. ATMC_EACH_DIS 0x32
0961  #:              0x03 DH. ATMC_EACH_DIS 0x33
0962  #:              0x06 DH. ATMC_EACH_DIS 0x30
0963  #:              0x09 DH. ATMC_EACH_DIS 0x31
0964  #:              0x0A MD1F. CPU_RESET_ENA
0965  #:              0x0B MD1F. CPU_RESET
0966  #:              0x0C MD1F. CPU_OFF
0967  #:              0x0D MD2F. CPU_RESET_ENA
0968  #:              0x0E MD2F. CPU_RESET
0969  #:              0x0F MD2F. CPU_OFF
0970  #
0971  . #
0972  . # < CHECK >
0973  CHECK DH. ATMC_ENA_DIS30                    =DIS
0974  CHECK DH. ATMC_ENA_DIS31                    =DIS
0975  CHECK DH. ATMC_ENA_DIS32                    =DIS
0976  CHECK DH. ATMC_ENA_DIS33                    =DIS
0977  #
0978  . # QL          Check-Item                    Spec                    Check
0979  # -----+-----+-----+-----+
0980  # MD1F_OBS_HK    << DPU1 H/W-HK >>
0981  #                HW_STATE2                    CPU_RST_ENA (short)
0982  #                HWCMD_CODE                    0x0801 (short)
0983  #                HW_STATE2                    STANDBY (short)

```

文 書 番 号	シート No.	版 数
	135	1

```

0984 # HWCMD_CODE 0x0702 (short)
0985 . #
0986 . #
0987 . # < CHECK >
0988 CHECK MD1F.DPU_HW_STATE2 =CPUOFF_DPUBUS
0989 CHECK MD1F.DPU_CMD_CODE 0x0202
0990 #
0991 . # QL Check-Item Spec Check
0992 # -----+-----+-----+-----
0993 # MD2F_OBS_HK << DPU2 H/W-HK >>
0994 # HW_STATE2 CPU_RST_ENA (short)
0995 # HWCMD_CODE 0x0801 (short)
0996 # HW_STATE2 STANDBY (short)
0997 # HWCMD_CODE 0x0702 (short)
0998 . #
0999 . #
1000 . # < CHECK >
1001 CHECK MD2F.DPU_HW_STATE2 =CPUOFF_DPUBUS
1002 CHECK MD2F.DPU_CMD_CODE 0x0202
1003 #
1004 . # Telemetry Operation Mode 設定
1005 . DH.OPE_MODE_32KBPS_CAT0x23 0x00 0x00000000000000000000000000000000
1006 #
1007 . # <<< MDP-DPU1/2 Power OFF >>>
1008 . DH.MDP_DPU12_POWER_OFF # DH.MC_EACH_EXE 0x0B0
1009 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
1010 #: 0x00 DH.ATMC_EACH_DIS 0x34
1011 #: 0x01 DH.SPW_RTEFDIR_SET 0x18
1012 #: 0x03 DH.ATMC_EACH_DIS 0x36
1013 #: 0x06 DH.ATMC_EACH_DIS 0x32
1014 #: 0x09 DH.ATMC_EACH_DIS 0x33
1015 #: 0x0A PCD.ON_OFF_ENA
1016 #: 0x0B PCD.PSU_MDP1_OFF
1017 #: 0x0C DH.ATMC_EACH_DIS 0x30
1018 #: 0x0D PCD.PSU_MDP2_OFF
1019 #: 0x0E PCD.ON_OFF_DIS
1020 #: 0x0F DH.ATMC_EACH_DIS 0x31
1021 #
1022 . #
1023 . # < CHECK >
1024 CHECK DH.ATMC_ENA_DIS30 =DIS
1025 CHECK DH.ATMC_ENA_DIS31 =DIS
1026 CHECK DH.ATMC_ENA_DIS32 =DIS
1027 CHECK DH.ATMC_ENA_DIS33 =DIS
1028 CHECK DH.ATMC_ENA_DIS34 =DIS
1029 CHECK DH.ATMC_ENA_DIS36 =DIS
1030 CHECK DH.SPW_FDIR_ENA_MDP1 =DIS
1031 CHECK DH.SPW_FDIR_ENA_MDP2 =DIS
1032 CHECK DH.SPW_FDIR_ENA_PCD =ENA
1033 CHECK DH.SPW_FDIR_ENA_RMTRTR =ENA
1034 CHECK PCD.PSU_MDP1_ON_OFF =OFF
1035 CHECK PCD.PSU_MDP2_ON_OFF =OFF

```

文 書 番 号	シート No.	版 数
	136	1

```

1036 CHECK PCD.ON_OFF_ENA_DIS =DIS
1037 . #
1038 . # QL Check-Item Spec Check
1039 . # -----+-----+-----
1040 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1041 . #
1042 . # =====
1043 . # 8.3. DRU/ACFS センサ/HTR CTRL OFF
1044 . # =====
1045 . #
1046 . TCS.MODE_STBY
1047 . #
1048 . # < CHECK >
1049 CHECK TCS.MODE_ST =STBY
1050 . #
1051 . TCS.CTRL_HTR_P1_DIS
1052 . TCS.CTRL_HTR_P2_DIS
1053 . TCS.CTRL_HTR_P3_DIS
1054 . TCS.CTRL_HTR_P4_DIS
1055 . #
1056 . # < CHECK >
1057 CHECK TCS.HTR01_P_VOL_ST =OFF
1058 CHECK TCS.HTR09_P_VOL_ST =OFF
1059 CHECK TCS.HTR17_P_VOL_ST =OFF
1060 CHECK TCS.HTR25_P_VOL_ST =OFF
1061 . #
1062 . ACS.DRU_SEL_NA
1063 . #
1064 . # < CHECK >
1065 CHECK ACS.DRU_SEL_PT =NA
1066 CHECK ACS_SYS.DRU_SEL_PT =NA
1067 CHECK ACS.BIT_DRU_SEL_PT =NA
1068 . #
1069 . PCD.ON_OFF_ENA
1070 . #
1071 . # < CHECK >
1072 CHECK PCD.ON_OFF_ENA_DIS =ENA
1073 . #
1074 . PCD.DRU_PSU_A_OFF
1075 . #
1076 . # < CHECK >
1077 CHECK PCD.DRU_PSU_A_ON_OFF =OFF
1078 . #
1079 . ACS.SSC_OFF
1080 . #
1081 . # < CHECK >
1082 CHECK ACS.SSC_PWR =OFF
1083 CHECK ACS_SYS.SSC_PWR =OFF
1084 CHECK ACS.BIT_SSC_PWR =OFF
1085 . #
1086 . TCIU.SSAS_A_OFF
1087 . #

```

文 書 番 号	シート No.	版 数
	137	1

```

1088 . # < CHECK >
1089 CHECK ACS.STS_SSASA_PWR =OFF
1090 CHECK ACS.STS_SSASB_PWR =OFF
1091 CHECK ACS_SYS.STS_SSASA_PWR =OFF
1092 CHECK ACS_SYS.STS_SSASB_PWR =OFF
1093 CHECK ACS.BIT_STS_SSASA_PWR =OFF
1094 CHECK ACS.BIT_STS_SSASB_PWR =OFF
1095 . #
1096 . PCD.ON_OFF_DIS
1097 . #
1098 . # < CHECK >
1099 CHECK PCD.ON_OFF_ENA_DIS =DIS
1100 . #
1101 . DH.XSW1_STRATE # DH.MC_EACH_EXE 0x0066
1102 . TCIU.XSW2_STRATE
1103 . #
1104 . # < CHECK >
1105 CHECK DH.XSW1_PARA_CROSS_SEL =HGA_TO_TWTA_A
1106 CHECK DH.XSW2_PARA_CROSS_SEL =HGA_TO_XTRP_A
1107 . #
1108 . # QL Check-Item Spec Check
1109 . # -----+-----+-----+-----
1110 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1111 . #
1112 . # *****
1113 . # 9. 立ち下げ
1114 . # *****
1115 . # =====
1116 . # 9.1. MC_DIS
1117 . # =====
1118 . #
1119 . DH.ATMC_DIS
1120 . #
1121 . # < CHECK >
1122 CHECK DH.ATMC_ENA_DIS =DIS
1123 . #
1124 . DH.RQMC_DIS
1125 . #
1126 . # < CHECK >
1127 CHECK DH.RQMC_ENA_DIS =DIS
1128 . #
1129 . DH.SYSTMR_DIS
1130 . #
1131 . # < CHECK >
1132 CHECK DH.SYSTMR_ENA_DIS =DIS
1133 . #
1134 . DH.ALL_MC_STOP
1135 . #
1136 . # < CHECK >
1137 CHECK DH.MC_ENA_DIS =DIS
1138 . #
1139 . # QL Check-Item Spec Check

```

```

1140 # -----+-----+-----+-----
1141 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1142 #
1143 # =====
1144 . # 9.2. 衛星 OFF
1145 # =====
1146 . #
1147 . # QL Check-Item Spec Check
1148 # -----+-----+-----+-----
1149 # PCS1 PCD. CHG_CC_LO ON G N
1150 # PCD. BAT_CV_ON_OFF OFF G N
1151 # PCD. BAT_OT_ON_OFF OFF G N
1152 # PCD. BAT_OV_ON_OFF OFF G N
1153 # PCD. TMR_CNT 0 G N
1154 # DMC-PSU-A, -B, XTRP-A, B, SV-Heater 以外が OFF であること G N
1155 # TCIU DH. XTRP_A_TX_ON_OFF OFF G N
1156 # DH. XTRP_B_TX_ON_OFF OFF G N
1157 # 局管制 Uplink OFF G N
1158 #
1159 #
1160 . # 【注意】
1161 . # CV_ON_OFF が ON になっている時は、必ず CV_OFF にすること。
1162 . # CC_LO ON で無い場合は、CCCV LO モードへ移行すること。
1163 . #
1164 . PCD. CHG_LR1_ON
1165 #
1166 . # < CHECK >
1167 CHECK PCD. CHG_LR1_ON_OFF =ON
1168 CHECK PCD. CHG_I -0.1 0.1
1169 CHECK PCD. DCHG_I -0.2 0.2
1170 #
1171 . #
1172 . PCD. BUS_SW_ENA
1173 #
1174 . # < CHECK >
1175 CHECK PCD. BUS_SW_ENA_DIS =ENA
1176 #
1177 . #
1178 . PCD. BUS_SW_OFF
1179 #
1180 . # < CHECK >
1181 CHECK PCD. BUS_SW_ENA_DIS =DIS
1182 CHECK PCD. BUS_SW_ON_OFF =OFF
1183 #
1184 . #
1185 . PCD. CHG_ENA
1186 PCD. CHG_CC_L2
1187 #
1188 . # < CHECK >
1189 CHECK PCD. CHG_ENA_DIS =ENA
1190 CHECK PCD. CHG_CC_L1_2 =L2
1191 CHECK PCD. CHG_CC_LO =OFF

```

文 書 番 号	シート No.	版 数
	139	1

```

1192  #
1193  . #
1194  . PCD. CHG_DIS
1195  #
1196  . # < CHECK >
1197  CHECK PCD. CHG_ENA_DIS          =DIS
1198  CHECK PCD. CHG_I                0.1 0.3
1199  CHECK PCD. DCHG_I                -0.2 0.2
1200  #
1201  . #
1202  . PCD. CHG_LR_OFF
1203  #
1204  . # < CHECK >
1205  CHECK PCD. CHG_LR1_ON_OFF        =OFF
1206  CHECK PCD. CHG_LR2_ON_OFF        =OFF
1207  CHECK PCD. CHG_I                -0.1 0.1
1208  CHECK PCD. DCHG_I                -0.2 0.2
1209  #
1210  . # 【確認】                      Check
1211  # BUS-V > BAT-V であることを確認する。      G N
1212  . #
1213  . PCD. INT_EXT_ENA
1214  . PCD. BAT_EXT
1215  #
1216  . # < CHECK >
1217  CHECK PCD. INT_EXT_ENA_DIS        =DIS
1218  CHECK PCD. BAT_INT_EXT            =EXT
1219  . #
1220  . PCD. CHG_BYPS_DIS
1221  #
1222  . # < CHECK >
1223  CHECK PCD. CHG_BYPS_ENA_DIS        255
1224  CHECK PCD. SA_V                  -
1225  CHECK PCD. SA_I                  -
1226  CHECK PCD. APR1_I                -
1227  CHECK PCD. APR2_I                -
1228  CHECK PCD. BUS_V                  -
1229  CHECK PCD. LOAD_I                 -
1230  CHECK PCD. CHG_I                  -
1231  CHECK PCD. DCHG_I                 -
1232  CHECK PCD. BAT_V                  -
1233  CHECK PCD. CEL1_V                 -
1234  CHECK PCD. CEL2_V                 -
1235  CHECK PCD. CEL3_V                 -
1236  CHECK PCD. CEL4_V                 -
1237  CHECK PCD. CEL5_V                 -
1238  CHECK PCD. CEL6_V                 -
1239  CHECK PCD. CEL7_V                 -
1240  CHECK PCD. CEL8_V                 -
1241  CHECK PCD. CEL9_V                 -
1242  CHECK PCD. CEL10_V                -
1243  CHECK PCD. CEL11_V                -

```

文 書 番 号	シート No.	版 数
	140	1

```

1244 CHECK PCD.CEL12_V -
1245 CHECK PCD.BAT_CV -
1246 CHECK PCD.BAT_CV_ON_OFF -
1247 . #
1248 . #
1249 . PCD.CEL_V_MONI_OFF # 【BAT 管理手順書 (TPP-7S8238-03K) へ記録】
1250 . #
1251 . # < CHECK >
1252 CHECK PCD.CEL_V_MONI_ON_OFF =OFF
1253 CHECK PCD.BAT_CV -
1254 CHECK PCD.BAT_CV_ON_OFF -
1255 . #
1256 . # 【操作】
1257 . # RD LINK OFF
1258 . # EXT-PS OFF
1259 . # BAT_MEASURE STOP (BAT 温度計測停止)
1260 . #
1261 . # QL Check-Item Spec Check
1262 . # -----+-----+-----+-----
1263 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1264 . #
1265 . # =====
1266 . # 9.3. 試験コンフィギュレーションの解除
1267 . # =====
1268 . #
1269 . # 【MIS(MPO Interface Simulator)】
1270 . # (1) System Control → MFE → Bus profile
1271 . # → Polling Sequence Table Control → stop
1272 . # (2) System Control → PFE → Power Control Bus A → ON をクリック → OFF
1273 . # (3) External Bus Power Supply の、DC VOLTS 表示=OFF を確認する
1274 . # (4) PFE Platform の Under Voltage の LED=点灯を確認する
1275 . # (5) Main Menu: Mode -> System Off-Line Mode を選択する
1276 . # (6) Confirm → YES
1277 . # (7) Main Menu: File -> Exit を選択する
1278 . # (8) Exit System 画面において、以下を選択し、OK ボタンをクリック
1279 . # Power FE : S/W Shutdown
1280 . # MIL-1553B FE : Shutdown
1281 . # Discrete FE : Shutdown
1282 . # CMDVS Core Environment : S/W Shutdown
1283 . # (9) MIS 本体の電源を以下の順に OFF する
1284 . # 1. MIL-1553/Discrete Front-End
1285 . # 2. PFE Platform
1286 . # 3. External Bus Power Supply (Agilent N5776A)
1287 . # (10) PC をシャットダウンする
1288 . #
1289 . # 【テレメトリ I/F 変換装置】
1290 . # PC をシャットダウンする
1291 . #
1292 . # 本手順書で変更したコンフィギュレーションを戻す。
1293 . #
1294 . # 【試験用接続の解除】
1295 . # PWI:MEFIST0-S1, S2 の TEST FINGER を外す。

```

文 書 番 号	シート No.	版 数
	141	1

1296 . #
1297 . # 試験コンフィギュレーション解除後、システムグラウンドの接地を 1MΩ に切替
1298 . # えること。
1299 . #
1300 . #
1301 . # ◆◇◆◆
1302 . #
1303 . # 試験終了
1304 . #
1305 . # ◆◇◆◆
1306 . #

文 書 番 号	シート No.	版 数
	142	1

付録-1

```

#!HEAD: main2-7361 2017-07-31 12:36:56 1634      45 MMO      MAIN2      //
MEFIST0 open impedance procedure
0001  #
0002  # =====
0003  . # 1. MEFIST0 open impedance: additional procedure
0004  # =====
0005  . #
0006  . # << MEFIST0 Open input test>>
0007  # Bias current DAC: from +0.45V to -0.45V
0008  # (Total run time: 180sec)
0009  # 送信前に PI 担当に確認する
0010  . # +0.450 V
0011  . #
0012  . MEFI.LOAD_BIAS1 133 194
0013  WAIT_SEC 1
0014  MEFI.LOAD_BIAS2 133 194
0015  WAIT_SEC 1
0016  MEFI.SET_DAC 0x80 0x00
0017  WAIT_SEC 1
0018  #
0019  # +0.445 V
0020  #
0021  MEFI.LOAD_BIAS1 133 178
0022  WAIT_SEC 1
0023  MEFI.LOAD_BIAS2 133 178
0024  WAIT_SEC 1
0025  MEFI.SET_DAC 0x80 0x00
0026  WAIT_SEC 1
0027  #
0028  # +0.440 V
0029  #
0030  MEFI.LOAD_BIAS1 133 161
0031  WAIT_SEC 1
0032  MEFI.LOAD_BIAS2 133 161
0033  WAIT_SEC 1
0034  MEFI.SET_DAC 0x80 0x00
0035  WAIT_SEC 1
0036  #
0037  # +0.435 V
0038  #
0039  MEFI.LOAD_BIAS1 133 145
0040  WAIT_SEC 1
0041  MEFI.LOAD_BIAS2 133 145
0042  WAIT_SEC 1
0043  MEFI.SET_DAC 0x80 0x00
0044  WAIT_SEC 1
0045  #
0046  # +0.430 V
0047  #
0048  MEFI.LOAD_BIAS1 133 129
0049  WAIT_SEC 1

```

文 書 番 号	シート No.	版 数
	143	1

0050	MEFI.LOAD_BIAS2 133 129
0051	WAIT_SEC 1
0052	MEFI.SET_DAC 0x80 0x00
0053	WAIT_SEC 1
0054	#
0055	# +0.425 V
0056	#
0057	MEFI.LOAD_BIAS1 133 112
0058	WAIT_SEC 1
0059	MEFI.LOAD_BIAS2 133 112
0060	WAIT_SEC 1
0061	MEFI.SET_DAC 0x80 0x00
0062	WAIT_SEC 1
0063	#
0064	# +0.420 V
0065	#
0066	MEFI.LOAD_BIAS1 133 96
0067	WAIT_SEC 1
0068	MEFI.LOAD_BIAS2 133 96
0069	WAIT_SEC 1
0070	MEFI.SET_DAC 0x80 0x00
0071	WAIT_SEC 1
0072	#
0073	# +0.415 V
0074	#
0075	MEFI.LOAD_BIAS1 133 79
0076	WAIT_SEC 1
0077	MEFI.LOAD_BIAS2 133 79
0078	WAIT_SEC 1
0079	MEFI.SET_DAC 0x80 0x00
0080	WAIT_SEC 1
0081	#
0082	# +0.410 V
0083	#
0084	MEFI.LOAD_BIAS1 133 63
0085	WAIT_SEC 1
0086	MEFI.LOAD_BIAS2 133 63
0087	WAIT_SEC 1
0088	MEFI.SET_DAC 0x80 0x00
0089	WAIT_SEC 1
0090	#
0091	# +0.405 V
0092	#
0093	MEFI.LOAD_BIAS1 133 47
0094	WAIT_SEC 1
0095	MEFI.LOAD_BIAS2 133 47
0096	WAIT_SEC 1
0097	MEFI.SET_DAC 0x80 0x00
0098	WAIT_SEC 1
0099	#
0100	# +0.400 V
0101	#

文 書 番 号	シ-ト No.	版 数
	144	1

0102 MEFI.LOAD_BIAS1 133 30
0103 WAIT_SEC 1
0104 MEFI.LOAD_BIAS2 133 30
0105 WAIT_SEC 1
0106 MEFI.SET_DAC 0x80 0x00
0107 WAIT_SEC 1
0108 #
0109 # +0.395 V
0110 #
0111 MEFI.LOAD_BIAS1 133 14
0112 WAIT_SEC 1
0113 MEFI.LOAD_BIAS2 133 14
0114 WAIT_SEC 1
0115 MEFI.SET_DAC 0x80 0x00
0116 WAIT_SEC 1
0117 #
0118 # +0.390 V
0119 #
0120 MEFI.LOAD_BIAS1 132 253
0121 WAIT_SEC 1
0122 MEFI.LOAD_BIAS2 132 253
0123 WAIT_SEC 1
0124 MEFI.SET_DAC 0x80 0x00
0125 WAIT_SEC 1
0126 #
0127 # +0.385 V
0128 #
0129 MEFI.LOAD_BIAS1 132 237
0130 WAIT_SEC 1
0131 MEFI.LOAD_BIAS2 132 237
0132 WAIT_SEC 1
0133 MEFI.SET_DAC 0x80 0x00
0134 WAIT_SEC 1
0135 #
0136 # +0.380 V
0137 #
0138 MEFI.LOAD_BIAS1 132 221
0139 WAIT_SEC 1
0140 MEFI.LOAD_BIAS2 132 221
0141 WAIT_SEC 1
0142 MEFI.SET_DAC 0x80 0x00
0143 WAIT_SEC 1
0144 #
0145 # +0.375 V
0146 #
0147 MEFI.LOAD_BIAS1 132 204
0148 WAIT_SEC 1
0149 MEFI.LOAD_BIAS2 132 204
0150 WAIT_SEC 1
0151 MEFI.SET_DAC 0x80 0x00
0152 WAIT_SEC 1
0153 #

文 書 番 号	シ-ト No.	版 数
	145	1

0154 # +0.370 V
0155 #
0156 MEFI.LOAD_BIAS1 132 188
0157 WAIT_SEC 1
0158 MEFI.LOAD_BIAS2 132 188
0159 WAIT_SEC 1
0160 MEFI.SET_DAC 0x80 0x00
0161 WAIT_SEC 1
0162 #
0163 # +0.365 V
0164 #
0165 MEFI.LOAD_BIAS1 132 172
0166 WAIT_SEC 1
0167 MEFI.LOAD_BIAS2 132 172
0168 WAIT_SEC 1
0169 MEFI.SET_DAC 0x80 0x00
0170 WAIT_SEC 1
0171 #
0172 # +0.360 V
0173 #
0174 MEFI.LOAD_BIAS1 132 155
0175 WAIT_SEC 1
0176 MEFI.LOAD_BIAS2 132 155
0177 WAIT_SEC 1
0178 MEFI.SET_DAC 0x80 0x00
0179 WAIT_SEC 1
0180 #
0181 # +0.355 V
0182 #
0183 MEFI.LOAD_BIAS1 132 139
0184 WAIT_SEC 1
0185 MEFI.LOAD_BIAS2 132 139
0186 WAIT_SEC 1
0187 MEFI.SET_DAC 0x80 0x00
0188 WAIT_SEC 1
0189 #
0190 # +0.350 V
0191 #
0192 MEFI.LOAD_BIAS1 132 122
0193 WAIT_SEC 1
0194 MEFI.LOAD_BIAS2 132 122
0195 WAIT_SEC 1
0196 MEFI.SET_DAC 0x80 0x00
0197 WAIT_SEC 1
0198 #
0199 # +0.345 V
0200 #
0201 MEFI.LOAD_BIAS1 132 106
0202 WAIT_SEC 1
0203 MEFI.LOAD_BIAS2 132 106
0204 WAIT_SEC 1
0205 MEFI.SET_DAC 0x80 0x00

文 書 番 号	シ-ト No.	版 数
	146	1

0206 WAIT_SEC 1
0207 #
0208 # +0.340 V
0209 #
0210 MEFI.LOAD_BIAS1 132 90
0211 WAIT_SEC 1
0212 MEFI.LOAD_BIAS2 132 90
0213 WAIT_SEC 1
0214 MEFI.SET_DAC 0x80 0x00
0215 WAIT_SEC 1
0216 #
0217 # +0.335 V
0218 #
0219 MEFI.LOAD_BIAS1 132 73
0220 WAIT_SEC 1
0221 MEFI.LOAD_BIAS2 132 73
0222 WAIT_SEC 1
0223 MEFI.SET_DAC 0x80 0x00
0224 WAIT_SEC 1
0225 #
0226 # +0.330 V
0227 #
0228 MEFI.LOAD_BIAS1 132 57
0229 WAIT_SEC 1
0230 MEFI.LOAD_BIAS2 132 57
0231 WAIT_SEC 1
0232 MEFI.SET_DAC 0x80 0x00
0233 WAIT_SEC 1
0234 #
0235 # +0.325 V
0236 #
0237 MEFI.LOAD_BIAS1 132 40
0238 WAIT_SEC 1
0239 MEFI.LOAD_BIAS2 132 40
0240 WAIT_SEC 1
0241 MEFI.SET_DAC 0x80 0x00
0242 WAIT_SEC 1
0243 #
0244 # +0.320 V
0245 #
0246 MEFI.LOAD_BIAS1 132 24
0247 WAIT_SEC 1
0248 MEFI.LOAD_BIAS2 132 24
0249 WAIT_SEC 1
0250 MEFI.SET_DAC 0x80 0x00
0251 WAIT_SEC 1
0252 #
0253 # +0.315 V
0254 #
0255 MEFI.LOAD_BIAS1 132 8
0256 WAIT_SEC 1
0257 MEFI.LOAD_BIAS2 132 8

文 書 番 号	シート No.	版 数
	147	1

0258	WAIT_SEC 1
0259	MEFI.SET_DAC 0x80 0x00
0260	WAIT_SEC 1
0261	#
0262	# +0.310 V
0263	#
0264	MEFI.LOAD_BIAS1 131 247
0265	WAIT_SEC 1
0266	MEFI.LOAD_BIAS2 131 247
0267	WAIT_SEC 1
0268	MEFI.SET_DAC 0x80 0x00
0269	WAIT_SEC 1
0270	#
0271	# +0.305 V
0272	#
0273	MEFI.LOAD_BIAS1 131 231
0274	WAIT_SEC 1
0275	MEFI.LOAD_BIAS2 131 231
0276	WAIT_SEC 1
0277	MEFI.SET_DAC 0x80 0x00
0278	WAIT_SEC 1
0279	#
0280	# +0.300 V
0281	#
0282	MEFI.LOAD_BIAS1 131 215
0283	WAIT_SEC 1
0284	MEFI.LOAD_BIAS2 131 215
0285	WAIT_SEC 1
0286	MEFI.SET_DAC 0x80 0x00
0287	WAIT_SEC 1
0288	#
0289	# +0.295 V
0290	#
0291	MEFI.LOAD_BIAS1 131 198
0292	WAIT_SEC 1
0293	MEFI.LOAD_BIAS2 131 198
0294	WAIT_SEC 1
0295	MEFI.SET_DAC 0x80 0x00
0296	WAIT_SEC 1
0297	#
0298	# +0.290 V
0299	#
0300	MEFI.LOAD_BIAS1 131 182
0301	WAIT_SEC 1
0302	MEFI.LOAD_BIAS2 131 182
0303	WAIT_SEC 1
0304	MEFI.SET_DAC 0x80 0x00
0305	WAIT_SEC 1
0306	#
0307	# +0.285 V
0308	#
0309	MEFI.LOAD_BIAS1 131 165

文 書 番 号	シート No.	版 数
	148	1

0310 WAIT_SEC 1
0311 MEFI.LOAD_BIAS2 131 165
0312 WAIT_SEC 1
0313 MEFI.SET_DAC 0x80 0x00
0314 WAIT_SEC 1
0315 #
0316 # +0.280 V
0317 #
0318 MEFI.LOAD_BIAS1 131 149
0319 WAIT_SEC 1
0320 MEFI.LOAD_BIAS2 131 149
0321 WAIT_SEC 1
0322 MEFI.SET_DAC 0x80 0x00
0323 WAIT_SEC 1
0324 #
0325 # +0.275 V
0326 #
0327 MEFI.LOAD_BIAS1 131 133
0328 WAIT_SEC 1
0329 MEFI.LOAD_BIAS2 131 133
0330 WAIT_SEC 1
0331 MEFI.SET_DAC 0x80 0x00
0332 WAIT_SEC 1
0333 #
0334 # +0.270 V
0335 #
0336 MEFI.LOAD_BIAS1 131 116
0337 WAIT_SEC 1
0338 MEFI.LOAD_BIAS2 131 116
0339 WAIT_SEC 1
0340 MEFI.SET_DAC 0x80 0x00
0341 WAIT_SEC 1
0342 #
0343 # +0.265 V
0344 #
0345 MEFI.LOAD_BIAS1 131 100
0346 WAIT_SEC 1
0347 MEFI.LOAD_BIAS2 131 100
0348 WAIT_SEC 1
0349 MEFI.SET_DAC 0x80 0x00
0350 WAIT_SEC 1
0351 #
0352 # +0.260 V
0353 #
0354 MEFI.LOAD_BIAS1 131 83
0355 WAIT_SEC 1
0356 MEFI.LOAD_BIAS2 131 83
0357 WAIT_SEC 1
0358 MEFI.SET_DAC 0x80 0x00
0359 WAIT_SEC 1
0360 #
0361 # +0.255 V

文 書 番 号	シート No.	版 数
	149	1

0362 #
0363 MEFI.LOAD_BIAS1 131 67
0364 WAIT_SEC 1
0365 MEFI.LOAD_BIAS2 131 67
0366 WAIT_SEC 1
0367 MEFI.SET_DAC 0x80 0x00
0368 WAIT_SEC 1
0369 #
0370 # +0.250 V
0371 #
0372 MEFI.LOAD_BIAS1 131 51
0373 WAIT_SEC 1
0374 MEFI.LOAD_BIAS2 131 51
0375 WAIT_SEC 1
0376 MEFI.SET_DAC 0x80 0x00
0377 WAIT_SEC 1
0378 #
0379 # +0.245 V
0380 #
0381 MEFI.LOAD_BIAS1 131 34
0382 WAIT_SEC 1
0383 MEFI.LOAD_BIAS2 131 34
0384 WAIT_SEC 1
0385 MEFI.SET_DAC 0x80 0x00
0386 WAIT_SEC 1
0387 #
0388 # +0.240 V
0389 #
0390 MEFI.LOAD_BIAS1 131 18
0391 WAIT_SEC 1
0392 MEFI.LOAD_BIAS2 131 18
0393 WAIT_SEC 1
0394 MEFI.SET_DAC 0x80 0x00
0395 WAIT_SEC 1
0396 #
0397 # +0.235 V
0398 #
0399 MEFI.LOAD_BIAS1 131 2
0400 WAIT_SEC 1
0401 MEFI.LOAD_BIAS2 131 2
0402 WAIT_SEC 1
0403 MEFI.SET_DAC 0x80 0x00
0404 WAIT_SEC 1
0405 #
0406 # +0.230 V
0407 #
0408 MEFI.LOAD_BIAS1 130 241
0409 WAIT_SEC 1
0410 MEFI.LOAD_BIAS2 130 241
0411 WAIT_SEC 1
0412 MEFI.SET_DAC 0x80 0x00
0413 WAIT_SEC 1

文 書 番 号	シート No.	版 数
	150	1

0414 #
0415 # +0.225 V
0416 #
0417 MEFI.LOAD_BIAS1 130 225
0418 WAIT_SEC 1
0419 MEFI.LOAD_BIAS2 130 225
0420 WAIT_SEC 1
0421 MEFI.SET_DAC 0x80 0x00
0422 WAIT_SEC 1
0423 #
0424 # +0.220 V
0425 #
0426 MEFI.LOAD_BIAS1 130 208
0427 WAIT_SEC 1
0428 MEFI.LOAD_BIAS2 130 208
0429 WAIT_SEC 1
0430 MEFI.SET_DAC 0x80 0x00
0431 WAIT_SEC 1
0432 #
0433 # +0.215 V
0434 #
0435 MEFI.LOAD_BIAS1 130 192
0436 WAIT_SEC 1
0437 MEFI.LOAD_BIAS2 130 192
0438 WAIT_SEC 1
0439 MEFI.SET_DAC 0x80 0x00
0440 WAIT_SEC 1
0441 #
0442 # +0.210 V
0443 #
0444 MEFI.LOAD_BIAS1 130 176
0445 WAIT_SEC 1
0446 MEFI.LOAD_BIAS2 130 176
0447 WAIT_SEC 1
0448 MEFI.SET_DAC 0x80 0x00
0449 WAIT_SEC 1
0450 #
0451 # +0.205 V
0452 #
0453 MEFI.LOAD_BIAS1 130 159
0454 WAIT_SEC 1
0455 MEFI.LOAD_BIAS2 130 159
0456 WAIT_SEC 1
0457 MEFI.SET_DAC 0x80 0x00
0458 WAIT_SEC 1
0459 #
0460 # +0.200 V
0461 #
0462 MEFI.LOAD_BIAS1 130 143
0463 WAIT_SEC 1
0464 MEFI.LOAD_BIAS2 130 143
0465 WAIT_SEC 1

文 書 番 号	シート No.	版 数
	151	1

0466 MEFI.SET_DAC 0x80 0x00
0467 WAIT_SEC 1
0468 #
0469 # +0.195 V
0470 #
0471 MEFI.LOAD_BIAS1 130 126
0472 WAIT_SEC 1
0473 MEFI.LOAD_BIAS2 130 126
0474 WAIT_SEC 1
0475 MEFI.SET_DAC 0x80 0x00
0476 WAIT_SEC 1
0477 #
0478 # +0.190 V
0479 #
0480 MEFI.LOAD_BIAS1 130 110
0481 WAIT_SEC 1
0482 MEFI.LOAD_BIAS2 130 110
0483 WAIT_SEC 1
0484 MEFI.SET_DAC 0x80 0x00
0485 WAIT_SEC 1
0486 #
0487 # +0.185 V
0488 #
0489 MEFI.LOAD_BIAS1 130 94
0490 WAIT_SEC 1
0491 MEFI.LOAD_BIAS2 130 94
0492 WAIT_SEC 1
0493 MEFI.SET_DAC 0x80 0x00
0494 WAIT_SEC 1
0495 #
0496 # +0.180 V
0497 #
0498 MEFI.LOAD_BIAS1 130 77
0499 WAIT_SEC 1
0500 MEFI.LOAD_BIAS2 130 77
0501 WAIT_SEC 1
0502 MEFI.SET_DAC 0x80 0x00
0503 WAIT_SEC 1
0504 #
0505 # +0.175 V
0506 #
0507 MEFI.LOAD_BIAS1 130 61
0508 WAIT_SEC 1
0509 MEFI.LOAD_BIAS2 130 61
0510 WAIT_SEC 1
0511 MEFI.SET_DAC 0x80 0x00
0512 WAIT_SEC 1
0513 #
0514 # +0.170 V
0515 #
0516 MEFI.LOAD_BIAS1 130 45
0517 WAIT_SEC 1

文 書 番 号	シート No.	版 数
	152	1

0518 MEFI.LOAD_BIAS2 130 45
0519 WAIT_SEC 1
0520 MEFI.SET_DAC 0x80 0x00
0521 WAIT_SEC 1
0522 #
0523 # +0.165 V
0524 #
0525 MEFI.LOAD_BIAS1 130 28
0526 WAIT_SEC 1
0527 MEFI.LOAD_BIAS2 130 28
0528 WAIT_SEC 1
0529 MEFI.SET_DAC 0x80 0x00
0530 WAIT_SEC 1
0531 #
0532 # +0.160 V
0533 #
0534 MEFI.LOAD_BIAS1 130 12
0535 WAIT_SEC 1
0536 MEFI.LOAD_BIAS2 130 12
0537 WAIT_SEC 1
0538 MEFI.SET_DAC 0x80 0x00
0539 WAIT_SEC 1
0540 #
0541 # +0.155 V
0542 #
0543 MEFI.LOAD_BIAS1 129 251
0544 WAIT_SEC 1
0545 MEFI.LOAD_BIAS2 129 251
0546 WAIT_SEC 1
0547 MEFI.SET_DAC 0x80 0x00
0548 WAIT_SEC 1
0549 #
0550 # +0.150 V
0551 #
0552 MEFI.LOAD_BIAS1 129 235
0553 WAIT_SEC 1
0554 MEFI.LOAD_BIAS2 129 235
0555 WAIT_SEC 1
0556 MEFI.SET_DAC 0x80 0x00
0557 WAIT_SEC 1
0558 #
0559 # +0.145 V
0560 #
0561 MEFI.LOAD_BIAS1 129 219
0562 WAIT_SEC 1
0563 MEFI.LOAD_BIAS2 129 219
0564 WAIT_SEC 1
0565 MEFI.SET_DAC 0x80 0x00
0566 WAIT_SEC 1
0567 #
0568 # +0.140 V
0569 #

文 書 番 号	シ-ト No.	版 数
	153	1

0570 MEFI.LOAD_BIAS1 129 202
0571 WAIT_SEC 1
0572 MEFI.LOAD_BIAS2 129 202
0573 WAIT_SEC 1
0574 MEFI.SET_DAC 0x80 0x00
0575 WAIT_SEC 1
0576 #
0577 # +0.135 V
0578 #
0579 MEFI.LOAD_BIAS1 129 186
0580 WAIT_SEC 1
0581 MEFI.LOAD_BIAS2 129 186
0582 WAIT_SEC 1
0583 MEFI.SET_DAC 0x80 0x00
0584 WAIT_SEC 1
0585 #
0586 # +0.130 V
0587 #
0588 MEFI.LOAD_BIAS1 129 169
0589 WAIT_SEC 1
0590 MEFI.LOAD_BIAS2 129 169
0591 WAIT_SEC 1
0592 MEFI.SET_DAC 0x80 0x00
0593 WAIT_SEC 1
0594 #
0595 # +0.125 V
0596 #
0597 MEFI.LOAD_BIAS1 129 153
0598 WAIT_SEC 1
0599 MEFI.LOAD_BIAS2 129 153
0600 WAIT_SEC 1
0601 MEFI.SET_DAC 0x80 0x00
0602 WAIT_SEC 1
0603 #
0604 # +0.120 V
0605 #
0606 MEFI.LOAD_BIAS1 129 137
0607 WAIT_SEC 1
0608 MEFI.LOAD_BIAS2 129 137
0609 WAIT_SEC 1
0610 MEFI.SET_DAC 0x80 0x00
0611 WAIT_SEC 1
0612 #
0613 # +0.115 V
0614 #
0615 MEFI.LOAD_BIAS1 129 120
0616 WAIT_SEC 1
0617 MEFI.LOAD_BIAS2 129 120
0618 WAIT_SEC 1
0619 MEFI.SET_DAC 0x80 0x00
0620 WAIT_SEC 1
0621 #

文 書 番 号	シート No.	版 数
	154	1

0622 # +0.110 V
0623 #
0624 MEFI.LOAD_BIAS1 129 104
0625 WAIT_SEC 1
0626 MEFI.LOAD_BIAS2 129 104
0627 WAIT_SEC 1
0628 MEFI.SET_DAC 0x80 0x00
0629 WAIT_SEC 1
0630 #
0631 # +0.105 V
0632 #
0633 MEFI.LOAD_BIAS1 129 88
0634 WAIT_SEC 1
0635 MEFI.LOAD_BIAS2 129 88
0636 WAIT_SEC 1
0637 MEFI.SET_DAC 0x80 0x00
0638 WAIT_SEC 1
0639 #
0640 # +0.100 V
0641 #
0642 MEFI.LOAD_BIAS1 129 71
0643 WAIT_SEC 1
0644 MEFI.LOAD_BIAS2 129 71
0645 WAIT_SEC 1
0646 MEFI.SET_DAC 0x80 0x00
0647 WAIT_SEC 1
0648 #
0649 # +0.095 V
0650 #
0651 MEFI.LOAD_BIAS1 129 55
0652 WAIT_SEC 1
0653 MEFI.LOAD_BIAS2 129 55
0654 WAIT_SEC 1
0655 MEFI.SET_DAC 0x80 0x00
0656 WAIT_SEC 1
0657 #
0658 # +0.090 V
0659 #
0660 MEFI.LOAD_BIAS1 129 38
0661 WAIT_SEC 1
0662 MEFI.LOAD_BIAS2 129 38
0663 WAIT_SEC 1
0664 MEFI.SET_DAC 0x80 0x00
0665 WAIT_SEC 1
0666 #
0667 # +0.085 V
0668 #
0669 MEFI.LOAD_BIAS1 129 22
0670 WAIT_SEC 1
0671 MEFI.LOAD_BIAS2 129 22
0672 WAIT_SEC 1
0673 MEFI.SET_DAC 0x80 0x00

文 書 番 号	シート No.	版 数
	155	1

0674 WAIT_SEC 1
0675 #
0676 # +0.080 V
0677 #
0678 MEFI.LOAD_BIAS1 129 6
0679 WAIT_SEC 1
0680 MEFI.LOAD_BIAS2 129 6
0681 WAIT_SEC 1
0682 MEFI.SET_DAC 0x80 0x00
0683 WAIT_SEC 1
0684 #
0685 # +0.075 V
0686 #
0687 MEFI.LOAD_BIAS1 128 245
0688 WAIT_SEC 1
0689 MEFI.LOAD_BIAS2 128 245
0690 WAIT_SEC 1
0691 MEFI.SET_DAC 0x80 0x00
0692 WAIT_SEC 1
0693 #
0694 # +0.070 V
0695 #
0696 MEFI.LOAD_BIAS1 128 229
0697 WAIT_SEC 1
0698 MEFI.LOAD_BIAS2 128 229
0699 WAIT_SEC 1
0700 MEFI.SET_DAC 0x80 0x00
0701 WAIT_SEC 1
0702 #
0703 # +0.065 V
0704 #
0705 MEFI.LOAD_BIAS1 128 212
0706 WAIT_SEC 1
0707 MEFI.LOAD_BIAS2 128 212
0708 WAIT_SEC 1
0709 MEFI.SET_DAC 0x80 0x00
0710 WAIT_SEC 1
0711 #
0712 # +0.060 V
0713 #
0714 MEFI.LOAD_BIAS1 128 196
0715 WAIT_SEC 1
0716 MEFI.LOAD_BIAS2 128 196
0717 WAIT_SEC 1
0718 MEFI.SET_DAC 0x80 0x00
0719 WAIT_SEC 1
0720 #
0721 # +0.055 V
0722 #
0723 MEFI.LOAD_BIAS1 128 180
0724 WAIT_SEC 1
0725 MEFI.LOAD_BIAS2 128 180

文 書 番 号	シート No.	版 数
	156	1

0726 WAIT_SEC 1
0727 MEFI.SET_DAC 0x80 0x00
0728 WAIT_SEC 1
0729 #
0730 # +0.050 V
0731 #
0732 MEFI.LOAD_BIAS1 128 163
0733 WAIT_SEC 1
0734 MEFI.LOAD_BIAS2 128 163
0735 WAIT_SEC 1
0736 MEFI.SET_DAC 0x80 0x00
0737 WAIT_SEC 1
0738 #
0739 # +0.045 V
0740 #
0741 MEFI.LOAD_BIAS1 128 147
0742 WAIT_SEC 1
0743 MEFI.LOAD_BIAS2 128 147
0744 WAIT_SEC 1
0745 MEFI.SET_DAC 0x80 0x00
0746 WAIT_SEC 1
0747 #
0748 # +0.040 V
0749 #
0750 MEFI.LOAD_BIAS1 128 131
0751 WAIT_SEC 1
0752 MEFI.LOAD_BIAS2 128 131
0753 WAIT_SEC 1
0754 MEFI.SET_DAC 0x80 0x00
0755 WAIT_SEC 1
0756 #
0757 # +0.035 V
0758 #
0759 MEFI.LOAD_BIAS1 128 114
0760 WAIT_SEC 1
0761 MEFI.LOAD_BIAS2 128 114
0762 WAIT_SEC 1
0763 MEFI.SET_DAC 0x80 0x00
0764 WAIT_SEC 1
0765 #
0766 # +0.030 V
0767 #
0768 MEFI.LOAD_BIAS1 128 98
0769 WAIT_SEC 1
0770 MEFI.LOAD_BIAS2 128 98
0771 WAIT_SEC 1
0772 MEFI.SET_DAC 0x80 0x00
0773 WAIT_SEC 1
0774 #
0775 # +0.025 V
0776 #
0777 MEFI.LOAD_BIAS1 128 81

文 書 番 号	シート No.	版 数
	157	1

0778 WAIT_SEC 1
0779 MEFI.LOAD_BIAS2 128 81
0780 WAIT_SEC 1
0781 MEFI.SET_DAC 0x80 0x00
0782 WAIT_SEC 1
0783 #
0784 # +0.020 V
0785 #
0786 MEFI.LOAD_BIAS1 128 65
0787 WAIT_SEC 1
0788 MEFI.LOAD_BIAS2 128 65
0789 WAIT_SEC 1
0790 MEFI.SET_DAC 0x80 0x00
0791 WAIT_SEC 1
0792 #
0793 # +0.015 V
0794 #
0795 MEFI.LOAD_BIAS1 128 49
0796 WAIT_SEC 1
0797 MEFI.LOAD_BIAS2 128 49
0798 WAIT_SEC 1
0799 MEFI.SET_DAC 0x80 0x00
0800 WAIT_SEC 1
0801 #
0802 # +0.010 V
0803 #
0804 MEFI.LOAD_BIAS1 128 32
0805 WAIT_SEC 1
0806 MEFI.LOAD_BIAS2 128 32
0807 WAIT_SEC 1
0808 MEFI.SET_DAC 0x80 0x00
0809 WAIT_SEC 1
0810 #
0811 # +0.005 V
0812 #
0813 MEFI.LOAD_BIAS1 128 16
0814 WAIT_SEC 1
0815 MEFI.LOAD_BIAS2 128 16
0816 WAIT_SEC 1
0817 MEFI.SET_DAC 0x80 0x00
0818 WAIT_SEC 1
0819 #
0820 # 0.000 V
0821 #
0822 MEFI.LOAD_BIAS1 128 0
0823 WAIT_SEC 1
0824 MEFI.LOAD_BIAS2 128 0
0825 WAIT_SEC 1
0826 MEFI.SET_DAC 0x80 0x00
0827 WAIT_SEC 1
0828 #
0829 # -0.005 V

文 書 番 号	シート No.	版 数
	158	1

0830 #
0831 MEFI.LOAD_BIAS1 127 240
0832 WAIT_SEC 1
0833 MEFI.LOAD_BIAS2 127 240
0834 WAIT_SEC 1
0835 MEFI.SET_DAC 0x80 0x00
0836 WAIT_SEC 1
0837 #
0838 # -0.010 V
0839 #
0840 MEFI.LOAD_BIAS1 127 224
0841 WAIT_SEC 1
0842 MEFI.LOAD_BIAS2 127 224
0843 WAIT_SEC 1
0844 MEFI.SET_DAC 0x80 0x00
0845 WAIT_SEC 1
0846 #
0847 # -0.015 V
0848 #
0849 MEFI.LOAD_BIAS1 127 207
0850 WAIT_SEC 1
0851 MEFI.LOAD_BIAS2 127 207
0852 WAIT_SEC 1
0853 MEFI.SET_DAC 0x80 0x00
0854 WAIT_SEC 1
0855 #
0856 # -0.020 V
0857 #
0858 MEFI.LOAD_BIAS1 127 191
0859 WAIT_SEC 1
0860 MEFI.LOAD_BIAS2 127 191
0861 WAIT_SEC 1
0862 MEFI.SET_DAC 0x80 0x00
0863 WAIT_SEC 1
0864 #
0865 # -0.025 V
0866 #
0867 MEFI.LOAD_BIAS1 127 175
0868 WAIT_SEC 1
0869 MEFI.LOAD_BIAS2 127 175
0870 WAIT_SEC 1
0871 MEFI.SET_DAC 0x80 0x00
0872 WAIT_SEC 1
0873 #
0874 # -0.030 V
0875 #
0876 MEFI.LOAD_BIAS1 127 158
0877 WAIT_SEC 1
0878 MEFI.LOAD_BIAS2 127 158
0879 WAIT_SEC 1
0880 MEFI.SET_DAC 0x80 0x00
0881 WAIT_SEC 1

文 書 番 号	シート No.	版 数
	159	1

0882 #
0883 # -0.035 V
0884 #
0885 MEFI.LOAD_BIAS1 127 142
0886 WAIT_SEC 1
0887 MEFI.LOAD_BIAS2 127 142
0888 WAIT_SEC 1
0889 MEFI.SET_DAC 0x80 0x00
0890 WAIT_SEC 1
0891 #
0892 # -0.040 V
0893 #
0894 MEFI.LOAD_BIAS1 127 125
0895 WAIT_SEC 1
0896 MEFI.LOAD_BIAS2 127 125
0897 WAIT_SEC 1
0898 MEFI.SET_DAC 0x80 0x00
0899 WAIT_SEC 1
0900 #
0901 # -0.045 V
0902 #
0903 MEFI.LOAD_BIAS1 127 109
0904 WAIT_SEC 1
0905 MEFI.LOAD_BIAS2 127 109
0906 WAIT_SEC 1
0907 MEFI.SET_DAC 0x80 0x00
0908 WAIT_SEC 1
0909 #
0910 # -0.050 V
0911 #
0912 MEFI.LOAD_BIAS1 127 93
0913 WAIT_SEC 1
0914 MEFI.LOAD_BIAS2 127 93
0915 WAIT_SEC 1
0916 MEFI.SET_DAC 0x80 0x00
0917 WAIT_SEC 1
0918 #
0919 # -0.055 V
0920 #
0921 MEFI.LOAD_BIAS1 127 76
0922 WAIT_SEC 1
0923 MEFI.LOAD_BIAS2 127 76
0924 WAIT_SEC 1
0925 MEFI.SET_DAC 0x80 0x00
0926 WAIT_SEC 1
0927 #
0928 # -0.060 V
0929 #
0930 MEFI.LOAD_BIAS1 127 60
0931 WAIT_SEC 1
0932 MEFI.LOAD_BIAS2 127 60
0933 WAIT_SEC 1

文 書 番 号	シート No.	版 数
	160	1

0934 MEFI.SET_DAC 0x80 0x00
0935 WAIT_SEC 1
0936 #
0937 # -0.065 V
0938 #
0939 MEFI.LOAD_BIAS1 127 44
0940 WAIT_SEC 1
0941 MEFI.LOAD_BIAS2 127 44
0942 WAIT_SEC 1
0943 MEFI.SET_DAC 0x80 0x00
0944 WAIT_SEC 1
0945 #
0946 # -0.070 V
0947 #
0948 MEFI.LOAD_BIAS1 127 27
0949 WAIT_SEC 1
0950 MEFI.LOAD_BIAS2 127 27
0951 WAIT_SEC 1
0952 MEFI.SET_DAC 0x80 0x00
0953 WAIT_SEC 1
0954 #
0955 # -0.075 V
0956 #
0957 MEFI.LOAD_BIAS1 127 11
0958 WAIT_SEC 1
0959 MEFI.LOAD_BIAS2 127 11
0960 WAIT_SEC 1
0961 MEFI.SET_DAC 0x80 0x00
0962 WAIT_SEC 1
0963 #
0964 # -0.080 V
0965 #
0966 MEFI.LOAD_BIAS1 126 250
0967 WAIT_SEC 1
0968 MEFI.LOAD_BIAS2 126 250
0969 WAIT_SEC 1
0970 MEFI.SET_DAC 0x80 0x00
0971 WAIT_SEC 1
0972 #
0973 # -0.085 V
0974 #
0975 MEFI.LOAD_BIAS1 126 234
0976 WAIT_SEC 1
0977 MEFI.LOAD_BIAS2 126 234
0978 WAIT_SEC 1
0979 MEFI.SET_DAC 0x80 0x00
0980 WAIT_SEC 1
0981 #
0982 # -0.090 V
0983 #
0984 MEFI.LOAD_BIAS1 126 218
0985 WAIT_SEC 1

文 書 番 号	シ-ト No.	版 数
	161	1

0986 MEFI.LOAD_BIAS2 126 218
0987 WAIT_SEC 1
0988 MEFI.SET_DAC 0x80 0x00
0989 WAIT_SEC 1
0990 #
0991 # -0.095 V
0992 #
0993 MEFI.LOAD_BIAS1 126 201
0994 WAIT_SEC 1
0995 MEFI.LOAD_BIAS2 126 201
0996 WAIT_SEC 1
0997 MEFI.SET_DAC 0x80 0x00
0998 WAIT_SEC 1
0999 #
1000 # -0.100 V
1001 #
1002 MEFI.LOAD_BIAS1 126 185
1003 WAIT_SEC 1
1004 MEFI.LOAD_BIAS2 126 185
1005 WAIT_SEC 1
1006 MEFI.SET_DAC 0x80 0x00
1007 WAIT_SEC 1
1008 #
1009 # -0.105 V
1010 #
1011 MEFI.LOAD_BIAS1 126 168
1012 WAIT_SEC 1
1013 MEFI.LOAD_BIAS2 126 168
1014 WAIT_SEC 1
1015 MEFI.SET_DAC 0x80 0x00
1016 WAIT_SEC 1
1017 #
1018 # -0.110 V
1019 #
1020 MEFI.LOAD_BIAS1 126 152
1021 WAIT_SEC 1
1022 MEFI.LOAD_BIAS2 126 152
1023 WAIT_SEC 1
1024 MEFI.SET_DAC 0x80 0x00
1025 WAIT_SEC 1
1026 #
1027 # -0.115 V
1028 #
1029 MEFI.LOAD_BIAS1 126 136
1030 WAIT_SEC 1
1031 MEFI.LOAD_BIAS2 126 136
1032 WAIT_SEC 1
1033 MEFI.SET_DAC 0x80 0x00
1034 WAIT_SEC 1
1035 #
1036 # -0.120 V
1037 #

文 書 番 号	シ-ト No.	版 数
	162	1

1038 MEFI.LOAD_BIAS1 126 119
1039 WAIT_SEC 1
1040 MEFI.LOAD_BIAS2 126 119
1041 WAIT_SEC 1
1042 MEFI.SET_DAC 0x80 0x00
1043 WAIT_SEC 1
1044 #
1045 # -0.125 V
1046 #
1047 MEFI.LOAD_BIAS1 126 103
1048 WAIT_SEC 1
1049 MEFI.LOAD_BIAS2 126 103
1050 WAIT_SEC 1
1051 MEFI.SET_DAC 0x80 0x00
1052 WAIT_SEC 1
1053 #
1054 # -0.130 V
1055 #
1056 MEFI.LOAD_BIAS1 126 87
1057 WAIT_SEC 1
1058 MEFI.LOAD_BIAS2 126 87
1059 WAIT_SEC 1
1060 MEFI.SET_DAC 0x80 0x00
1061 WAIT_SEC 1
1062 #
1063 # -0.135 V
1064 #
1065 MEFI.LOAD_BIAS1 126 70
1066 WAIT_SEC 1
1067 MEFI.LOAD_BIAS2 126 70
1068 WAIT_SEC 1
1069 MEFI.SET_DAC 0x80 0x00
1070 WAIT_SEC 1
1071 #
1072 # -0.140 V
1073 #
1074 MEFI.LOAD_BIAS1 126 54
1075 WAIT_SEC 1
1076 MEFI.LOAD_BIAS2 126 54
1077 WAIT_SEC 1
1078 MEFI.SET_DAC 0x80 0x00
1079 WAIT_SEC 1
1080 #
1081 # -0.145 V
1082 #
1083 MEFI.LOAD_BIAS1 126 37
1084 WAIT_SEC 1
1085 MEFI.LOAD_BIAS2 126 37
1086 WAIT_SEC 1
1087 MEFI.SET_DAC 0x80 0x00
1088 WAIT_SEC 1
1089 #

文 書 番 号	シ-ト No.	版 数
	163	1

1090 # -0.150 V
1091 #
1092 MEFI.LOAD_BIAS1 126 21
1093 WAIT_SEC 1
1094 MEFI.LOAD_BIAS2 126 21
1095 WAIT_SEC 1
1096 MEFI.SET_DAC 0x80 0x00
1097 WAIT_SEC 1
1098 #
1099 # -0.155 V
1100 #
1101 MEFI.LOAD_BIAS1 126 5
1102 WAIT_SEC 1
1103 MEFI.LOAD_BIAS2 126 5
1104 WAIT_SEC 1
1105 MEFI.SET_DAC 0x80 0x00
1106 WAIT_SEC 1
1107 #
1108 # -0.160 V
1109 #
1110 MEFI.LOAD_BIAS1 125 244
1111 WAIT_SEC 1
1112 MEFI.LOAD_BIAS2 125 244
1113 WAIT_SEC 1
1114 MEFI.SET_DAC 0x80 0x00
1115 WAIT_SEC 1
1116 #
1117 # -0.165 V
1118 #
1119 MEFI.LOAD_BIAS1 125 228
1120 WAIT_SEC 1
1121 MEFI.LOAD_BIAS2 125 228
1122 WAIT_SEC 1
1123 MEFI.SET_DAC 0x80 0x00
1124 WAIT_SEC 1
1125 #
1126 # -0.170 V
1127 #
1128 MEFI.LOAD_BIAS1 125 211
1129 WAIT_SEC 1
1130 MEFI.LOAD_BIAS2 125 211
1131 WAIT_SEC 1
1132 MEFI.SET_DAC 0x80 0x00
1133 WAIT_SEC 1
1134 #
1135 # -0.175 V
1136 #
1137 MEFI.LOAD_BIAS1 125 195
1138 WAIT_SEC 1
1139 MEFI.LOAD_BIAS2 125 195
1140 WAIT_SEC 1
1141 MEFI.SET_DAC 0x80 0x00

文 書 番 号	シート No.	版 数
	164	1

1142 WAIT_SEC 1
1143 #
1144 # -0.180 V
1145 #
1146 MEFI.LOAD_BIAS1 125 179
1147 WAIT_SEC 1
1148 MEFI.LOAD_BIAS2 125 179
1149 WAIT_SEC 1
1150 MEFI.SET_DAC 0x80 0x00
1151 WAIT_SEC 1
1152 #
1153 # -0.185 V
1154 #
1155 MEFI.LOAD_BIAS1 125 162
1156 WAIT_SEC 1
1157 MEFI.LOAD_BIAS2 125 162
1158 WAIT_SEC 1
1159 MEFI.SET_DAC 0x80 0x00
1160 WAIT_SEC 1
1161 #
1162 # -0.190 V
1163 #
1164 MEFI.LOAD_BIAS1 125 146
1165 WAIT_SEC 1
1166 MEFI.LOAD_BIAS2 125 146
1167 WAIT_SEC 1
1168 MEFI.SET_DAC 0x80 0x00
1169 WAIT_SEC 1
1170 #
1171 # -0.195 V
1172 #
1173 MEFI.LOAD_BIAS1 125 130
1174 WAIT_SEC 1
1175 MEFI.LOAD_BIAS2 125 130
1176 WAIT_SEC 1
1177 MEFI.SET_DAC 0x80 0x00
1178 WAIT_SEC 1
1179 #
1180 # -0.200 V
1181 #
1182 MEFI.LOAD_BIAS1 125 113
1183 WAIT_SEC 1
1184 MEFI.LOAD_BIAS2 125 113
1185 WAIT_SEC 1
1186 MEFI.SET_DAC 0x80 0x00
1187 WAIT_SEC 1
1188 #
1189 # -0.205 V
1190 #
1191 MEFI.LOAD_BIAS1 125 97
1192 WAIT_SEC 1
1193 MEFI.LOAD_BIAS2 125 97

文 書 番 号	シ-ト No.	版 数
	165	1

1194 WAIT_SEC 1
1195 MEFI.SET_DAC 0x80 0x00
1196 WAIT_SEC 1
1197 #
1198 # -0.210 V
1199 #
1200 MEFI.LOAD_BIAS1 125 80
1201 WAIT_SEC 1
1202 MEFI.LOAD_BIAS2 125 80
1203 WAIT_SEC 1
1204 MEFI.SET_DAC 0x80 0x00
1205 WAIT_SEC 1
1206 #
1207 # -0.215 V
1208 #
1209 MEFI.LOAD_BIAS1 125 64
1210 WAIT_SEC 1
1211 MEFI.LOAD_BIAS2 125 64
1212 WAIT_SEC 1
1213 MEFI.SET_DAC 0x80 0x00
1214 WAIT_SEC 1
1215 #
1216 # -0.220 V
1217 #
1218 MEFI.LOAD_BIAS1 125 48
1219 WAIT_SEC 1
1220 MEFI.LOAD_BIAS2 125 48
1221 WAIT_SEC 1
1222 MEFI.SET_DAC 0x80 0x00
1223 WAIT_SEC 1
1224 #
1225 # -0.225 V
1226 #
1227 MEFI.LOAD_BIAS1 125 31
1228 WAIT_SEC 1
1229 MEFI.LOAD_BIAS2 125 31
1230 WAIT_SEC 1
1231 MEFI.SET_DAC 0x80 0x00
1232 WAIT_SEC 1
1233 #
1234 # -0.230 V
1235 #
1236 MEFI.LOAD_BIAS1 125 15
1237 WAIT_SEC 1
1238 MEFI.LOAD_BIAS2 125 15
1239 WAIT_SEC 1
1240 MEFI.SET_DAC 0x80 0x00
1241 WAIT_SEC 1
1242 #
1243 # -0.235 V
1244 #
1245 MEFI.LOAD_BIAS1 124 254

文 書 番 号	シート No.	版 数
	166	1

1246	WAIT_SEC 1
1247	MEFI.LOAD_BIAS2 124 254
1248	WAIT_SEC 1
1249	MEFI.SET_DAC 0x80 0x00
1250	WAIT_SEC 1
1251	#
1252	# -0.240 V
1253	#
1254	MEFI.LOAD_BIAS1 124 238
1255	WAIT_SEC 1
1256	MEFI.LOAD_BIAS2 124 238
1257	WAIT_SEC 1
1258	MEFI.SET_DAC 0x80 0x00
1259	WAIT_SEC 1
1260	#
1261	# -0.245 V
1262	#
1263	MEFI.LOAD_BIAS1 124 222
1264	WAIT_SEC 1
1265	MEFI.LOAD_BIAS2 124 222
1266	WAIT_SEC 1
1267	MEFI.SET_DAC 0x80 0x00
1268	WAIT_SEC 1
1269	#
1270	# -0.250 V
1271	#
1272	MEFI.LOAD_BIAS1 124 205
1273	WAIT_SEC 1
1274	MEFI.LOAD_BIAS2 124 205
1275	WAIT_SEC 1
1276	MEFI.SET_DAC 0x80 0x00
1277	WAIT_SEC 1
1278	#
1279	# -0.255 V
1280	#
1281	MEFI.LOAD_BIAS1 124 189
1282	WAIT_SEC 1
1283	MEFI.LOAD_BIAS2 124 189
1284	WAIT_SEC 1
1285	MEFI.SET_DAC 0x80 0x00
1286	WAIT_SEC 1
1287	#
1288	# -0.260 V
1289	#
1290	MEFI.LOAD_BIAS1 124 173
1291	WAIT_SEC 1
1292	MEFI.LOAD_BIAS2 124 173
1293	WAIT_SEC 1
1294	MEFI.SET_DAC 0x80 0x00
1295	WAIT_SEC 1
1296	#
1297	# -0.265 V

文 書 番 号	シ ー ト No.	版 数
	167	1

1298 #
1299 MEFI.LOAD_BIAS1 124 156
1300 WAIT_SEC 1
1301 MEFI.LOAD_BIAS2 124 156
1302 WAIT_SEC 1
1303 MEFI.SET_DAC 0x80 0x00
1304 WAIT_SEC 1
1305 #
1306 # -0.270 V
1307 #
1308 MEFI.LOAD_BIAS1 124 140
1309 WAIT_SEC 1
1310 MEFI.LOAD_BIAS2 124 140
1311 WAIT_SEC 1
1312 MEFI.SET_DAC 0x80 0x00
1313 WAIT_SEC 1
1314 #
1315 # -0.275 V
1316 #
1317 MEFI.LOAD_BIAS1 124 123
1318 WAIT_SEC 1
1319 MEFI.LOAD_BIAS2 124 123
1320 WAIT_SEC 1
1321 MEFI.SET_DAC 0x80 0x00
1322 WAIT_SEC 1
1323 #
1324 # -0.280 V
1325 #
1326 MEFI.LOAD_BIAS1 124 107
1327 WAIT_SEC 1
1328 MEFI.LOAD_BIAS2 124 107
1329 WAIT_SEC 1
1330 MEFI.SET_DAC 0x80 0x00
1331 WAIT_SEC 1
1332 #
1333 # -0.285 V
1334 #
1335 MEFI.LOAD_BIAS1 124 91
1336 WAIT_SEC 1
1337 MEFI.LOAD_BIAS2 124 91
1338 WAIT_SEC 1
1339 MEFI.SET_DAC 0x80 0x00
1340 WAIT_SEC 1
1341 #
1342 # -0.290 V
1343 #
1344 MEFI.LOAD_BIAS1 124 74
1345 WAIT_SEC 1
1346 MEFI.LOAD_BIAS2 124 74
1347 WAIT_SEC 1
1348 MEFI.SET_DAC 0x80 0x00
1349 WAIT_SEC 1

文 書 番 号	シ-ト No.	版 数
	168	1

1350 #
1351 # -0.295 V
1352 #
1353 MEFI.LOAD_BIAS1 124 58
1354 WAIT_SEC 1
1355 MEFI.LOAD_BIAS2 124 58
1356 WAIT_SEC 1
1357 MEFI.SET_DAC 0x80 0x00
1358 WAIT_SEC 1
1359 #
1360 # -0.300 V
1361 #
1362 MEFI.LOAD_BIAS1 124 41
1363 WAIT_SEC 1
1364 MEFI.LOAD_BIAS2 124 41
1365 WAIT_SEC 1
1366 MEFI.SET_DAC 0x80 0x00
1367 WAIT_SEC 1
1368 #
1369 # -0.305 V
1370 #
1371 MEFI.LOAD_BIAS1 124 25
1372 WAIT_SEC 1
1373 MEFI.LOAD_BIAS2 124 25
1374 WAIT_SEC 1
1375 MEFI.SET_DAC 0x80 0x00
1376 WAIT_SEC 1
1377 #
1378 # -0.310 V
1379 #
1380 MEFI.LOAD_BIAS1 124 9
1381 WAIT_SEC 1
1382 MEFI.LOAD_BIAS2 124 9
1383 WAIT_SEC 1
1384 MEFI.SET_DAC 0x80 0x00
1385 WAIT_SEC 1
1386 #
1387 # -0.315 V
1388 #
1389 MEFI.LOAD_BIAS1 123 248
1390 WAIT_SEC 1
1391 MEFI.LOAD_BIAS2 123 248
1392 WAIT_SEC 1
1393 MEFI.SET_DAC 0x80 0x00
1394 WAIT_SEC 1
1395 #
1396 # -0.320 V
1397 #
1398 MEFI.LOAD_BIAS1 123 232
1399 WAIT_SEC 1
1400 MEFI.LOAD_BIAS2 123 232
1401 WAIT_SEC 1

文 書 番 号	シート No.	版 数
	169	1

1402 MEFI.SET_DAC 0x80 0x00
1403 WAIT_SEC 1
1404 #
1405 # -0.325 V
1406 #
1407 MEFI.LOAD_BIAS1 123 216
1408 WAIT_SEC 1
1409 MEFI.LOAD_BIAS2 123 216
1410 WAIT_SEC 1
1411 MEFI.SET_DAC 0x80 0x00
1412 WAIT_SEC 1
1413 #
1414 # -0.330 V
1415 #
1416 MEFI.LOAD_BIAS1 123 199
1417 WAIT_SEC 1
1418 MEFI.LOAD_BIAS2 123 199
1419 WAIT_SEC 1
1420 MEFI.SET_DAC 0x80 0x00
1421 WAIT_SEC 1
1422 #
1423 # -0.335 V
1424 #
1425 MEFI.LOAD_BIAS1 123 183
1426 WAIT_SEC 1
1427 MEFI.LOAD_BIAS2 123 183
1428 WAIT_SEC 1
1429 MEFI.SET_DAC 0x80 0x00
1430 WAIT_SEC 1
1431 #
1432 # -0.340 V
1433 #
1434 MEFI.LOAD_BIAS1 123 166
1435 WAIT_SEC 1
1436 MEFI.LOAD_BIAS2 123 166
1437 WAIT_SEC 1
1438 MEFI.SET_DAC 0x80 0x00
1439 WAIT_SEC 1
1440 #
1441 # -0.345 V
1442 #
1443 MEFI.LOAD_BIAS1 123 150
1444 WAIT_SEC 1
1445 MEFI.LOAD_BIAS2 123 150
1446 WAIT_SEC 1
1447 MEFI.SET_DAC 0x80 0x00
1448 WAIT_SEC 1
1449 #
1450 # -0.350 V
1451 #
1452 MEFI.LOAD_BIAS1 123 134
1453 WAIT_SEC 1

文 書 番 号	シート No.	版 数
	170	1

1454 MEFI.LOAD_BIAS2 123 134
1455 WAIT_SEC 1
1456 MEFI.SET_DAC 0x80 0x00
1457 WAIT_SEC 1
1458 #
1459 # -0.355 V
1460 #
1461 MEFI.LOAD_BIAS1 123 117
1462 WAIT_SEC 1
1463 MEFI.LOAD_BIAS2 123 117
1464 WAIT_SEC 1
1465 MEFI.SET_DAC 0x80 0x00
1466 WAIT_SEC 1
1467 #
1468 # -0.360 V
1469 #
1470 MEFI.LOAD_BIAS1 123 101
1471 WAIT_SEC 1
1472 MEFI.LOAD_BIAS2 123 101
1473 WAIT_SEC 1
1474 MEFI.SET_DAC 0x80 0x00
1475 WAIT_SEC 1
1476 #
1477 # -0.365 V
1478 #
1479 MEFI.LOAD_BIAS1 123 84
1480 WAIT_SEC 1
1481 MEFI.LOAD_BIAS2 123 84
1482 WAIT_SEC 1
1483 MEFI.SET_DAC 0x80 0x00
1484 WAIT_SEC 1
1485 #
1486 # -0.370 V
1487 #
1488 MEFI.LOAD_BIAS1 123 69
1489 WAIT_SEC 1
1490 MEFI.LOAD_BIAS2 123 69
1491 WAIT_SEC 1
1492 MEFI.SET_DAC 0x80 0x00
1493 WAIT_SEC 1
1494 #
1495 # -0.375 V
1496 #
1497 MEFI.LOAD_BIAS1 123 52
1498 WAIT_SEC 1
1499 MEFI.LOAD_BIAS2 123 52
1500 WAIT_SEC 1
1501 MEFI.SET_DAC 0x80 0x00
1502 WAIT_SEC 1
1503 #
1504 # -0.380 V
1505 #

文 書 番 号	シ-ト No.	版 数
	171	1

1506 MEFI.LOAD_BIAS1 123 35
1507 WAIT_SEC 1
1508 MEFI.LOAD_BIAS2 123 35
1509 WAIT_SEC 1
1510 MEFI.SET_DAC 0x80 0x00
1511 WAIT_SEC 1
1512 #
1513 # -0.385 V
1514 #
1515 MEFI.LOAD_BIAS1 123 19
1516 WAIT_SEC 1
1517 MEFI.LOAD_BIAS2 123 19
1518 WAIT_SEC 1
1519 MEFI.SET_DAC 0x80 0x00
1520 WAIT_SEC 1
1521 #
1522 # -0.390 V
1523 #
1524 MEFI.LOAD_BIAS1 123 3
1525 WAIT_SEC 1
1526 MEFI.LOAD_BIAS2 123 3
1527 WAIT_SEC 1
1528 MEFI.SET_DAC 0x80 0x00
1529 WAIT_SEC 1
1530 #
1531 # -0.400 V
1532 #
1533 MEFI.LOAD_BIAS1 122 226
1534 WAIT_SEC 1
1535 MEFI.LOAD_BIAS2 122 226
1536 WAIT_SEC 1
1537 MEFI.SET_DAC 0x80 0x00
1538 WAIT_SEC 1
1539 #
1540 # -0.405 V
1541 #
1542 MEFI.LOAD_BIAS1 122 209
1543 WAIT_SEC 1
1544 MEFI.LOAD_BIAS2 122 209
1545 WAIT_SEC 1
1546 MEFI.SET_DAC 0x80 0x00
1547 WAIT_SEC 1
1548 #
1549 # -0.410 V
1550 #
1551 MEFI.LOAD_BIAS1 122 193
1552 WAIT_SEC 1
1553 MEFI.LOAD_BIAS2 122 193
1554 WAIT_SEC 1
1555 MEFI.SET_DAC 0x80 0x00
1556 WAIT_SEC 1
1557 #

文 書 番 号	シ-ト No.	版 数
	172	1

1558 # -0.415 V
1559 #
1560 MEFI.LOAD_BIAS1 122 177
1561 WAIT_SEC 1
1562 MEFI.LOAD_BIAS2 122 177
1563 WAIT_SEC 1
1564 MEFI.SET_DAC 0x80 0x00
1565 WAIT_SEC 1
1566 #
1567 # -0.420 V
1568 #
1569 MEFI.LOAD_BIAS1 122 160
1570 WAIT_SEC 1
1571 MEFI.LOAD_BIAS2 122 160
1572 WAIT_SEC 1
1573 MEFI.SET_DAC 0x80 0x00
1574 WAIT_SEC 1
1575 #
1576 # -0.425 V
1577 #
1578 MEFI.LOAD_BIAS1 122 144
1579 WAIT_SEC 1
1580 MEFI.LOAD_BIAS2 122 144
1581 WAIT_SEC 1
1582 MEFI.SET_DAC 0x80 0x00
1583 WAIT_SEC 1
1584 #
1585 # -0.430 V
1586 #
1587 MEFI.LOAD_BIAS1 122 127
1588 WAIT_SEC 1
1589 MEFI.LOAD_BIAS2 122 127
1590 WAIT_SEC 1
1591 MEFI.SET_DAC 0x80 0x00
1592 WAIT_SEC 1
1593 #
1594 # -0.435 V
1595 #
1596 MEFI.LOAD_BIAS1 122 111
1597 WAIT_SEC 1
1598 MEFI.LOAD_BIAS2 122 111
1599 WAIT_SEC 1
1600 MEFI.SET_DAC 0x80 0x00
1601 WAIT_SEC 1
1602 #
1603 # -0.440 V
1604 #
1605 MEFI.LOAD_BIAS1 122 95
1606 WAIT_SEC 1
1607 MEFI.LOAD_BIAS2 122 95
1608 WAIT_SEC 1
1609 MEFI.SET_DAC 0x80 0x00

文 書 番 号	シ-ト No.	版 数
	173	1

```
1610 WAIT_SEC 1
1611 #
1612 # -0.445 V
1613 #
1614 MEFI.LOAD_BIAS1 122 78
1615 WAIT_SEC 1
1616 MEFI.LOAD_BIAS2 122 78
1617 WAIT_SEC 1
1618 MEFI.SET_DAC 0x80 0x00
1619 WAIT_SEC 1
1620 #
1621 # -0.450 V
1622 #
1623 MEFI.LOAD_BIAS1 122 62
1624 WAIT_SEC 1
1625 MEFI.LOAD_BIAS2 122 62
1626 WAIT_SEC 1
1627 MEFI.SET_DAC 0x80 0x00
1628 WAIT_SEC 1
1629 . #
1630 . #
1631 . MEFI.LOAD_BIAS1 0x80 0x00
1632 . MEFI.LOAD_BIAS2 0x80 0x00
1633 . MEFI.SET_DAC 0x80 0x00
1634 . #
```

文 書 番 号	シ-ト No.	版 数
	173-1	1

付録-2

```

#!HEAD: dcsn-f310-1 2018-06-05 19:27:09 169          45 MMO          DCSM
// func-d#13(2)_追加-1
8001  #
8002  . WAIT_SEC 40
8003  #
8004  # < CHECK >
8005  CHECK ENA. ENA_PROCESS_MODE          =MASS
8006  CHECK ENA. OBS_MODE                  =COINCI
8007  CHECK ENA. SV_WAVE1_TBL              2
8008  CHECK ENA. SV_WAVE2A_TBL            2
8009  CHECK ENA. SV_WAVE2B_TBL            2
8010  CHECK ENA. SV_LENS_TBL              2
8011  CHECK ENA. ENA_PHASE_NUMID          =n_16
8012  CHECK ENA. ENA_ENGY_NUMID           =n_8
8013  CHECK ENA. ENA_CH_NUMID             =n_8
8014  CHECK ENA. ENA_MASS_NUMID           =n_8
8015  CHECK ENA. ENA_ACCUM_NUMID          =sp_4
8016  CHECK ENA. ENA_TRG_ENADIS          =DIS
8017  #
8018  . DH. DR_REC_ON
8019  #
8020  . # < CHECK >
8021  CHECK DH. DR_REC_ON_OFF              =ON
8022  #
8023  WAIT_SEC 60
8024  #
8025  . # < cal pulse 発生(1kHz) >
8026  . MD1U. ENA_S_CAL_MODE_SET 0x31 0x01 0x01 0x00 0x00 0x02
8027  #
8028  WAIT_SEC 40
8029  #
8030  # < CHECK >
8031  CHECK ENA. CAL_FREQ                  =f_1k
8032  CHECK ENA. CAL_STARTR1_ON            =ON
8033  CHECK ENA. CAL_STARTS1_ON            =ON
8034  CHECK ENA. CAL_STOPS1_ON             =ON
8035  CHECK ENA. CAL_TIMING_STARTR         0x00
8036  CHECK ENA. CAL_TIMING_STARTS        0x00
8037  CHECK ENA. CAL_TIMING_STOPS         0x02
8038  #
8039  WAIT_SEC 20          # 1 分以上待つ (40+20 = 60s)
8040  #
8041  #
8042  . # < cal pulse 発生(1kHz) >
8043  . MD1U. ENA_S_CAL_MODE_SET 0x32 0x02 0x02 0x00 0x00 0x02
8044  #
8045  WAIT_SEC 40
8046  #
8047  # < CHECK >
8048  CHECK ENA. CAL_FREQ                  =f_1k
8049  CHECK ENA. CAL_STARTR2_ON            =ON

```

文 書 番 号	シート No.	版 数
	173-2	1

```

8050 CHECK ENA.CAL_STARTS2_ON          =ON
8051 CHECK ENA.CAL_STOPS2_ON          =ON
8052 CHECK ENA.CAL_TIMING_STARTR      0x00
8053 CHECK ENA.CAL_TIMING_STARTS      0x00
8054 CHECK ENA.CAL_TIMING_STOPS       0x02
8055 #
8056 WAIT_SEC 20                        # 1 分以上待つ (40+20 = 60s)
8057 #
8058 # < cal pulse 発生(1kHz) >
8059 MD1U.ENA_S_CAL_MODE_SET 0x34 0x04 0x04 0x00 0x00 0x02
8060 #
8061 WAIT_SEC 40
8062 #
8063 # < CHECK >
8064 CHECK ENA.CAL_FREQ                  =f_1k
8065 CHECK ENA.CAL_STARTR3_ON           =ON
8066 CHECK ENA.CAL_STARTS3_ON           =ON
8067 CHECK ENA.CAL_STOPS3_ON            =ON
8068 CHECK ENA.CAL_TIMING_STARTR        0x00
8069 CHECK ENA.CAL_TIMING_STARTS        0x00
8070 CHECK ENA.CAL_TIMING_STOPS         0x02
8071 #
8072 WAIT_SEC 20                        # 1 分以上待つ (40+20 = 60s)
8073 #
8074 # < cal pulse 発生(1kHz) >
8075 MD1U.ENA_S_CAL_MODE_SET 0x38 0x08 0x08 0x00 0x00 0x02
8076 #
8077 WAIT_SEC 40
8078 #
8079 # < CHECK >
8080 CHECK ENA.CAL_FREQ                  =f_1k
8081 CHECK ENA.CAL_STARTR4_ON           =ON
8082 CHECK ENA.CAL_STARTS4_ON           =ON
8083 CHECK ENA.CAL_STOPS4_ON            =ON
8084 CHECK ENA.CAL_TIMING_STARTR        0x00
8085 CHECK ENA.CAL_TIMING_STARTS        0x00
8086 CHECK ENA.CAL_TIMING_STOPS         0x02
8087 #
8088 WAIT_SEC 20                        # 1 分以上待つ (40+20 = 60s)
8089 #
8090 # < cal pulse 発生(1kHz) >
8091 MD1U.ENA_S_CAL_MODE_SET 0x31 0x10 0x10 0x00 0x00 0x02
8092 #
8093 WAIT_SEC 40
8094 #
8095 # < CHECK >
8096 CHECK ENA.CAL_FREQ                  =f_1k
8097 CHECK ENA.CAL_STARTR1_ON           =ON
8098 CHECK ENA.CAL_STARTS5_ON           =ON
8099 CHECK ENA.CAL_STOPS5_ON            =ON
8100 CHECK ENA.CAL_TIMING_STARTR        0x00
8101 CHECK ENA.CAL_TIMING_STARTS        0x00

```

文 書 番 号	シート No.	版 数
	173-3	1

```

8102 CHECK ENA.CAL_TIMING_STOPS 0x02
8103 #
8104 WAIT_SEC 20 # 1 分以上待つ (40+20 = 60s)
8105 #
8106 # < cal pulse 発生(1kHz) >
8107 MD1U.ENA_S_CAL_MODE_SET 0x32 0x20 0x20 0x00 0x00 0x02
8108 #
8109 WAIT_SEC 40
8110 #
8111 # < CHECK >
8112 CHECK ENA.CAL_FREQ =f_1k
8113 CHECK ENA.CAL_STARTR2_ON =ON
8114 CHECK ENA.CAL_STARTS6_ON =ON
8115 CHECK ENA.CAL_STOPS6_ON =ON
8116 CHECK ENA.CAL_TIMING_STARTR 0x00
8117 CHECK ENA.CAL_TIMING_STARTS 0x00
8118 CHECK ENA.CAL_TIMING_STOPS 0x02
8119 #
8120 WAIT_SEC 20 # 1 分以上待つ (40+20 = 60s)
8121 #
8122 # < cal pulse 発生(1kHz) >
8123 MD1U.ENA_S_CAL_MODE_SET 0x34 0x40 0x40 0x00 0x00 0x02
8124 #
8125 WAIT_SEC 40
8126 #
8127 # < CHECK >
8128 CHECK ENA.CAL_FREQ =f_1k
8129 CHECK ENA.CAL_STARTR3_ON =ON
8130 CHECK ENA.CAL_STARTS7_ON =ON
8131 CHECK ENA.CAL_STOPS7_ON =ON
8132 CHECK ENA.CAL_TIMING_STARTR 0x00
8133 CHECK ENA.CAL_TIMING_STARTS 0x00
8134 CHECK ENA.CAL_TIMING_STOPS 0x02
8135 #
8136 WAIT_SEC 20 # 1 分以上待つ (40+20 = 60s)
8137 #
8138 # < cal pulse 発生(1kHz) >
8139 MD1U.ENA_S_CAL_MODE_SET 0x38 0x40 0x80 0x00 0x00 0x02
8140 #
8141 WAIT_SEC 40
8142 #
8143 # < CHECK >
8144 CHECK ENA.CAL_FREQ =f_1k
8145 CHECK ENA.CAL_STARTR1_ON =ON
8146 CHECK ENA.CAL_STARTS7_ON =ON
8147 CHECK ENA.CAL_STOPS8_ON =ON
8148 CHECK ENA.CAL_TIMING_STARTR 0x00
8149 CHECK ENA.CAL_TIMING_STARTS 0x00
8150 CHECK ENA.CAL_TIMING_STOPS 0x02
8151 #
8152 WAIT_SEC 20 # 1 分以上待つ (40+20 = 60s)
8153 #

```

文 書 番 号	シート No.	版 数
	173-4	1

```
8154 . # < cal pulse 発生(1kHz) >
8155 . MD1U. ENA_S_CAL_MODE_SET 0x31 0x01 0x01 0x00 0x00 0x02
8156 #
8157 WAIT_SEC 40
8158 #
8159 . # < CHECK >
8160 CHECK ENA. CAL_FREQ =f_1k
8161 CHECK ENA. CAL_STARTR1_ON =ON
8162 CHECK ENA. CAL_STARTS1_ON =ON
8163 CHECK ENA. CAL_STOPS1_ON =ON
8164 CHECK ENA. CAL_TIMING_STARTR 0x00
8165 CHECK ENA. CAL_TIMING_STARTS 0x00
8166 CHECK ENA. CAL_TIMING_STOPS 0x02
8167 #
8168 WAIT_SEC 20 # 1 分以上待つ (40+20 = 60s)
8169 #
```

文 書 番 号	シート No.	版 数
	173-5	1

付録-3

```

#!HEAD: dcsn-f310-2 2018-06-08 23:43:00 128          45 MMO          DCSM
// func-d#13(2)_追加-2
8001 . #
8002 . # < **** ENA_OFF **** >
8003 . #
8004 . ENA.SHUTDOWN 0x00
8005 . #
8006 . WAIT_SEC 40
8007 . #
8008 . # < CHECK >
8009 . CHECK ENA.HV_MAIN_LVLSET          259.163 # 0x00
8010 . CHECK ENA.HV_STARTMCP_LVLSET      -912.18 # 0x00
8011 . CHECK ENA.HV_STOPMCP_LVLSET       -1095.1 # 0x00
8012 . CHECK ENA.HV_DEF_LVLSET           -158.482 # 0x00
8013 . CHECK ENA.HV_ON                   =OFF
8014 . CHECK ENA.HV_SHUTDOWN_MODE        =NML
8015 . #
8016 . ENA.CLEAR_FLG 0x03
8017 . #
8018 . # < CHECK >
8019 . CHECK ENA.HV_SHUTDOWN_MODE        =NON
8020 . #
8021 . DH.ENA_OFF # DH.MC_EACH_EXE 0x00A8
8022 . #:          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
8023 . #:          0x00 MD1F.ENA_CLCT_STOP
8024 . #:          0x0D PCD.ON_OFF_ENA
8025 . #:          0x0E PCD.PSU_ENA_OFF
8026 . #:          0x0F PCD.ON_OFF_DIS
8027 . #
8028 . # < CHECK >
8029 . CHECK PCD.PSU_ENA_ON_OFF           =OFF
8030 . CHECK MD1F.MS3_LINK_STAT           =OFF
8031 . CHECK MD1F.MS3_DAT_CLCT           =OFF
8032 . #
8033 . #
8034 . # QL          Check-Item          Spec          Check
8035 . # -----+-----+-----+-----+-----+-----+-----+-----+-----+
8036 . # CMD 出力装置  Message          STATUS CHECK 結果 OK   G   N
8037 . #
8038 . #
8039 . #
8040 . # < **** ENA ON HV ON **** >
8041 . #
8042 . # !!! 以下のマクロ内容は高圧セット値が全て 1 となっていること。!!!
8043 . # !!!   また、マクロ内で safety を解除していないこと          !!!
8044 . #
8045 . CHECK PCD.PSU_ENA_ON_OFF           =OFF
8046 . #
8047 . DH.ENA_ON_HV_ON # DH.MC_EACH_EXE 0x0115
8048 . #
8049 . WAIT_SEC 480

```

文 書 番 号	シ-ト No.	版 数
	173-6	1

```

8050 #
8051 CHECK ENA. HV_SAFTY_DIS =ENA
8052 CHECK ENA. HV_SHUTDOWN_MODE =NON
8053 CHECK ENA. HV_ON =ON
8054 CHECK ENA. HV_MAIN_LVLSET 279.54 # 0x01
8055 CHECK ENA. HV_STARTMCP_LVLSET -894.83 # 0x01
8056 CHECK ENA. HV_STOPMCP_LVLSET -1076.5 # 0x01
8057 CHECK ENA. HV_DEF_LVLSET -145.03 # 0x01
8058 CHECK ENA. ENA_HK_INTVAL =sec_256
8059 #
8060 . DH. DR_REC_ON
8061 #
8062 . # < CHECK >
8063 CHECK DH. DR_REC_ON_OFF =ON
8064 #
8065 . # << 1 分以上待つ >>
8066 . #
8067 . DH. DR_REC_OFF
8068 #
8069 . # < CHECK >
8070 CHECK DH. DR_REC_ON_OFF =OFF
8071 #
8072 #
8073 . # < cal pulse 発生(1kHz) >
8074 . MD1U. ENA_S_CAL_MODE_SET 0x31 0x01 0x01 0x00 0x00 0x02
8075 #
8076 WAIT_SEC 40
8077 #
8078 . # < CHECK >
8079 CHECK ENA. CAL_FREQ =f_1k
8080 CHECK ENA. CAL_STARTR1_ON =ON
8081 CHECK ENA. CAL_STARTS1_ON =ON
8082 CHECK ENA. CAL_STOPS1_ON =ON
8083 CHECK ENA. CAL_TIMING_STARTR 0x00
8084 CHECK ENA. CAL_TIMING_STARTS 0x00
8085 CHECK ENA. CAL_TIMING_STOPS 0x02
8086 #
8087 . DH. DR_REC_ON
8088 #
8089 . # < CHECK >
8090 CHECK DH. DR_REC_ON_OFF =ON
8091 #
8092 . # << 1 分以上待つ >>
8093 . #
8094 . DH. DR_REC_OFF
8095 #
8096 . # < CHECK >
8097 CHECK DH. DR_REC_ON_OFF =OFF
8098 #
8099 . # < **** HV SHUTDOWN **** >
8100 . #
8101 . ENA. SHUTDOWN 0x00

```

文 書 番 号	シ-ト No.	版 数
	173-7	1

```
8102      #
8103      WAIT_SEC 40
8104      #
8105      # < CHECK >
8106      CHECK ENA. HV_MAIN_LVLSET          259.163 # 0x00
8107      CHECK ENA. HV_STARTMCP_LVLSET      -912.18 # 0x00
8108      CHECK ENA. HV_STOPMCP_LVLSET        -1095.1 # 0x00
8109      CHECK ENA. HV_DEF_LVLSET            -158.482 # 0x00
8110      CHECK ENA. HV_ON                    =OFF
8111      CHECK ENA. HV_SHUTDOWN_MODE        =NML
8112      . #
8113      . ENA. CLEAR_FLG 0x03
8114      #
8115      . # < CHECK >
8116      CHECK ENA. HV_SHUTDOWN_MODE          =NON
8117      #
8118      . DH. DR_REC_ON
8119      #
8120      . # < CHECK >
8121      CHECK DH. DR_REC_ON_OFF              =ON
8122      #
8123      . # << 1 分以上待つ >>
8124      . #
8125      . DH. DR_REC_OFF
8126      #
8127      . CHECK DH. DR_REC_ON_OFF          =OFF
8128      #
```

文 書 番 号	シート No.	版 数
TPP-FS8238-31K	174	1

表 4-1 ソフトウェアバージョン管理表

Component	Software(オブジェクト)/Table	Version	Program (ソース)	Revision
DMC	IPLソフトウェア	1.00.00	IPL	77
	DMCソフトウェア	1.01.23	PLTS	1850
			DHFS	1615
			ACFS	1553
			TCFS	1443
			T-Kernel	293
			T-Monitor	110
	MC-S_EEPROM	5.37	-	-
	MC-L_EEPROM	5.44	-	-
	ST_EEPROM	5.36	-	-
	AT_EEPROM	5.43	-	-
	RQ_EEPROM	5.13	-	-
	CATEGORY_TABLE_EEPROM	3.8	-	-
	PARTITION_TABLE_EEPROM	3.9	-	-
MDP (DPU1)	TM_DATA_BLOCK_TABLE_EEPROM	3.9	-	-
	TCS_TBL_ROM	2.3	-	-
MDP (DPU2)	Middleware	2018/06/08(Ver. 7.5)	-	-
	Application software	2018/06/08(Ver. 7.5)	-	-
MSA	Middleware	2018/06/08(Ver. 7.5)	-	-
	Application software	2018/06/08(Ver. 7.5)	-	-
MIA	-	2018/06/11(Ver. 42)	-	-
MEA1	-	2015/2/13	-	-
MEA2	-	2017/1/26	-	-

*供試体がない場合はN/Aとすること。

文 書 番 号	シート No.	版 数
	175	1

5 コンフィギュレーション

コネクタの接続については、実施者と非実施者によるダブルチェックを実施し記録を残すこと

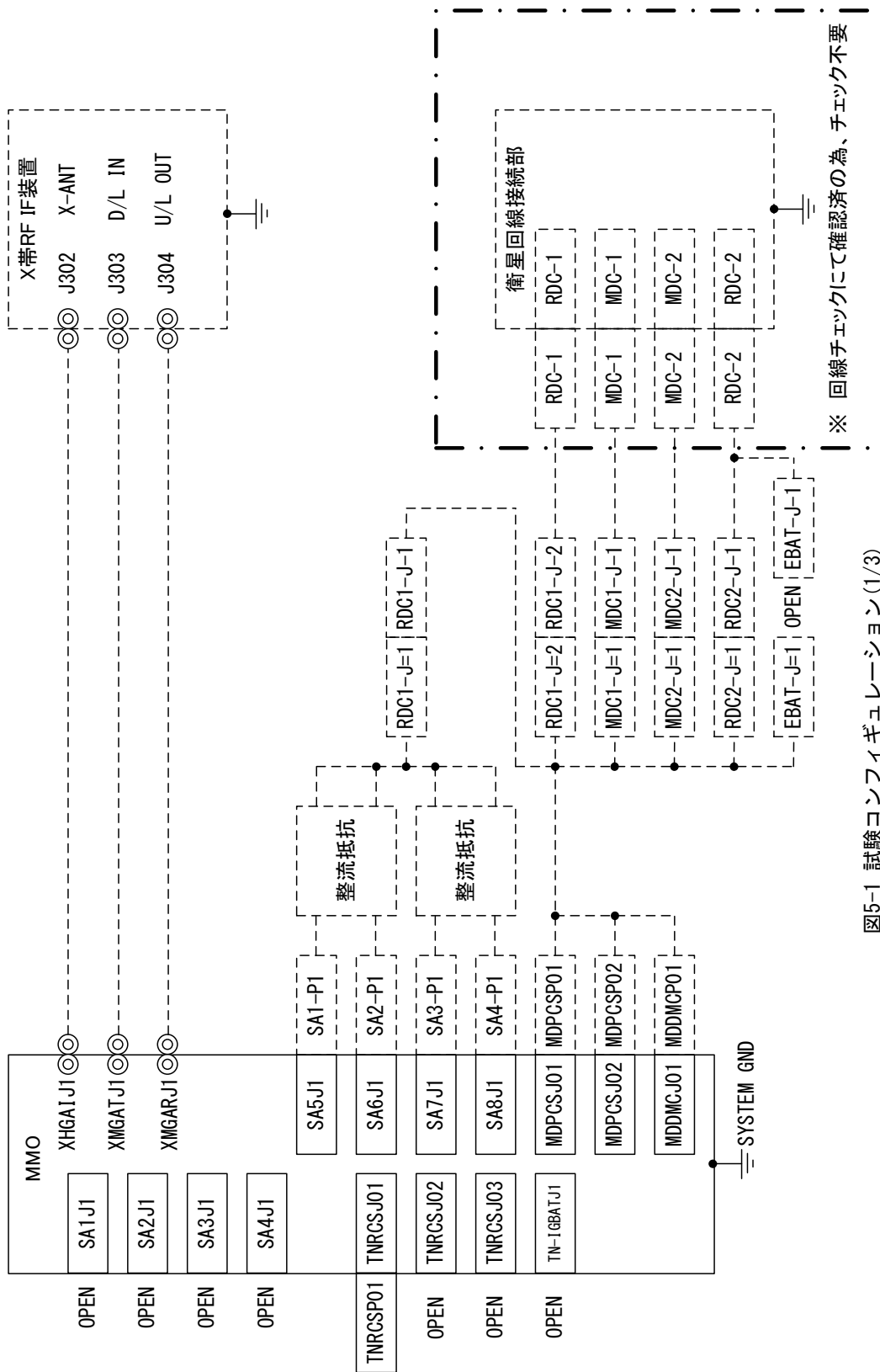


図5-1 試験コンフィギュレーション(1/3)

文 書 番 号	シート No.	版 数
	176	1

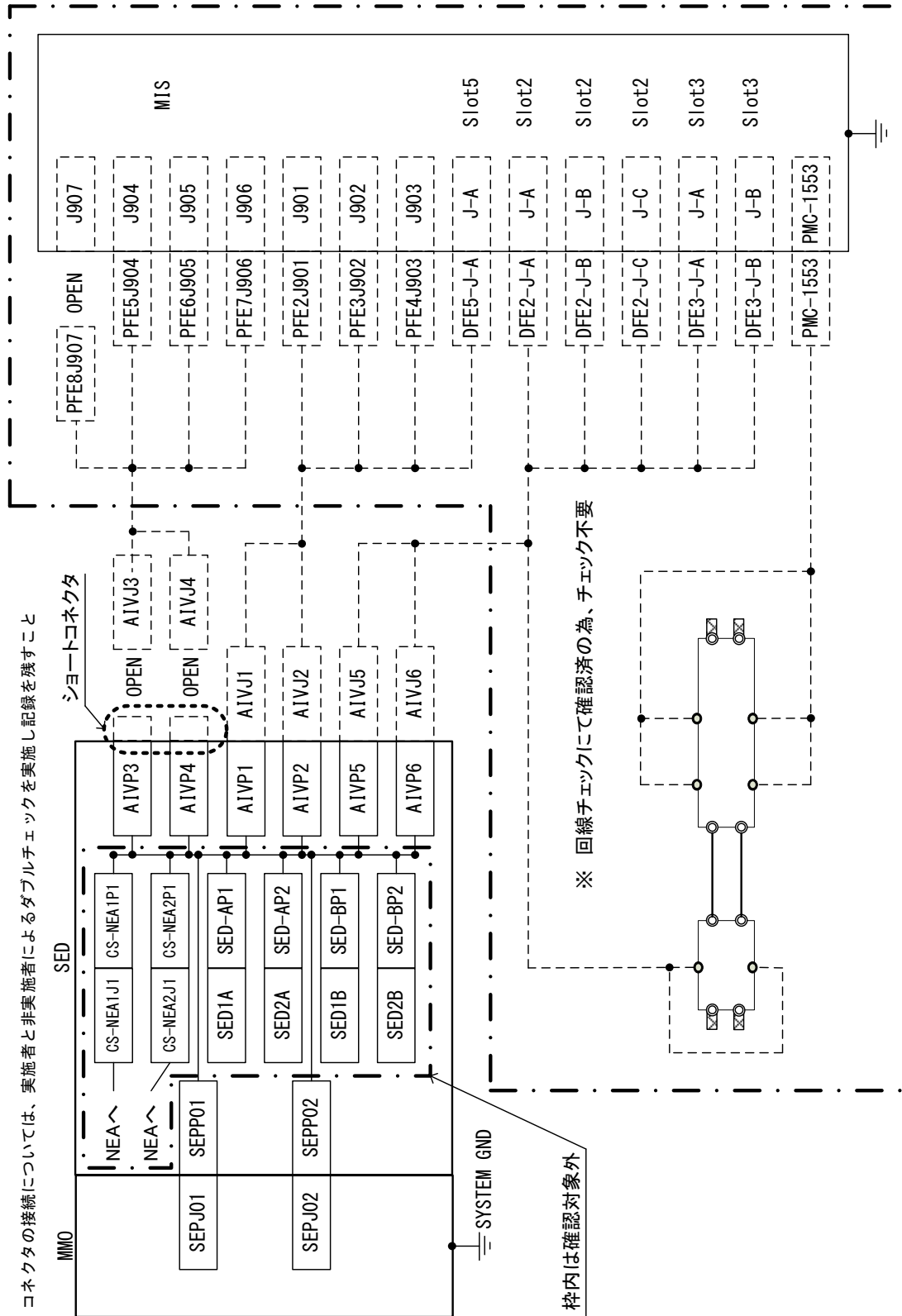


図5-1 試験コンフィギュレーション(2/3)

文 書 番 号	シート No.	版 数
	177	1

コネクタの接続については、実施者と非実施者によるダブルチェックを実施し記録を残すこと

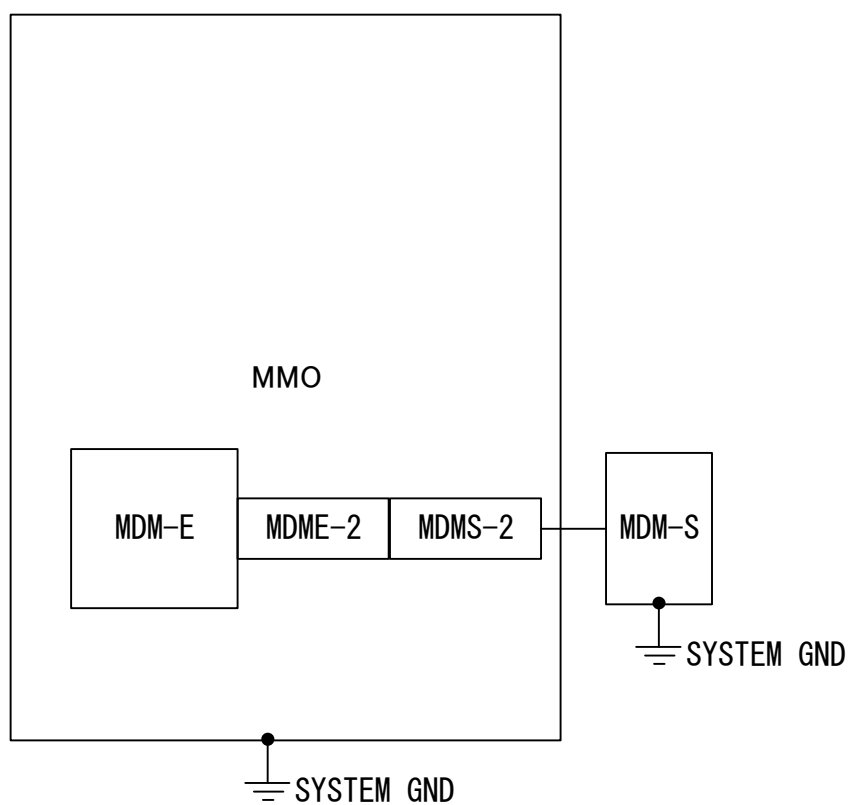


図5-1 試験コンフィギュレーション(3/3)

文 書 番 号	シート No.	版 数
TPP-FS8238-31K	178	1

表 5-1 コネクタ嵌合一覧

IFコネクタ名称	嵌合○/ 非嵌合×	用途	備考
TNRCSJ01	○	圧力センサモニタ HLV OPEN/CLOSEモニタ タンク温度モニタ	RCS圧力センサ確認のため嵌合
TNRCSJ02	×	HLVドライブ	誤動作リスク低減のためOPEN
TNRCSJ03	×	THVドライブ	誤動作リスク低減のためOPEN
TN-IGBATJ1	×	NEAドライブ(HGA/MGA)	NEA誤動作リスクを低減するためOPEN
SA1J1	×	SAP1電源入力	使用しないI/FのためOPEN
SA2J1	×	SAP2電源入力	使用しないI/FのためOPEN
SA3J1	×	SAP3電源入力	使用しないI/FのためOPEN
SA4J1	×	SAP4電源入力	使用しないI/FのためOPEN
SA5J1	○	SAP5電源入力	EXT-PS入力のため地上試験装置と接続
SA6J1	○	SAP6電源入力	EXT-PS入力のため地上試験装置と接続
SA7J1	○	SAP7電源入力	EXT-PS入力のため地上試験装置と接続
SA8J1	○	SAP8電源入力	EXT-PS入力のため地上試験装置と接続
XHGAIJ1	○	HGA TX/RX	試験をRF有線で実施のため試験装置と接続
XMGATJ1	○	MGA-TX TX	試験をRF有線で実施のため試験装置と接続
XMGARJ1	○	MGA RX	試験をRF有線で実施のため試験装置と接続
MDPCSJ01	○	MD(電流/電圧値)	モニタ用のため地上試験装置と接続
MDPCSJ02	○	MD(電流/AGC値)	モニタ用のため地上試験装置と接続
MDDMCJ01	○	RD(CMD通信)	RDによる通信のため地上試験装置と接続
AIVP1	○	電源(28V/SEDヒーター)(主系)(CNV-A) CNV PWR SWドライブ(主系)(CNV-A)	MIS電源使用のため嵌合(バックアップ)
AIVP2	○	電源(28V/SEDヒーター)(主系)(CNV-B) CNV PWR SWドライブ(主系)(CNV-B)	MIS電源使用のため嵌合(バックアップ)
AIVP3	○	NEA/Pyroドライブ(CSM/SED)(主系)	安全のためショートコネクタを嵌合
AIVP4	○	NEA/Pyroドライブ(CSM/SED)(従系)	安全のためショートコネクタを嵌合
AIVP5	○	分離ステータス/温度モニタ(主系) 1553B通信(主系)	1553B I/F使用のため嵌合(バックアップ)
AIVP6	○	分離ステータス/温度モニタ(従系) 1553B通信(従系)	1553B I/F使用のため嵌合(バックアップ)

文 書 番 号	シート No.	版 数
	179	1

6 データシート

<データシート2. 7-1 消費電力取得 (QL/MD)>

実施日:

実施者:

No.	項目	QL取得値	MD取得値	No.	項目	QL取得値	MD取得値
1	SA_V	V	V	19	XTRP_A_I	A	A
2	SA_I	A	A	20	XTRP_B_I	A	A
3	BUS_V	V	V	21	PSU_MDP1_I	A	A
4	LOAD_I	A	A	22	PSU_MDP2_I	A	A
5	BAT_V	V	V	23	PSU_PME_I	A	A
6	CHG_I	A	A	24	PSU_MGF_I_I	A	A
7	DCHG_I	A	A	25	PSU_MOTOR_A_I	A	A
8	PCD_I	A	A	26	PSU_MOTOR_B_I	A	A
9	DMC_PSU_A_I	A	A	27	PSU_ENA_I	A	A
10	DMC_PSU_B_I	A	A	28	PSU_MEA1_I	A	A
11	DMC_PSU_C_I	A	A	29	PSU_MEA2_I	A	A
12	EPC_A_I	A	A	30	PSU_MSA_I	A	A
13	EPC_B_I	A	A	31	PSU_MIA_I	A	A
14	DRU_HTR_I	A	A	32	PSU_HEP_I_I	A	A
15	DRU_PSU_A_I	A	A	33	PSU_HEP_E_I	A	A
16	DRU_PSU_B_I	A	A	34	MDM_E_I	A	A
17	ADMCE_A_I	A	A	35	MSASI_I	A	A
18	ADMCE_B_I	A	A	36	SURVIVAL_HTR_I	A	A

文 書 番 号

シート No.

版 数

TPP-FS8238-31K

180

1

<データシート3.17-1 消費電力取得 (QL/MD)>

実施日:

実施者:

No.	項目	QL取得値	MD取得値
1	SA_V	V	V
2	SA_I	A	A
3	BUS-V	V	V
4	LOAD_I	A	A
5	BAT_V	V	V
6	CHG_I	A	A
7	DCHG_I	A	A
8	PCD_I	A	A
9	DMC_PSU_A_I	A	A
10	DMC_PSU_B_I	A	A
11	DMC_PSU_C_I	A	A
12	EPC_A_I	A	A
13	EPC_B_I	A	A
14	DRU_HTR_I	A	A
15	DRU_PSU_A_I	A	A
16	DRU_PSU_B_I	A	A
17	ADMCE_A_I	A	A
18	ADMCE_B_I	A	A

No.	項目	QL取得値	MD取得値
19	XTRP_A_I	A	A
20	XTRP_B_I	A	A
21	PSU_MDP1_I	A	A
22	PSU_MDP2_I	A	A
23	PSU_PME_I	A	A
24	PSU_MGF_I_I	A	A
25	PSU_MOTOR_A_I	A	A
26	PSU_MOTOR_B_I	A	A
27	PSU_ENA_I	A	A
28	PSU_MEA1_I	A	A
29	PSU_MEA2_I	A	A
30	PSU_MSA_I	A	A
31	PSU_MIA_I	A	A
32	PSU_HEP_I_I	A	A
33	PSU_HEP_E_I	A	A
34	MDM_E_I	A	A
35	MSAS1_I	A	A
36	SURVIVAL_HTR_I	A	A

文 書 番 号	シート No.	版 数
	181	1

<データシート4. 14-1 消費電力取得 (QL/MD)>

実施日:

実施者:

No.	項目	QL取得値	MD取得値
1	SA_V	V	V
2	SA_I	A	A
3	BUS-V	V	V
4	LOAD_I	A	A
5	BAT_V	V	V
6	CHG_I	A	A
7	DCHG_I	A	A
8	PCD_I	A	A
9	DMC_PSU_A_I	A	A
10	DMC_PSU_B_I	A	A
11	DMC_PSU_C_I	A	A
12	EPC_A_I	A	A
13	EPC_B_I	A	A
14	DRU_HTR_I	A	A
15	DRU_PSU_A_I	A	A
16	DRU_PSU_B_I	A	A
17	ADMCE_A_I	A	A
18	ADMCE_B_I	A	A

No.	項目	QL取得値	MD取得値
19	XTRP_A_I	A	A
20	XTRP_B_I	A	A
21	PSU_MDP1_I	A	A
22	PSU_MDP2_I	A	A
23	PSU_PME_I	A	A
24	PSU_MGF_I_I	A	A
25	PSU_MOTOR_A_I	A	A
26	PSU_MOTOR_B_I	A	A
27	PSU_ENA_I	A	A
28	PSU_MEA1_I	A	A
29	PSU_MEA2_I	A	A
30	PSU_MSA_I	A	A
31	PSU_MIA_I	A	A
32	PSU_HEP_I_I	A	A
33	PSU_HEP_E_I	A	A
34	MDM_E_I	A	A
35	MSAS1_I	A	A
36	SURVIVAL_HTR_I	A	A

表 7-1 不具合記録表

[illegible]