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### 3.7 試験/作業項目 (試験フロー)

本試験の手順フローを以下に示す。

Main2-7850 (MEA1/MEA2 低圧部チェック)

1. SETUP/衛星立ち上げ
  - 1.1. SETUP
  - 1.2. 衛星立ち上げ
    - 1.2.1. CNV-A MAIN MODE 選択
    - 1.2.2. CNV-B MAIN MODE 選択
    - 1.2.3. 衛星立ち上げ (CNV)
    - 1.2.4. CMD/TLM LINK 確認 (1553B)
    - 1.2.5. BAT INT
    - 1.2.6. インデックスパルス出力
    - 1.2.7. TCFS 設定
  - 1.3. SI 系初期 CO 時における AT/RQ/ST ENA/DIS 設定 (MDP 立ち上げ前)
  - 1.4. SI 機器初期チェックアウト時設定
2. MDP 立ち上げ
3. MEA1 初期 CO
  - 3.1. MEA1 function check
    - 3.1.1. MEA1 Startup  
 <<M15-1 Initial Power ON   Duration:10 min Day:35>>
    - 3.1.2. MEM\_DUMP  
 <<M15-2 Cal Check   Duration:40 min Day:35>>
    - 3.1.3. SWITCH ON SCAN, 10 min
    - 3.1.4. TEST MODE ON, 15 min
    - 3.1.5. TEST MODE OFF, 10 min
    - 3.1.6. SWITCH OFF the SCAN (5 minutes)
  - 3.2. MEA1 SHUTDOWN
4. MEA2 初期 CO
  - 4.1. MEA2 function check
    - 4.1.1. MEA2 Startup  
 <<M16-1 Initial Power ON   Duration:10 min Day:35>>
    - 4.1.2. MEM\_DUMP  
 <<M16-2 Cal Check   Duration:40 min Day:35>>
    - 4.1.3. SWITCH ON SCAN, 10 min
    - 4.1.4. TEST MODE ON, 15 min
    - 4.1.5. TEST MODE OFF, 10 min
    - 4.1.6. SWITCH OFF the SCAN (5 minutes)
  - 4.2. MEA2 SHUTDOWN
5. 立ち下げ手順
  - 5.1. MDP 立ち下げ
  - 5.2. TCS 立ち下げ (HTR CTRL OFF/DRU\_OFF)
  - 5.3. MC\_DIS
  - 5.4. 衛星立ち下げ

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Main2-7851 (MEA1 高圧部チェック)

1. SETUP/衛星立ち上げ

1.1. SETUP

1.2. 衛星立ち上げ

1.2.1. CNV-A MAIN MODE 選択

1.2.2. CNV-B MAIN MODE 選択

1.2.3. 衛星立ち上げ (CNV)

1.2.4. CMD/TLM LINK 確認 (1553B)

1.2.5. BAT INT

1.2.6. インデックスパルス出力

1.2.7. TCFS 設定

1.3. SI 系初期 CO 時における AT/RQ/ST ENA/DIS 設定 (MDP 立ち上げ前)

1.4. SI 機器初期チェックアウト時設定

2. MDP 立ち上げ

3. MEA1 初期高圧 CO

3.1. MEA1 LV check

3.1.1. MEA1 SWITCH ON

<<M15-5 Power ON Duration:5 min Day:69>>

3.1.2. MEM\_DUMP

<<M15-6 Cal Check Duration:15 min Day:69>>

3.2. SECURITY OFF

<<M15-7 DISABLE HV SECURITY Duration:5 min Day:69>>

3.2.1. DISABLE HV SECURITY (MCP + SPHERES), 10 min"

3.2.2. DISABLE MCP SECURITY

3.2.3. Disable the MCP bit security (This function is checked after)

3.3. SWITCH ON MCP (2750V in vacuum)

<<M15-8 SWITCH ON MCP Duration:140 min Day:69>>

3.3.1. SWITCH ON MCP, 25 min to verify the MCP HV

3.3.2. ENABLE MCP SECURITY, 5 min

3.3.3. DISABLE MCP SECURITY, 5 min

3.3.4. SWITCH OFF MCP, 25 min

3.4. SWITCH ON SCAN, 15 min

<<M15-9 SWITCH ON SCAN Duration:120 min Day:69>>

3.5. TEST MODE ON, 150 min

<<M15-10 TEST MODE ON Duration:30 min Day:69>>

3.6. TEST MODE OFF, 10 min

<<M15-12 TEST MODE OFF Duration:5 min Day:69>>

3.7. ENABLE HV of ANALYZER (25 minutes)

<<M15-\*\* Duration:\*\* min Day:69>>

3.8. SWITCH ON MCP to verify the MCP noise (25 minutes)

<<M15-\*\* Duration:\*\* min Day:69>>

3.9. GF (20 minutes duration, 5 minutes per geometric factor)

<<M15-\*\* Duration:\*\* min Day:69>>

3.10. SWITCH OFF MCP in VACUUM (10 MINUTES)

<<M15-\*\* Duration:\*\* min Day:69>>

3.11. SWITCH OFF the SCAN (15 minutes)

<<M15-\*\* Duration:\*\* min Day:69>>

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## 4. MEA1 SHUTDOWN

## 5. 立ち下げ手順

- 5.1. MDP 立ち下げ
- 5.2. TCS 立ち下げ (HTR CTRL OFF/DRU\_OFF)
- 5.3. MC\_DIS
- 5.4. 衛星立ち下げ

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Main2-7852 (MEA2 高圧部チェック)

1. SETUP/衛星立ち上げ

1.1. SETUP

1.2. 衛星立ち上げ

1.2.1. CNV-A MAIN MODE 選択

1.2.2. CNV-B MAIN MODE 選択

1.2.3. 衛星立ち上げ (CNV)

1.2.4. CMD/TLM LINK 確認 (1553B)

1.2.5. BAT INT

1.2.6. インデックスパルス出力

1.2.7. TCFS 設定

1.3. SI 系初期 CO 時における AT/RQ/ST ENA/DIS 設定 (MDP 立ち上げ前)

1.4. SI 機器初期チェックアウト時設定

2. MDP 立ち上げ

3. MEA2 初期高圧 CO

3.1. MEA2 LV check

3.1.1. MEA2 SWITCH ON

<<M16-5 Power ON Duration:5 min Day:70>>

3.1.2. MEM\_DUMP

<<M16-6 Cal Check Duration:15 min Day:70>>

3.2. SECURITY OFF

<<M16-7 DISABLE HV SECURITY Duration:5 min Day:70>>

3.2.1. DISABLE HV SECURITY (MCP + SPHERES), 10 min"

3.2.2. DISABLE MCP SECURITY

3.2.3. Disable the MCP bit security (This function is checked after)

3.3. SWITCH ON MCP (2750V in vacuum)

<<M16-8 SWITCH ON MCP Duration:140 min Day:70>>

3.3.1. SWITCH ON MCP, 25 min to verify the MCP HV

3.3.2. ENABLE MCP SECURITY, 5 min

3.3.3. DISABLE MCP SECURITY, 5 min

3.3.4. SWITCH OFF MCP, 25 min

3.4. SWITCH ON SCAN, 15 min

<<M16-9 SWITCH ON SCAN Duration:120 min Day:70>>

3.5. TEST MODE ON, 150 min

<<M16-10 TEST MODE ON Duration:30 min Day:70>>

3.6. TEST MODE OFF, 10 min

<<M16-12 TEST MODE OFF Duration:5 min Day:70>>

3.7. ENABLE HV of ANALYZER (25 minutes)

<<M16-\*\* Duration:\*\* min Day:70>>

3.8. SWITCH ON MCP to verify the MCP noise (25 minutes)

<<M16-\*\* Duration:\*\* min Day:70>>

3.9. GF (20 minutes duration, 5 minutes per geometric factor)

<<M16-\*\* Duration:\*\* min Day:70>>

3.10. SWITCH OFF MCP in VACUUM (10 MINUTES)

<<M16-\*\* Duration:\*\* min Day:70>>

3.11. SWITCH OFF the SCAN (15 minutes)

<<M16-\*\* Duration:\*\* min Day:70>>

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## 4. MEA2 SHUTDOWN

## 5. 立ち下げ手順

- 5.1. MDP 立ち下げ
- 5.2. TCS 立ち下げ(HTR CTRL OFF/DRU\_OFF)
- 5.3. MC\_DIS
- 5.4. 衛星立ち下げ
- 5.5. 試験コンフィギュレーションの解除

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0049 . # (7) Main Menu: Control -> System Control を選択  
 0050 . # (8) System Control 画面の FE status Storage selection において  
 0051 . #       □ PFE  
 0052 . #       □ MFE  
 0053 . #       □ DFE  
 0054 . #       をクリックする。チェックマークが表示される  
 0055 . # (9) Enabled Selected Categories ボタンをクリック  
 0056 . #       (ログ取得開始)  
 0057 . # (10) System Control 画面で“CCI”をクリック  
 0058 . # (11) Interface Management:  
 0059 . #       Open Interface  
 0060 . #       TM Distribution: Enable  
 0061 . #       TC Handling: Enable  
 0062 . #       をクリック  
 0063 . # (12) I/F 変換装置の Operation Log 画面で以下のメッセージを確認する  
 0064 . #       Socket opened CCS/MIS-EGSE 3000  
 0065 . #       Socket opened CCS/MIS-EGSE 3001  
 0066 . # (13) System Control 画面で“MFE”をクリック  
 0067 . # (14) Bus profile ボタンをクリック  
 0068 . # (15) Polling Sequence Table Selection and Verification において  
 0069 . #       ファイル BCMIS\_MMO PST defs\BCMIS\_MMO.PST を選択し、Verify ボタンを  
 0070 . #       クリック → load ボタンをクリック  
 0071 . # (16) Polling Sequence Table Control において、start ボタンをクリック  
 0072 . #       (1553B バスへの Sync メッセージ出力開始)  
 0073 . # (17) System Control 画面で“PFE”をクリック  
 0074 . # (18) Power Control Bus A の“OFF”をクリック  
 0075 . #       “OFF”→“ON”になる  
 0076 . # (19) External Bus Power Supply の、DC VOLTS 表示 = 28.00 を確認する  
 0077 . # (20) PFE Platform の、Under Voltage の LED=消灯を確認する  
 0078 . # (21) System Control -> DFE -> Aquisition Control -> Start をクリック  
 0079 . #  
 0080 . # 【MIS 操作】 System Control->MFE->Platform->Bus:A 選択->Apply  
 0081 . #  
 0082 . # 【操作】  
 0083 . # 下記手順に従い、MIS にて 1553B バスモニタを開始する。  
 0084 . # (1)start 1553 BM client アイコンをダブルクリック  
 0085 . #       Bus Monitor が起動する  
 0086 . # (2)Main Menu: Mode -> On-Line Mode クリック  
 0087 . # (3)Main Menu: Mode -> Start New Acquisition クリック  
 0088 . #  
 0089 . # 【局管制 設定】  
 0090 . # 初期設定               : INIT\_MMO\_RF. -> INIT\_MMO\_RD.  
 0091 . #  
 0092 . # 【衛星管制 設定】  
 0093 . # 接続 CMD               : ccs-mis-if1  
 0094 . # 接続 TLM               : ccs-mis-if1  
 0095 . # プリチェック           : OFF  
 0096 . # センドベリファイ       : 強制 Type-B  
 0097 . # サクセスベリファイ     : OFF  
 0098 . # RF/LINE 切換え          : RF  
 0099 . #  
 0100 . # 実運用(ESOC からの運用 or MPO MTL による運用)を想定し、

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0101 . # プリチェック OFF、センドベリファイ 強制 Type-B、サクセスベリファイ OFF とする。
0102 . #
0103 . # 【地上装置設定】
0104 . # EXT-PS CV: 90.0V
0105 . #      CC: 6.0A
0106 . # OVP   54.0V
0107 . # UVP   32.0V
0108 . #
0109 . # 【共通 QL 設定】
0110 . # MPO_RL_X 選択
0111 . #
0112 . # =====
0113 . # 1.2. 衛星立ち上げ
0114 . # =====
0115 . # -----
0116 . # 1.2.1. CNV-A MAIN MODE 選択
0117 . # -----
0118 . #
0119 . # MIS にて "HPC_Command1(MIS 出力 ch37)"を出力し、CNV-A の MODE を"MAIN"へ
0120 . # 切り替えること。
0121 . #
0122 . # 【MIS コマンド操作】
0123 . # "SYSTEM Control → DFE → IO Activation Control → IO Number → 37
0124 . # → Activate"
0125 . #
0126 . # 【MIS ステータス確認操作】
0127 . # "View → TM/TC Data Monitor → FE Data Filter
0128 . # → DFE TM Packet をチェック → Data Category List 右クリック
0129 . # → Track Latest Entry → Datafield Decode → Acquisition Data
0130 . # → Relay Status 参照"
0131 . #
0132 . # MIS          Check-Item          Spec          Check
0133 . # -----+-----+-----+-----
0134 . # Relay Status CNV-A MAIN (RLY6)    SHORT:32767(dec) 以下    G  N
0135 . #              CNV-A HTR  (RLY7)    OPEN :32768(dec) 以上    G  N
0136 . #
0137 . # -----
0138 . # 1.2.2. CNV-B MAIN MODE 選択
0139 . # -----
0140 . #
0141 . # MIS にて "HPC_Command3(MIS 出力 ch39)"を出力し、CNV-B の MODE を"MAIN"へ
0142 . # 切り替えること。
0143 . #
0144 . # 【MIS コマンド操作】
0145 . # "SYSTEM Control → DFE → IO Activation Control → IO Number → 39
0146 . # → Activate"
0147 . #
0148 . # 【MIS ステータス確認操作】
0149 . # "View → TM/TC Data Monitor → FE Data Filter
0150 . # → DFE TM Packet をチェック → Data Category List 右クリック
0151 . # → Track Latest Entry → Datafield Decode → Acquisition Data
0152 . # → Relay Status 参照"

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| 0153 | # |                                                      |                    |                      |       |
|------|---|------------------------------------------------------|--------------------|----------------------|-------|
| 0154 | # | MIS                                                  | Check-Item         | Spec                 | Check |
| 0155 | # | -----+-----+-----+-----                              |                    |                      |       |
| 0156 | # | Relay Status                                         | CNV-B MAIN (RLY13) | SHORT :32767(dec) 以下 | G N   |
| 0157 | # |                                                      | CNV-B HTR (RLY14)  | OPEN :32768(dec) 以上  | G N   |
| 0158 | # |                                                      |                    |                      |       |
| 0159 | # | -----+-----+-----+-----                              |                    |                      |       |
| 0160 | # | 1. 2. 3. 衛星立ち上げ(CNV)                                 |                    |                      |       |
| 0161 | # | -----+-----+-----+-----                              |                    |                      |       |
| 0162 | # |                                                      |                    |                      |       |
| 0163 | # | 【MIS コマンド操作】                                         |                    |                      |       |
| 0164 | # | MIS 電源出力 A 系 ON(SV-HTR)                              |                    |                      |       |
| 0165 | # | "SYSTEM Control → PFE → Modules[2] → PRI → ON → YES" |                    |                      |       |
| 0166 | # |                                                      |                    |                      |       |
| 0167 | # | -----+-----+-----+-----                              |                    |                      |       |
| 0168 | # | 1. 2. 4. CMD/TLM LINK 確認(1553B)                      |                    |                      |       |
| 0169 | # | -----+-----+-----+-----                              |                    |                      |       |
| 0170 | # |                                                      |                    |                      |       |
| 0171 | # | < CHECK >                                            |                    |                      |       |
| 0172 |   | CHECK DH. CATEGORY_TBL_NO                            |                    | 0x00                 |       |
| 0173 |   | CHECK DH. DWNLNK_RATE                                |                    | =BPS32768            |       |
| 0174 |   | CHECK DH. ATMC_FMT_ERR                               |                    | =OK                  |       |
| 0175 |   | CHECK DH. ATMC_SUM_STAT                              |                    | =OK                  |       |
| 0176 |   | CHECK DH. MC_FMT_ERR                                 |                    | =OK                  |       |
| 0177 |   | CHECK DH. MC_SUM_STAT                                |                    | =OK                  |       |
| 0178 |   | CHECK DH. RQMC_FMT_ERR                               |                    | =OK                  |       |
| 0179 |   | CHECK DH. RQMC_SUM_STAT                              |                    | =OK                  |       |
| 0180 |   | CHECK DH. SYSTM_R_FMT_ERR                            |                    | =OK                  |       |
| 0181 |   | CHECK DH. SYSTM_R_SUM_STAT                           |                    | =OK                  |       |
| 0182 |   | CHECK DH. TL_FMT_ERR                                 |                    | =OK                  |       |
| 0183 |   | CHECK DH. TL_SUM_STAT                                |                    | =OK                  |       |
| 0184 |   | CHECK DH. CATGRY_SUM_STAT                            |                    | =OK                  |       |
| 0185 |   | CHECK DH. TM1TBL_SUM_STAT                            |                    | =OK                  |       |
| 0186 |   | CHECK DH. TM2TBL_SUM_STAT                            |                    | =OK                  |       |
| 0187 |   | CHECK DH. ONOFKY_SUMERR                              |                    | =OK                  |       |
| 0188 |   | CHECK DH. CPU_CMD_RESET                              |                    | =NON                 |       |
| 0189 |   | CHECK DH. CUPA_ECC_2BE                               |                    | =NON                 |       |
| 0190 |   | CHECK DH. CUPA_SW_RST                                |                    | =NON                 |       |
| 0191 |   | CHECK DH. CUPA_WDT_OVF                               |                    | =NON                 |       |
| 0192 |   | CHECK DH. CPUB_ECC_2BE                               |                    | =NON                 |       |
| 0193 |   | CHECK DH. CPUB_SW_RST                                |                    | =NON                 |       |
| 0194 |   | CHECK DH. CPUB_WDT_OVF                               |                    | =NON                 |       |
| 0195 |   | CHECK DH. CPU_AB                                     |                    | =CPU_A               |       |
| 0196 |   | CHECK DH. HK_ACF_S_VALID                             |                    | =ENA                 |       |
| 0197 |   | CHECK DH. HK_DHF_S_VALID                             |                    | =ENA                 |       |
| 0198 |   | CHECK DH. HK_SPW_VALID                               |                    | =ENA                 |       |
| 0199 |   | CHECK DH. HK_TCF_S_VALID                             |                    | =ENA                 |       |
| 0200 |   | CHECK DH. NML_IPL                                    |                    | =NML                 |       |
| 0201 |   | CHECK DH. TCIU_ECC_2BE                               |                    | =NON                 |       |
| 0202 |   | CHECK DH. DR_SUM_STAT                                |                    | =OK                  |       |
| 0203 |   | CHECK PCD. APR1_ON_OFF                               |                    | =OFF                 |       |
| 0204 |   | CHECK PCD. APR2_ON_OFF                               |                    | =ON                  |       |

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0205  #
0206  . # < CHECK >
0207      CHECK DH. ENA_TEMP          -
0208      CHECK DH. TANK_TEMP          -
0209      CHECK DH. ADM_TEMP           -
0210      CHECK DH. APM_TEMP           -
0211      CHECK DH. UP_PNL2_TEMP        -
0212      CHECK DH. UP_PNL3_TEMP        -
0213      CHECK DH. UP_PNL6_TEMP        -
0214      CHECK DH. UP_PNL7_TEMP        -
0215      CHECK DH. UP_PNL8_TEMP        -
0216      CHECK DH. LO_PNL1_TEMP        -
0217      CHECK DH. LO_PNL2_TEMP        -
0218      CHECK DH. LO_PNL5_TEMP        -
0219      CHECK DH. LO_PNL6_TEMP        -
0220      CHECK DH. LO_PNL8_TEMP        -
0221      CHECK DH. SSAS_TEMP           -
0222      CHECK DH. THR_T1T2_TEMP       -
0223      CHECK DH. SSC_TEMP1          -
0224      CHECK DH. ACM1_TEMP           -
0225      CHECK DH. THR_T3T4_TEMP       -
0226      CHECK DH. ACM2_TEMP           -
0227      CHECK DH. THR_A1_TEMP         -
0228      CHECK DH. THR_A2_TEMP         -
0229      CHECK DH. VLV_M_TEMP          -
0230      CHECK DH. EPC_A_TEMP          -
0231      CHECK DH. EPC_B_TEMP          -
0232      CHECK DH. TWT_A_TEMP          -
0233      CHECK DH. TWT_B_TEMP          -
0234      CHECK DH. XTRP_A_TEMP         -
0235      CHECK DH. XTRP_B_TEMP         -
0236      CHECK DH. BAT_TEMP1           -
0237      CHECK DH. BAT_TEMP2           -
0238      CHECK DH. SSC_TEMP2           -
0239      CHECK DH. SIDE1_TEMP          -
0240      CHECK DH. SIDE2_TEMP          -
0241      CHECK DH. SIDE3_TEMP          -
0242      CHECK DH. SIDE4_TEMP          -
0243      CHECK DH. UP_PNL1_TEMP        -
0244      CHECK DH. UP_PNL5_TEMP        -
0245      CHECK DH. MAST_MGF_TEMP        -
0246      CHECK DH. LO_PNL3_TEMP        -
0247      CHECK DH. LO_PNL7_TEMP        -
0248      CHECK DH. SIDE5_TEMP          -
0249      CHECK DH. SIDE6_TEMP          -
0250      CHECK DH. SIDE7_TEMP          -
0251      CHECK DH. SIDE8_TEMP          -
0252  . #
0253      DH. MC_START
0254  . #
0255  . # < CHECK >
0256      CHECK DH. MC_ENA_DIS          =ENA

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0257 CHECK PCD.ON_OFF_ENA_DIS =DIS
0258 CHECK PCD.BUS_V 48 52
0259 . #
0260 . # 【衛星管制設定】
0261 . # プリチェック : OFF
0262 . # センドベリファイ : 強制 TYPE-B
0263 . # サクセスベリファイ: OFF
0264 . # COP N(S)値設定
0265 . #
0266 . # QL Check-Item Spec Check
0267 . # -----+-----+-----+-----
0268 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0269 . #
0270 . # -----
0271 . # 1.2.5. BAT INT
0272 . # -----
0273 . #
0274 . PCD.BAT_OT_DIS
0275 WAIT_SEC 2
0276 PCD.BAT_OT_ON_SET 0xBD # 28degC
0277 WAIT_SEC 2
0278 DH.BAT_SM_EXE
0279 WAIT_SEC 2
0280 PCD.BAT_OT_OFF_SET 0xC5 # 26degC
0281 WAIT_SEC 2
0282 DH.BAT_SM_EXE
0283 WAIT_SEC 2
0284 PCD.CEL_V_MONI_ON # 【BAT 管理手順書 (TPP-7S8238-03K) へ記録】
0285 . #
0286 . # < CHECK >
0287 CHECK PCD.CEL1_V >3.705
0288 CHECK PCD.CEL2_V >3.705
0289 CHECK PCD.CEL3_V >3.705
0290 CHECK PCD.CEL4_V >3.705
0291 CHECK PCD.CEL5_V >3.705
0292 CHECK PCD.CEL6_V >3.705
0293 CHECK PCD.CEL7_V >3.705
0294 CHECK PCD.CEL8_V >3.705
0295 CHECK PCD.CEL9_V >3.705
0296 CHECK PCD.CEL10_V >3.705
0297 CHECK PCD.CEL11_V >3.705
0298 CHECK PCD.CEL12_V >3.705
0299 CHECK PCD.BAT_INT_EXT =EXT
0300 CHECK PCD.BUS_SW_ON_OFF =OFF
0301 CHECK PCD.BAT_OT_ENA_DIS =DIS
0302 . #
0303 . # < CHECK >
0304 CHECK PCD.OT_ON_SET 27.5 28.5
0305 CHECK PCD.OT_OFF_SET 25.5 26.5
0306 . #
0307 . PCD.BAT_OT_ENA
0308 . #

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0309 . # < CHECK >
0310 . CHECK PCD. BAT_OT_ENA_DIS          =ENA
0311 . CHECK PCD. BAT_OT_ON_OFF          =OFF
0312 . #
0313 . # 【確認】                                Check
0314 . # BUS-V > BAT-V であることを確認する。      G  N
0315 . #
0316 . PCD. INT_EXT_ENA
0317 . #
0318 . # < CHECK >
0319 . CHECK PCD. INT_EXT_ENA_DIS          =ENA
0320 . #
0321 . PCD. BAT_INT
0322 . #
0323 . # < CHECK >
0324 . CHECK PCD. INT_EXT_ENA_DIS          =DIS
0325 . CHECK PCD. BAT_INT_EXT              =INT
0326 . CHECK PCD. BUS_SW_ENA_DIS          =DIS
0327 . CHECK PCD. BUS_SW_ON_OFF          =OFF
0328 . CHECK PCD. CHG_I                    -0.1 0.1
0329 . CHECK PCD. DCHG_I                    -0.2 0.2
0330 . #
0331 . #
0332 . PCD. BAT_K1_SET 0xA7          # K1 167
0333 . WAIT_SEC 2
0334 . DH. BAT_SM_EXE
0335 . WAIT_SEC 2
0336 . PCD. BAT_K2_SET 0xA5          # K2 165
0337 . WAIT_SEC 2
0338 . DH. BAT_SM_EXE
0339 . WAIT_SEC 2
0340 . PCD. BAT_CD_SET 0x80          # 10.56Ah
0341 . WAIT_SEC 2
0342 . DH. BAT_SM_EXE
0343 . #
0344 . # < CHECK >
0345 . CHECK PCD. BAT_K1                167
0346 . CHECK PCD. BAT_K2                165
0347 . CHECK PCD. BAT_CD                10.55 10.57
0348 . CHECK PCD. BAT_CV                -
0349 . #
0350 . # QL          Check-Item          Spec          Check
0351 . # -----+-----+-----+-----+
0352 . # CMD 出力装置  Message          STATUS CHECK 結果 OK      G  N
0353 . #
0354 . # -----+-----+-----+-----+
0355 . # 1.2.6. インデックスパルス出力
0356 . # -----+-----+-----+-----+
0357 . #
0358 . DH. MC_EACH_EXE 0x0032 # 1553B TLM mode 6 (SYS HK 1/2, BUS User HK 1/4, DUMP 1/4,
0359 . #:          << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0360 . #:          0x00 0x00 DH. DMP_PERIOD 0x02

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0361  #:          0x02 0x02 ACS.ACS_PKT_FMT_1
0362  #:          0x05 0x05 DH.OPE_MODE 0x0C 0x26 0x28 0x03 0x00000000000000000000000000000000
0363  #:          0x06 0x06 DH.TLMFMT1553B_CHG 0x06
0364  #
0365  WAIT_SEC 40
0366  #
0367  CHECK DH.DMP_PERIOD                      =SEC_4
0368  CHECK DH.CATEGORY_TBL_NO                0x28
0369  CHECK DH.DWNLNK_RATE                    =BPS16384
0370  CHECK DH.TLM1553BFMTNO                 0x06
0371  CHECK ACS.F_ACS_PKT_FMT                =ACSHK1
0372  #
0373  . # < CHECK >
0374  CHECK ACS.F_ACS_PKT_FMT                  =ACSHK1
0375  CHECK ACS.STS_IP_SRC                     =NULL
0376  CHECK ACS.STS_IP_OUT                     =DIS
0377  CHECK ACS.STS_IP_GNRT                    =DIS
0378  CHECK ACS.I_IPCHK                        =RST
0379  CHECK ACS.I_IP_GNRT                      =SELF
0380  CHECK ACS_SYS.I_IPCHK                    =RST
0381  CHECK ACS_SYS.I_IP_GNRT                  =SELF
0382  CHECK ACS.BIT_I_IPCHK                    =RST
0383  #
0384  . # SSAS を擬似モードにする
0385  . ACS.SSAS_SEL_IMT
0386  #
0387  # 疑似サンパルスを発生
0388  . # (SIN:2.5V, COS:5.0V, START:0x80, COARSE:0x10, Period:4.138sec)
0389  . ACS.DUM_SP_ON 2047 4095 128 16 8475
0390  #
0391  . # < CHECK >
0392  CHECK ACS.SSAS_ST_PT                     =IMT
0393  CHECK ACS.SSAS_ST                        =IMT
0394  CHECK ACS.F_SPN_ERR_SEL                  =OFF
0395  CHECK ACS.P_SPIN_SSAS_SEL                4.137 4.139
0396  CHECK ACS.THT_CRS_SEL                    0x10
0397  CHECK ACS.SUN_ANG_COS                    4.999 5.001
0398  CHECK ACS.SUN_ANG_SIN                    2.499 2.501
0399  CHECK ACS.SUN_ANG_SEL                    -2 2
0400  CHECK ACS_SYS.SSAS_ST                    =IMT
0401  CHECK ACS_SYS.P_SPIN_SSAS_SEL            4.137 4.139
0402  CHECK ACS_SYS.THT_CRS_SEL                0x10
0403  CHECK ACS_SYS.SUN_ANG_SEL                -2 2
0404  CHECK ACS.BIT_SSAS_ST                    =IMT
0405  CHECK ACS.BIT_P_SPIN_SSAS_SEL            4.013 4.263
0406  CHECK ACS.BIT_SUN_ANG_SEL                -0.125 0.125
0407  #
0408  . # ACS HK パケットフォーマットを ACS_HK4 に設定
0409  . ACS.ACS_PKT_FMT_4
0410  #
0411  . # < CHECK >
0412  CHECK ACS.SSAS_IMT_SP_STS                =ON

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0413 CHECK ACS.E_SSAS_SPIN 0.0999 0.1001
0414 CHECK ACS.N_SPINRATE_AVE 5 5
0415 CHECK ACS_SYS.F_ECL =OFF
0416 CHECK ACS_SYS.I_ATT =STBY
0417 CHECK ACS_SYS.P_B_AVE 4.137 4.139
0418 #
0419 . # IP フィードバックゲインを設定する。
0420 . ACS.IP_K_DT 0x3C9BA5E3 0x3C9BA5E3
0421 #
0422 . # < CHECK >
0423 CHECK ACS.K_DT0 0.018999 0.019001
0424 CHECK ACS.K_DT1 0.018999 0.019001
0425 #
0426 . # IP 源泉を IP にする
0427 . ACS.IP_SRC_IP
0428 . # IP 生成機能を ON にする
0429 WAIT_SEC 2
0430 ACS.IP_GNRT_ON
0431 . # MDP への IP 出力を開始する
0432 WAIT_SEC 2
0433 ACS.IP_OUT_ENA
0434 . # ACS HK パケットフォーマットを ACS_HK1 に設定
0435 WAIT_SEC 2
0436 ACS.ACS_PKT_FMT_1
0437 #
0438 . # < CHECK >
0439 CHECK ACS.STS_IP_SRC =IP
0440 CHECK ACS.STS_IP_OUT =ENA
0441 CHECK ACS.STS_IP_GNRT =ENA
0442 CHECK ACS.I_IP_GNRT =SSAS
0443 CHECK ACS_SYS.I_IP_GNRT =SSAS
0444 #
0445 . # << DR STATUS CHECK >>
0446 #
0447 . # < CHECK >
0448 CHECK DH.DR_STATE =STANDBY
0449 CHECK DH.DR_CHK_ON_OFF =OFF
0450 . #
0451 . DH.DR_ERRCLR
0452 . # QL Check-Item Spec Check
0453 # -----+-----+-----+-----
0454 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0455 #
0456 # -----
0457 . # 1.2.7. TCFS 設定
0458 # -----
0459 . #
0460 . PCD.ON_OFF_ENA
0461 . #
0462 . # < CHECK >
0463 CHECK PCD.ON_OFF_ENA_DIS =ENA
0464 . #

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0465 . PCD.DRU_PSU_A_ON
0466 . #
0467 . # < CHECK >
0468 CHECK PCD.DRU_PSU_A_ON_OFF =ON
0469 . #
0470 . PCD.ON_OFF_DIS
0471 . #
0472 . # < CHECK >
0473 CHECK PCD.ON_OFF_ENA_DIS =DIS
0474 . #
0475 . ACS.DRU_SEL_A
0476 . #
0477 . # < CHECK >
0478 CHECK ACS.DRU_SEL_PT =A
0479 CHECK ACS_SYS.DRU_SEL_PT =A
0480 CHECK ACS.BIT_DRU_SEL_PT =A
0481 . #
0482 . ACS.ACS_IOPERR_CLR
0483 . #
0484 . # < CHECK >
0485 CHECK ACS.DRU_A_ONOFF_ST =ON
0486 CHECK ACS.DRU_B_ONOFF_ST =OFF
0487 CHECK ACS.DRU_ACIMIF_A_ST =ON
0488 CHECK ACS.DRU_ACIMIF_B_ST =OFF
0489 CHECK ACS.SRAM1_MEMST =MEMO
0490 CHECK ACS.SRAM2_MEMST =MEMO
0491 CHECK ACS.C_DRU_CMD_ECC2 0
0492 CHECK ACS.C_DRU_TLM_ECC2 0
0493 CHECK ACS.C_LBUSU_ECC1 0
0494 CHECK ACS.C_LBUSL_ECC1 0
0495 CHECK ACS.C_DRU_CMD_Prt 0
0496 CHECK ACS.C_DRU_TLM_Prt 0
0497 CHECK ACS.DATA_ERR_STATUS =OK
0498 CHECK ACS.STS_DRUAPM_ON =OFF
0499 CHECK ACS.I_ADMCE_A_PWR =OFF
0500 CHECK ACS.I_ADMCE_B_PWR =OFF
0501 CHECK ACS.ADM_SEL_PORT1 =A
0502 CHECK ACS.SRAM_ST =SRAM1
0503 CHECK ACS_SYS.DRU_A_ONOFF_ST =ON
0504 CHECK ACS_SYS.DRU_B_ONOFF_ST =OFF
0505 CHECK ACS_SYS.DRU_ACIMIF_A_ST =ON
0506 CHECK ACS_SYS.DRU_ACIMIF_B_ST =OFF
0507 CHECK ACS_SYS.SRAM1_MEMST =MEMO
0508 CHECK ACS_SYS.SRAM2_MEMST =MEMO
0509 CHECK ACS_SYS.C_DRU_CMD_ECC2 0
0510 CHECK ACS_SYS.C_DRU_TLM_ECC2 0
0511 CHECK ACS_SYS.C_LBUSU_ECC1 0
0512 CHECK ACS_SYS.C_LBUSL_ECC1 0
0513 CHECK ACS_SYS.C_DRU_CMD_Prt 0
0514 CHECK ACS_SYS.C_DRU_TLM_Prt 0
0515 CHECK ACS_SYS.DATA_ERR_STATUS =OK
0516 CHECK ACS_SYS.STS_DRUAPM_ON =OFF

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0517 CHECK ACS_SYS. I_ADMCE_A_PWR =OFF
0518 CHECK ACS_SYS. I_ADMCE_B_PWR =OFF
0519 CHECK ACS_SYS. ADM_SEL_PORT1 =A
0520 CHECK ACS_SYS. SRAM_ST =SRAM1
0521 CHECK ACS. BIT_DRU_A_ONOFF_ST =ON
0522 CHECK ACS. BIT_DRU_B_ONOFF_ST =OFF
0523 CHECK ACS. BIT_I_ADMCE_A_PWR =OFF
0524 CHECK ACS. BIT_I_ADMCE_B_PWR =OFF
0525 . #
0526 . ACS. ACS_PKT_FMT_4
0527 . #
0528 . # < CHECK >
0529 CHECK ACS. F_ACS_PKT_FMT =ACSHK4
0530 CHECK ACS. I_ECCERR_S_D 0
0531 CHECK ACS. I_ECCERR_S_L 0
0532 CHECK ACS. I_DRU_ACIMIF1 0
0533 CHECK ACS. I_DRU_ACIMIF2 0
0534 CHECK ACS. I_DRU_ACIMIF3 0
0535 CHECK ACS. I_DRU_ACIMIF4 0
0536 CHECK ACS. DRU_CMD_ECC2 0
0537 CHECK ACS. DRU_TLM_ECC2 0
0538 CHECK ACS. DRU_CMD_Parity1 0
0539 CHECK ACS. DRU_CMD_Parity2 0
0540 CHECK ACS. DRU_TLM_Parity1 0
0541 CHECK ACS. DRU_TLM_Parity2 0
0542 CHECK ACS. I_CMD_FLAG1 0x02ec0c00
0543 CHECK ACS. I_CMD_FLAG2 0
0544 . #
0545 . ACS. ACS_PKT_FMT_1
0546 . #
0547 . # < CHECK >
0548 CHECK ACS. F_ACS_PKT_FMT =ACSHK1
0549 . #
0550 . TCS. SET_TBL_ENA
0551 . #
0552 . # < CHECK >
0553 CHECK TCS. SET_TBL_ST =ENA
0554 . #
0555 . # < BKUP DTY TBL1 >
0556 . TCS. SET_BKUP_DTY_TBL 0x00 0x00 0x0020 # BKUP DTY TBL1 CH01: 32
0557 WAIT_SEC 2
0558 TCS. SET_BKUP_DTY_TBL 0x00 0x01 0x0020 # BKUP DTY TBL1 CH02: 32
0559 WAIT_SEC 2
0560 TCS. SET_BKUP_DTY_TBL 0x00 0x02 0x0020 # BKUP DTY TBL1 CH03: 32
0561 WAIT_SEC 2
0562 TCS. SET_BKUP_DTY_TBL 0x00 0x03 0x0020 # BKUP DTY TBL1 CH04: 32
0563 WAIT_SEC 2
0564 TCS. SET_BKUP_DTY_TBL 0x00 0x04 0x0020 # BKUP DTY TBL1 CH05: 32
0565 WAIT_SEC 2
0566 TCS. SET_BKUP_DTY_TBL 0x00 0x05 0x0020 # BKUP DTY TBL1 CH06: 32
0567 WAIT_SEC 2
0568 TCS. SET_BKUP_DTY_TBL 0x00 0x06 0x0020 # BKUP DTY TBL1 CH07: 32

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0569 WAIT_SEC 2
0570 TCS.SET_BKUP_DTY_TBL 0x00 0x07 0x0020 # BKUP DTY TBL1 CH08: 32
0571 WAIT_SEC 2
0572 TCS.SET_BKUP_DTY_TBL 0x00 0x08 0x0020 # BKUP DTY TBL1 CH09: 32
0573 WAIT_SEC 2
0574 TCS.SET_BKUP_DTY_TBL 0x00 0x09 0x0020 # BKUP DTY TBL1 CH10: 32
0575 WAIT_SEC 2
0576 TCS.SET_BKUP_DTY_TBL 0x00 0x0A 0x0020 # BKUP DTY TBL1 CH11: 32
0577 WAIT_SEC 2
0578 TCS.SET_BKUP_DTY_TBL 0x00 0x0B 0x0020 # BKUP DTY TBL1 CH12: 32
0579 WAIT_SEC 2
0580 TCS.SET_BKUP_DTY_TBL 0x00 0x0C 0x0020 # BKUP DTY TBL1 CH13: 32
0581 WAIT_SEC 2
0582 TCS.SET_BKUP_DTY_TBL 0x00 0x0D 0x0000 # BKUP DTY TBL1 CH14: 0
0583 WAIT_SEC 2
0584 TCS.SET_BKUP_DTY_TBL 0x00 0x0E 0x0000 # BKUP DTY TBL1 CH15: 0
0585 WAIT_SEC 2
0586 TCS.SET_BKUP_DTY_TBL 0x00 0x0F 0x0000 # BKUP DTY TBL1 CH16: 0
0587 WAIT_SEC 2
0588 TCS.SET_BKUP_DTY_TBL 0x00 0x10 0x0020 # BKUP DTY TBL1 CH17: 32
0589 WAIT_SEC 2
0590 TCS.SET_BKUP_DTY_TBL 0x00 0x11 0x0020 # BKUP DTY TBL1 CH18: 32
0591 WAIT_SEC 2
0592 TCS.SET_BKUP_DTY_TBL 0x00 0x12 0x0020 # BKUP DTY TBL1 CH19: 32
0593 WAIT_SEC 2
0594 TCS.SET_BKUP_DTY_TBL 0x00 0x13 0x0020 # BKUP DTY TBL1 CH20: 32
0595 WAIT_SEC 2
0596 TCS.SET_BKUP_DTY_TBL 0x00 0x14 0x0020 # BKUP DTY TBL1 CH21: 32
0597 WAIT_SEC 2
0598 TCS.SET_BKUP_DTY_TBL 0x00 0x15 0x0020 # BKUP DTY TBL1 CH22: 32
0599 WAIT_SEC 2
0600 TCS.SET_BKUP_DTY_TBL 0x00 0x16 0x0020 # BKUP DTY TBL1 CH23: 32
0601 WAIT_SEC 2
0602 TCS.SET_BKUP_DTY_TBL 0x00 0x17 0x0020 # BKUP DTY TBL1 CH24: 32
0603 WAIT_SEC 2
0604 TCS.SET_BKUP_DTY_TBL 0x00 0x18 0x0020 # BKUP DTY TBL1 CH25: 32
0605 WAIT_SEC 2
0606 TCS.SET_BKUP_DTY_TBL 0x00 0x19 0x0020 # BKUP DTY TBL1 CH26: 32
0607 WAIT_SEC 2
0608 TCS.SET_BKUP_DTY_TBL 0x00 0x1A 0x0020 # BKUP DTY TBL1 CH27: 32
0609 WAIT_SEC 2
0610 TCS.SET_BKUP_DTY_TBL 0x00 0x1B 0x0020 # BKUP DTY TBL1 CH28: 32
0611 WAIT_SEC 2
0612 TCS.SET_BKUP_DTY_TBL 0x00 0x1C 0x0020 # BKUP DTY TBL1 CH29: 32
0613 WAIT_SEC 2
0614 TCS.SET_BKUP_DTY_TBL 0x00 0x1D 0x0000 # BKUP DTY TBL1 CH30: 0
0615 WAIT_SEC 2
0616 TCS.SET_BKUP_DTY_TBL 0x00 0x1E 0x0000 # BKUP DTY TBL1 CH31: 0
0617 WAIT_SEC 2
0618 TCS.SET_BKUP_DTY_TBL 0x00 0x1F 0x0000 # BKUP DTY TBL1 CH32: 0
0619 #
0620 # < NMNL DTY TBL1 >

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|      |                                       |                          |
|------|---------------------------------------|--------------------------|
| 0621 | TCS.SET_NMNL_DTY_TBL 0x00 0x00 0x0040 | # NMNL DTY TBL1 CH01: 64 |
| 0622 | WAIT_SEC 2                            |                          |
| 0623 | TCS.SET_NMNL_DTY_TBL 0x00 0x01 0x0040 | # NMNL DTY TBL1 CH02: 64 |
| 0624 | WAIT_SEC 2                            |                          |
| 0625 | TCS.SET_NMNL_DTY_TBL 0x00 0x02 0x0040 | # NMNL DTY TBL1 CH03: 64 |
| 0626 | WAIT_SEC 2                            |                          |
| 0627 | TCS.SET_NMNL_DTY_TBL 0x00 0x03 0x0040 | # NMNL DTY TBL1 CH04: 64 |
| 0628 | WAIT_SEC 2                            |                          |
| 0629 | TCS.SET_NMNL_DTY_TBL 0x00 0x04 0x0040 | # NMNL DTY TBL1 CH05: 64 |
| 0630 | WAIT_SEC 2                            |                          |
| 0631 | TCS.SET_NMNL_DTY_TBL 0x00 0x05 0x0040 | # NMNL DTY TBL1 CH06: 64 |
| 0632 | WAIT_SEC 2                            |                          |
| 0633 | TCS.SET_NMNL_DTY_TBL 0x00 0x06 0x0040 | # NMNL DTY TBL1 CH07: 64 |
| 0634 | WAIT_SEC 2                            |                          |
| 0635 | TCS.SET_NMNL_DTY_TBL 0x00 0x07 0x0040 | # NMNL DTY TBL1 CH08: 64 |
| 0636 | WAIT_SEC 2                            |                          |
| 0637 | TCS.SET_NMNL_DTY_TBL 0x00 0x08 0x0040 | # NMNL DTY TBL1 CH09: 64 |
| 0638 | WAIT_SEC 2                            |                          |
| 0639 | TCS.SET_NMNL_DTY_TBL 0x00 0x09 0x0040 | # NMNL DTY TBL1 CH10: 64 |
| 0640 | WAIT_SEC 2                            |                          |
| 0641 | TCS.SET_NMNL_DTY_TBL 0x00 0x0A 0x0040 | # NMNL DTY TBL1 CH11: 64 |
| 0642 | WAIT_SEC 2                            |                          |
| 0643 | TCS.SET_NMNL_DTY_TBL 0x00 0x0B 0x0040 | # NMNL DTY TBL1 CH12: 64 |
| 0644 | WAIT_SEC 2                            |                          |
| 0645 | TCS.SET_NMNL_DTY_TBL 0x00 0x0C 0x0040 | # NMNL DTY TBL1 CH13: 64 |
| 0646 | WAIT_SEC 2                            |                          |
| 0647 | TCS.SET_NMNL_DTY_TBL 0x00 0x0D 0x0000 | # NMNL DTY TBL1 CH14: 0  |
| 0648 | WAIT_SEC 2                            |                          |
| 0649 | TCS.SET_NMNL_DTY_TBL 0x00 0x0E 0x0000 | # NMNL DTY TBL1 CH15: 0  |
| 0650 | WAIT_SEC 2                            |                          |
| 0651 | TCS.SET_NMNL_DTY_TBL 0x00 0x0F 0x0000 | # NMNL DTY TBL1 CH16: 0  |
| 0652 | WAIT_SEC 2                            |                          |
| 0653 | TCS.SET_NMNL_DTY_TBL 0x00 0x10 0x0040 | # NMNL DTY TBL1 CH17: 64 |
| 0654 | WAIT_SEC 2                            |                          |
| 0655 | TCS.SET_NMNL_DTY_TBL 0x00 0x11 0x0040 | # NMNL DTY TBL1 CH18: 64 |
| 0656 | WAIT_SEC 2                            |                          |
| 0657 | TCS.SET_NMNL_DTY_TBL 0x00 0x12 0x0040 | # NMNL DTY TBL1 CH19: 64 |
| 0658 | WAIT_SEC 2                            |                          |
| 0659 | TCS.SET_NMNL_DTY_TBL 0x00 0x13 0x0040 | # NMNL DTY TBL1 CH20: 64 |
| 0660 | WAIT_SEC 2                            |                          |
| 0661 | TCS.SET_NMNL_DTY_TBL 0x00 0x14 0x0040 | # NMNL DTY TBL1 CH21: 64 |
| 0662 | WAIT_SEC 2                            |                          |
| 0663 | TCS.SET_NMNL_DTY_TBL 0x00 0x15 0x0040 | # NMNL DTY TBL1 CH22: 64 |
| 0664 | WAIT_SEC 2                            |                          |
| 0665 | TCS.SET_NMNL_DTY_TBL 0x00 0x16 0x0040 | # NMNL DTY TBL1 CH23: 64 |
| 0666 | WAIT_SEC 2                            |                          |
| 0667 | TCS.SET_NMNL_DTY_TBL 0x00 0x17 0x0040 | # NMNL DTY TBL1 CH24: 64 |
| 0668 | WAIT_SEC 2                            |                          |
| 0669 | TCS.SET_NMNL_DTY_TBL 0x00 0x18 0x0040 | # NMNL DTY TBL1 CH25: 64 |
| 0670 | WAIT_SEC 2                            |                          |
| 0671 | TCS.SET_NMNL_DTY_TBL 0x00 0x19 0x0040 | # NMNL DTY TBL1 CH26: 64 |
| 0672 | WAIT_SEC 2                            |                          |

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0673 TCS.SET_NMNL_DTY_TBL 0x00 0x1A 0x0040 # NMNL DTY TBL1 CH27: 64
0674 WAIT_SEC 2
0675 TCS.SET_NMNL_DTY_TBL 0x00 0x1B 0x0040 # NMNL DTY TBL1 CH28: 64
0676 WAIT_SEC 2
0677 TCS.SET_NMNL_DTY_TBL 0x00 0x1C 0x0040 # NMNL DTY TBL1 CH29: 64
0678 WAIT_SEC 2
0679 TCS.SET_NMNL_DTY_TBL 0x00 0x1D 0x0000 # NMNL DTY TBL1 CH30: 0
0680 WAIT_SEC 2
0681 TCS.SET_NMNL_DTY_TBL 0x00 0x1E 0x0000 # NMNL DTY TBL1 CH31: 0
0682 WAIT_SEC 2
0683 TCS.SET_NMNL_DTY_TBL 0x00 0x1F 0x0000 # NMNL DTY TBL1 CH32: 0
0684 #
0685 # < NMNL TBL1 ヒータ ON/OFF スレッシュホールド設定 >
0686 TCS.SET_NMNL_TMP_TBL 0x00 0x00 0x9E 0xA2 # NMNL TBL1 CH01 ON: 27.94 OFF: 30
0687 WAIT_SEC 2
0688 TCS.SET_NMNL_TMP_TBL 0x00 0x01 0x9C 0xA0 # NMNL TBL1 CH02 ON: 27.94 OFF: 30
0689 WAIT_SEC 2
0690 TCS.SET_NMNL_TMP_TBL 0x00 0x02 0x9B 0x9F # NMNL TBL1 CH03 ON: 27.94 OFF: 30
0691 WAIT_SEC 2
0692 TCS.SET_NMNL_TMP_TBL 0x00 0x03 0x98 0x9C # NMNL TBL1 CH04 ON: 27.94 OFF: 30
0693 WAIT_SEC 2
0694 TCS.SET_NMNL_TMP_TBL 0x00 0x04 0x9C 0xA0 # NMNL TBL1 CH05 ON: 27.94 OFF: 30
0695 WAIT_SEC 2
0696 TCS.SET_NMNL_TMP_TBL 0x00 0x05 0x9B 0x9F # NMNL TBL1 CH06 ON: 27.94 OFF: 30
0697 WAIT_SEC 2
0698 TCS.SET_NMNL_TMP_TBL 0x00 0x06 0x9C 0xA0 # NMNL TBL1 CH07 ON: 27.94 OFF: 30
0699 WAIT_SEC 2
0700 TCS.SET_NMNL_TMP_TBL 0x00 0x07 0x83 0x84 # NMNL TBL1 CH08 ON: 28.46 OFF: 29.49
0701 WAIT_SEC 2
0702 TCS.SET_NMNL_TMP_TBL 0x00 0x08 0x9D 0xA1 # NMNL TBL1 CH09 ON: 27.94 OFF: 30
0703 WAIT_SEC 2
0704 TCS.SET_NMNL_TMP_TBL 0x00 0x09 0x9C 0xA0 # NMNL TBL1 CH10 ON: 27.94 OFF: 30
0705 WAIT_SEC 2
0706 TCS.SET_NMNL_TMP_TBL 0x00 0x0A 0x9A 0x9E # NMNL TBL1 CH11 ON: 27.75 OFF: 29.8
0707 WAIT_SEC 2
0708 TCS.SET_NMNL_TMP_TBL 0x00 0x0B 0x9A 0x9E # NMNL TBL1 CH12 ON: 27.94 OFF: 30
0709 WAIT_SEC 2
0710 TCS.SET_NMNL_TMP_TBL 0x00 0x0C 0x9B 0x9F # NMNL TBL1 CH13 ON: 27.94 OFF: 30
0711 WAIT_SEC 2
0712 TCS.SET_NMNL_TMP_TBL 0x00 0x0D 0x00 0x00 # NMNL TBL1 CH14 ON: -50.41 OFF: -50.41
0713 WAIT_SEC 2
0714 TCS.SET_NMNL_TMP_TBL 0x00 0x0E 0x00 0x00 # NMNL TBL1 CH15 ON: -50.41 OFF: -50.41
0715 WAIT_SEC 2
0716 TCS.SET_NMNL_TMP_TBL 0x00 0x0F 0x00 0x00 # NMNL TBL1 CH16 ON: -50.41 OFF: -50.41
0717 WAIT_SEC 2
0718 TCS.SET_NMNL_TMP_TBL 0x00 0x10 0x9E 0xA2 # NMNL TBL1 CH17 ON: 27.94 OFF: 30
0719 WAIT_SEC 2
0720 TCS.SET_NMNL_TMP_TBL 0x00 0x11 0x9D 0xA1 # NMNL TBL1 CH18 ON: 27.94 OFF: 30
0721 WAIT_SEC 2
0722 TCS.SET_NMNL_TMP_TBL 0x00 0x12 0x9C 0xA0 # NMNL TBL1 CH19 ON: 27.94 OFF: 30
0723 WAIT_SEC 2
0724 TCS.SET_NMNL_TMP_TBL 0x00 0x13 0x9C 0xA0 # NMNL TBL1 CH20 ON: 27.94 OFF: 30

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0725 WAIT_SEC 2
0726 TCS.SET_NMNL_TMP_TBL 0x00 0x14 0x9D 0xA1 # NMNL TBL1 CH21 ON: 27.94 OFF: 30
0727 WAIT_SEC 2
0728 TCS.SET_NMNL_TMP_TBL 0x00 0x15 0x9B 0x9F # NMNL TBL1 CH22 ON: 27.94 OFF: 30
0729 WAIT_SEC 2
0730 TCS.SET_NMNL_TMP_TBL 0x00 0x16 0x9A 0x9E # NMNL TBL1 CH23 ON: 27.94 OFF: 30
0731 WAIT_SEC 2
0732 TCS.SET_NMNL_TMP_TBL 0x00 0x17 0x9F 0xA3 # NMNL TBL1 CH24 ON: 27.94 OFF: 30
0733 WAIT_SEC 2
0734 TCS.SET_NMNL_TMP_TBL 0x00 0x18 0x9A 0x9E # NMNL TBL1 CH25 ON: 27.94 OFF: 30
0735 WAIT_SEC 2
0736 TCS.SET_NMNL_TMP_TBL 0x00 0x19 0x9C 0xA0 # NMNL TBL1 CH26 ON: 27.94 OFF: 30
0737 WAIT_SEC 2
0738 TCS.SET_NMNL_TMP_TBL 0x00 0x1A 0x9B 0x9F # NMNL TBL1 CH27 ON: 27.94 OFF: 30
0739 WAIT_SEC 2
0740 TCS.SET_NMNL_TMP_TBL 0x00 0x1B 0x99 0x9D # NMNL TBL1 CH28 ON: 27.95 OFF: 30
0741 WAIT_SEC 2
0742 TCS.SET_NMNL_TMP_TBL 0x00 0x1C 0x9B 0x9F # NMNL TBL1 CH29 ON: 27.94 OFF: 30
0743 WAIT_SEC 2
0744 TCS.SET_NMNL_TMP_TBL 0x00 0x1D 0x00 0x00 # NMNL TBL1 CH30 ON: -50.41 OFF: -50.41
0745 WAIT_SEC 2
0746 TCS.SET_NMNL_TMP_TBL 0x00 0x1E 0x00 0x00 # NMNL TBL1 CH31 ON: -50.41 OFF: -50.41
0747 WAIT_SEC 2
0748 TCS.SET_NMNL_TMP_TBL 0x00 0x1F 0x00 0x00 # NMNL TBL1 CH32 ON: -50.41 OFF: -50.41
0749 #
0750 # <ヒータ ENA/DIS テーブル設定>
0751 TCS.SET_HTR_ENA 0x05 0x00 # ヒータ CH06: BAT1-1 DIS
0752 WAIT_SEC 2
0753 TCS.SET_HTR_ENA 0x0C 0x00 # ヒータ CH13: BAT2-1 DIS
0754 WAIT_SEC 2
0755 TCS.SET_HTR_ENA 0x15 0x00 # ヒータ CH22: BAT1-2 DIS
0756 WAIT_SEC 2
0757 TCS.SET_HTR_ENA 0x1C 0x00 # ヒータ CH29: BAT2-2 DIS
0758 #
0759 TCS.ONESHOT_TLM_0x11
0760 TCS.ONESHOT_TLM_0x19
0761 TCS.ONESHOT_TLM_0x1F
0762 TCS.ONESHOT_TLM_0x25
0763 #
0764 # < CHECK >
0765 CHECK TCS.BK_DTY_TBL1_01CH 32
0766 CHECK TCS.BK_DTY_TBL1_02CH 32
0767 CHECK TCS.BK_DTY_TBL1_03CH 32
0768 CHECK TCS.BK_DTY_TBL1_04CH 32
0769 CHECK TCS.BK_DTY_TBL1_05CH 32
0770 CHECK TCS.BK_DTY_TBL1_06CH 32
0771 CHECK TCS.BK_DTY_TBL1_07CH 32
0772 CHECK TCS.BK_DTY_TBL1_08CH 32
0773 CHECK TCS.BK_DTY_TBL1_09CH 32
0774 CHECK TCS.BK_DTY_TBL1_10CH 32
0775 CHECK TCS.BK_DTY_TBL1_11CH 32
0776 CHECK TCS.BK_DTY_TBL1_12CH 32

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|------|-----------------------------|----|
| 0777 | CHECK TCS. BK_DTY_TBL1_13CH | 32 |
| 0778 | CHECK TCS. BK_DTY_TBL1_14CH | 0  |
| 0779 | CHECK TCS. BK_DTY_TBL1_15CH | 0  |
| 0780 | CHECK TCS. BK_DTY_TBL1_16CH | 0  |
| 0781 | CHECK TCS. BK_DTY_TBL1_17CH | 32 |
| 0782 | CHECK TCS. BK_DTY_TBL1_18CH | 32 |
| 0783 | CHECK TCS. BK_DTY_TBL1_19CH | 32 |
| 0784 | CHECK TCS. BK_DTY_TBL1_20CH | 32 |
| 0785 | CHECK TCS. BK_DTY_TBL1_21CH | 32 |
| 0786 | CHECK TCS. BK_DTY_TBL1_22CH | 32 |
| 0787 | CHECK TCS. BK_DTY_TBL1_23CH | 32 |
| 0788 | CHECK TCS. BK_DTY_TBL1_24CH | 32 |
| 0789 | CHECK TCS. BK_DTY_TBL1_25CH | 32 |
| 0790 | CHECK TCS. BK_DTY_TBL1_26CH | 32 |
| 0791 | CHECK TCS. BK_DTY_TBL1_27CH | 32 |
| 0792 | CHECK TCS. BK_DTY_TBL1_28CH | 32 |
| 0793 | CHECK TCS. BK_DTY_TBL1_29CH | 32 |
| 0794 | CHECK TCS. BK_DTY_TBL1_30CH | 0  |
| 0795 | CHECK TCS. BK_DTY_TBL1_31CH | 0  |
| 0796 | CHECK TCS. BK_DTY_TBL1_32CH | 0  |
| 0797 | CHECK TCS. NM_DTY_TBL1_01CH | 64 |
| 0798 | CHECK TCS. NM_DTY_TBL1_02CH | 64 |
| 0799 | CHECK TCS. NM_DTY_TBL1_03CH | 64 |
| 0800 | CHECK TCS. NM_DTY_TBL1_04CH | 64 |
| 0801 | CHECK TCS. NM_DTY_TBL1_05CH | 64 |
| 0802 | CHECK TCS. NM_DTY_TBL1_06CH | 64 |
| 0803 | CHECK TCS. NM_DTY_TBL1_07CH | 64 |
| 0804 | CHECK TCS. NM_DTY_TBL1_08CH | 64 |
| 0805 | CHECK TCS. NM_DTY_TBL1_09CH | 64 |
| 0806 | CHECK TCS. NM_DTY_TBL1_10CH | 64 |
| 0807 | CHECK TCS. NM_DTY_TBL1_11CH | 64 |
| 0808 | CHECK TCS. NM_DTY_TBL1_12CH | 64 |
| 0809 | CHECK TCS. NM_DTY_TBL1_13CH | 64 |
| 0810 | CHECK TCS. NM_DTY_TBL1_14CH | 0  |
| 0811 | CHECK TCS. NM_DTY_TBL1_15CH | 0  |
| 0812 | CHECK TCS. NM_DTY_TBL1_16CH | 0  |
| 0813 | CHECK TCS. NM_DTY_TBL1_17CH | 64 |
| 0814 | CHECK TCS. NM_DTY_TBL1_18CH | 64 |
| 0815 | CHECK TCS. NM_DTY_TBL1_19CH | 64 |
| 0816 | CHECK TCS. NM_DTY_TBL1_20CH | 64 |
| 0817 | CHECK TCS. NM_DTY_TBL1_21CH | 64 |
| 0818 | CHECK TCS. NM_DTY_TBL1_22CH | 64 |
| 0819 | CHECK TCS. NM_DTY_TBL1_23CH | 64 |
| 0820 | CHECK TCS. NM_DTY_TBL1_24CH | 64 |
| 0821 | CHECK TCS. NM_DTY_TBL1_25CH | 64 |
| 0822 | CHECK TCS. NM_DTY_TBL1_26CH | 64 |
| 0823 | CHECK TCS. NM_DTY_TBL1_27CH | 64 |
| 0824 | CHECK TCS. NM_DTY_TBL1_28CH | 64 |
| 0825 | CHECK TCS. NM_DTY_TBL1_29CH | 64 |
| 0826 | CHECK TCS. NM_DTY_TBL1_30CH | 0  |
| 0827 | CHECK TCS. NM_DTY_TBL1_31CH | 0  |
| 0828 | CHECK TCS. NM_DTY_TBL1_32CH | 0  |

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|------|-----------------------------|-------|-------|
| 0829 | CHECK TCS. NM_OFF_TBL1_01CH | 29.5  | 30.5  |
| 0830 | CHECK TCS. NM_OFF_TBL1_02CH | 29.5  | 30.5  |
| 0831 | CHECK TCS. NM_OFF_TBL1_03CH | 29.5  | 30.5  |
| 0832 | CHECK TCS. NM_OFF_TBL1_04CH | 29.5  | 30.5  |
| 0833 | CHECK TCS. NM_OFF_TBL1_05CH | 29.5  | 30.5  |
| 0834 | CHECK TCS. NM_OFF_TBL1_06CH | 29.5  | 30.5  |
| 0835 | CHECK TCS. NM_OFF_TBL1_07CH | 29.5  | 30.5  |
| 0836 | CHECK TCS. NM_OFF_TBL1_08CH | 28.99 | 29.99 |
| 0837 | CHECK TCS. NM_OFF_TBL1_09CH | 29.5  | 30.5  |
| 0838 | CHECK TCS. NM_OFF_TBL1_10CH | 29.5  | 30.5  |
| 0839 | CHECK TCS. NM_OFF_TBL1_11CH | 29.3  | 30.3  |
| 0840 | CHECK TCS. NM_OFF_TBL1_12CH | 29.5  | 30.5  |
| 0841 | CHECK TCS. NM_OFF_TBL1_13CH | 29.5  | 30.5  |
| 0842 | CHECK TCS. NM_OFF_TBL1_17CH | 29.5  | 30.5  |
| 0843 | CHECK TCS. NM_OFF_TBL1_18CH | 29.5  | 30.5  |
| 0844 | CHECK TCS. NM_OFF_TBL1_19CH | 29.5  | 30.5  |
| 0845 | CHECK TCS. NM_OFF_TBL1_20CH | 29.5  | 30.5  |
| 0846 | CHECK TCS. NM_OFF_TBL1_21CH | 29.5  | 30.5  |
| 0847 | CHECK TCS. NM_OFF_TBL1_22CH | 29.5  | 30.5  |
| 0848 | CHECK TCS. NM_OFF_TBL1_23CH | 29.5  | 30.5  |
| 0849 | CHECK TCS. NM_OFF_TBL1_24CH | 29.5  | 30.5  |
| 0850 | CHECK TCS. NM_OFF_TBL1_25CH | 29.5  | 30.5  |
| 0851 | CHECK TCS. NM_OFF_TBL1_26CH | 29.5  | 30.5  |
| 0852 | CHECK TCS. NM_OFF_TBL1_27CH | 29.5  | 30.5  |
| 0853 | CHECK TCS. NM_OFF_TBL1_28CH | 29.5  | 30.5  |
| 0854 | CHECK TCS. NM_OFF_TBL1_29CH | 29.5  | 30.5  |
| 0855 | CHECK TCS. NM_ON_TBL1_01CH  | 27.44 | 28.44 |
| 0856 | CHECK TCS. NM_ON_TBL1_02CH  | 27.44 | 28.44 |
| 0857 | CHECK TCS. NM_ON_TBL1_03CH  | 27.44 | 28.44 |
| 0858 | CHECK TCS. NM_ON_TBL1_04CH  | 27.44 | 28.44 |
| 0859 | CHECK TCS. NM_ON_TBL1_05CH  | 27.44 | 28.44 |
| 0860 | CHECK TCS. NM_ON_TBL1_06CH  | 27.44 | 28.44 |
| 0861 | CHECK TCS. NM_ON_TBL1_07CH  | 27.44 | 28.44 |
| 0862 | CHECK TCS. NM_ON_TBL1_08CH  | 27.96 | 28.96 |
| 0863 | CHECK TCS. NM_ON_TBL1_09CH  | 27.44 | 28.44 |
| 0864 | CHECK TCS. NM_ON_TBL1_10CH  | 27.44 | 28.44 |
| 0865 | CHECK TCS. NM_ON_TBL1_11CH  | 27.25 | 28.25 |
| 0866 | CHECK TCS. NM_ON_TBL1_12CH  | 27.44 | 28.44 |
| 0867 | CHECK TCS. NM_ON_TBL1_13CH  | 27.44 | 28.44 |
| 0868 | CHECK TCS. NM_ON_TBL1_17CH  | 27.44 | 28.44 |
| 0869 | CHECK TCS. NM_ON_TBL1_18CH  | 27.44 | 28.44 |
| 0870 | CHECK TCS. NM_ON_TBL1_19CH  | 27.44 | 28.44 |
| 0871 | CHECK TCS. NM_ON_TBL1_20CH  | 27.44 | 28.44 |
| 0872 | CHECK TCS. NM_ON_TBL1_21CH  | 27.44 | 28.44 |
| 0873 | CHECK TCS. NM_ON_TBL1_22CH  | 27.44 | 28.44 |
| 0874 | CHECK TCS. NM_ON_TBL1_23CH  | 27.44 | 28.44 |
| 0875 | CHECK TCS. NM_ON_TBL1_24CH  | 27.44 | 28.44 |
| 0876 | CHECK TCS. NM_ON_TBL1_25CH  | 27.44 | 28.44 |
| 0877 | CHECK TCS. NM_ON_TBL1_26CH  | 27.44 | 28.44 |
| 0878 | CHECK TCS. NM_ON_TBL1_27CH  | 27.44 | 28.44 |
| 0879 | CHECK TCS. NM_ON_TBL1_28CH  | 27.45 | 28.45 |
| 0880 | CHECK TCS. NM_ON_TBL1_29CH  | 27.44 | 28.44 |

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0881 CHECK TCS. HTR_ENADIS_06CH =DIS
0882 CHECK TCS. HTR_ENADIS_13CH =DIS
0883 CHECK TCS. HTR_ENADIS_22CH =DIS
0884 CHECK TCS. HTR_ENADIS_29CH =DIS
0885 . #
0886 . TCS. SET_PRM_ENA
0887 . #
0888 . # < CHECK >
0889 CHECK TCS. SET_PRM_ST =ENA
0890 . #
0891 . TCS. SET_PEAK_PWR 0x2328
0892 . TCS. CTRL_PKPWR_EXE
0893 . #
0894 . # < CHECK >
0895 CHECK TCS. SET_POWER 90
0896 . #
0897 . TCS. CTRL_HTR_P1_ENA
0898 WAIT_SEC 2
0899 TCS. CTRL_HTR_P2_ENA
0900 WAIT_SEC 2
0901 TCS. CTRL_HTR_P3_ENA
0902 WAIT_SEC 2
0903 TCS. CTRL_HTR_P4_ENA
0904 . #
0905 . # < CHECK >
0906 CHECK TCS. HTR01_P_VOL_ST =ON
0907 CHECK TCS. HTR09_P_VOL_ST =ON
0908 CHECK TCS. HTR17_P_VOL_ST =ON
0909 CHECK TCS. HTR25_P_VOL_ST =ON
0910 . #
0911 . # < TABLE 選択 >
0912 . TCS. SET_NMNL_SEL_TBL_1
0913 WAIT_SEC 2
0914 TCS. SET_ATSF_SEL_TBL_1
0915 WAIT_SEC 2
0916 TCS. SET_BKUP_SEL_TBL_1
0917 . #
0918 . # < CHECK >
0919 CHECK TCS. SET_NMNL_TBL =TBL1
0920 CHECK TCS. SET_SAFE_TBL1 =ENA
0921 CHECK TCS. SET_SAFE_TBL2 =DIS
0922 CHECK TCS. SET_SAFE_TBL3 =DIS
0923 CHECK TCS. SET_SAFE_TBL4 =DIS
0924 CHECK TCS. SET_BKUP_TBL1 =ENA
0925 CHECK TCS. SET_BKUP_TBL2 =DIS
0926 CHECK TCS. SET_BKUP_TBL3 =DIS
0927 CHECK TCS. SET_BKUP_TBL4 =DIS
0928 . #
0929 . # < ピーク電力制御 ON >
0930 . TCS. CTRL_PKPWR_ON
0931 . #
0932 . # < CHECK >

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0933 CHECK TCS.PPWR_CTRL =ON
0934 #
0935 . # < ノミナル制御モード移行 >
0936 . TCS.MODE_NMNL
0937 . #
0938 . # < CHECK >
0939 CHECK TCS.MODE_ST =NMNL
0940 #
0941 . # QL Check-Item Spec Check
0942 . # -----+-----+-----+-----
0943 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0944 . #
0945 . # =====
0946 . # 1.3. SI 系初期 CO 時における AT/RQ/ST ENA/DIS 設定 (MDP 立ち上げ前)
0947 . # =====
0948 . #
0949 . # << 概要 >>
0950 . #
0951 . # ATMC 0x01 は、BAT INT 後に ENA、BAT EXT 前に DIS とする。
0952 . # ATMC 0x30-0x34, 0x36 は MDP 立ち上げ後に ENA、MDP 立ち上げ前に DIS とする。
0953 . # ATMC 0x2B は、衛星立ち上げ後に ENA、立下げ前に DIS とする。
0954 . # ATMC 0x09 は、ヒータ設定/制御開始後に ENA、ヒータ立下げ前に DIS とする。
0955 . #
0956 . # RQMC 0x08-0x0B, 0x0D, 0x0E は、各 SI 機器 OFF のリクエストであり、ENA とする
0957 . #
0958 . # << ATMC_ENA/DIS >>
0959 . #
0960 . DH.ATMC_EACH_DIS 0x00 # // 分離後 DMC CPU リセット検出
0961 WAIT_SEC 2
0962 DH.ATMC_EACH_ENA 0x01 # // BAT UVC in Cruise mode
0963 WAIT_SEC 2
0964 DH.ATMC_EACH_DIS 0x02 # //Separation Detection1
0965 WAIT_SEC 2
0966 DH.ATMC_EACH_DIS 0x03 # //Separation Detection2
0967 WAIT_SEC 2
0968 DH.ATMC_EACH_DIS 0x04 # // Backup Separation Detection1 (SA-V)
0969 WAIT_SEC 2
0970 DH.ATMC_EACH_DIS 0x05 # // Backup Separation Detection2 (SSAS_STATUS)
0971 WAIT_SEC 2
0972 DH.ATMC_EACH_DIS 0x06 # //Status check before separation #1
0973 WAIT_SEC 2
0974 DH.ATMC_EACH_DIS 0x07 # //Status check before separation #2
0975 WAIT_SEC 2
0976 DH.ATMC_EACH_DIS 0x08 # N/A
0977 WAIT_SEC 2
0978 DH.ATMC_EACH_ENA 0x09 # //分離前 CPU リセット時 TCS モード再度 NMNL 制御
0979 WAIT_SEC 2
0980 DH.ATMC_EACH_DIS 0x0A # N/A
0981 WAIT_SEC 2
0982 DH.ATMC_EACH_DIS 0x0B # N/A
0983 WAIT_SEC 2
0984 DH.ATMC_EACH_DIS 0x0C # //APR 故障検出

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0985 WAIT_SEC 2
0986 DH. ATMC_EACH_DIS 0x0D # // DRU-A OFF 検出 #1
0987 WAIT_SEC 2
0988 DH. ATMC_EACH_DIS 0x0E # // DRU OFF 検出 #2
0989 WAIT_SEC 2
0990 DH. ATMC_EACH_DIS 0x0F # // MGA deploy monitor
0991 WAIT_SEC 2
0992 DH. ATMC_EACH_DIS 0x10 # //SSAS サンプルスチェック (ACS 自律化 A)
0993 WAIT_SEC 2
0994 DH. ATMC_EACH_DIS 0x11 # //SSAS 太陽角チェック (ACS 自律化 B)
0995 WAIT_SEC 2
0996 DH. ATMC_EACH_DIS 0x12 # // CH1 FIFO エラーリセット
0997 WAIT_SEC 2
0998 DH. ATMC_EACH_DIS 0x13 # N/A
0999 WAIT_SEC 2
1000 DH. ATMC_EACH_DIS 0x14 # // ON/OFF Keying (APM Temperature)
1001 WAIT_SEC 2
1002 DH. ATMC_EACH_DIS 0x15 # // ON/OFF Keying (TWTA-EPC-A Temperature)
1003 WAIT_SEC 2
1004 DH. ATMC_EACH_DIS 0x16 # // ON/OFF Keying (TWTA-EPC-B Temperature)
1005 WAIT_SEC 2
1006 DH. ATMC_EACH_DIS 0x17 # // ON/OFF Keying (LO deck (DMC) Temperature)
1007 WAIT_SEC 2
1008 DH. ATMC_EACH_DIS 0x18 # // ON/OFF Keying (LO deck Temperature)
1009 WAIT_SEC 2
1010 DH. ATMC_EACH_DIS 0x19 # // ON/OFF Keying (SAP Temperature)
1011 WAIT_SEC 2
1012 DH. ATMC_EACH_DIS 0x1A # // ON/OFF Keying (BAT1 Temperature)
1013 WAIT_SEC 2
1014 DH. ATMC_EACH_DIS 0x1B # // ON/OFF Keying (RCS Tank Pressure)
1015 WAIT_SEC 2
1016 DH. ATMC_EACH_DIS 0x1C # N/A
1017 WAIT_SEC 2
1018 DH. ATMC_EACH_DIS 0x1D # //APR2 復帰#2
1019 WAIT_SEC 2
1020 DH. ATMC_EACH_DIS 0x1E # //ON/OFF Keying (ADMCE-A 電流)
1021 WAIT_SEC 2
1022 DH. ATMC_EACH_DIS 0x1F # //ON/OFF Keying (ADMCE-B 電流)
1023 WAIT_SEC 2
1024 DH. ATMC_EACH_DIS 0x20 # //ON/OFF Keying (BUS 電圧)
1025 WAIT_SEC 2
1026 DH. ATMC_EACH_DIS 0x21 # //ON/OFF Keying (BUS 電流)
1027 WAIT_SEC 2
1028 DH. ATMC_EACH_DIS 0x22 # //ON/OFF Keying (BAT 放電)
1029 WAIT_SEC 2
1030 DH. ATMC_EACH_DIS 0x23 # //ON/OFF Keying (SA 電圧)
1031 WAIT_SEC 2
1032 DH. ATMC_EACH_DIS 0x24 # //ON/OFF Keying (DMC-PSU-A 電流)
1033 WAIT_SEC 2
1034 DH. ATMC_EACH_DIS 0x25 # //ON/OFF Keying (DMC-PSU-B 電流)
1035 WAIT_SEC 2
1036 DH. ATMC_EACH_DIS 0x26 # //ON/OFF Keying (TWTA-EPC-A 電流)

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1037 WAIT_SEC 2
1038 DH. ATMC_EACH_DIS 0x27 # //ON/OFF Keying (TWTA-EPC-B 電流)
1039 WAIT_SEC 2
1040 DH. ATMC_EACH_DIS 0x28 # //ON/OFF Keying (DRU-A 電流)
1041 WAIT_SEC 2
1042 DH. ATMC_EACH_DIS 0x29 # //ON/OFF Keying (DRU-B 電流)
1043 WAIT_SEC 2
1044 DH. ATMC_EACH_DIS 0x2A # // BAT UVC
1045 WAIT_SEC 2
1046 DH. ATMC_EACH_ENA 0x2B # //PCD NIC リセットモニタ
1047 WAIT_SEC 2
1048 DH. ATMC_EACH_DIS 0x2C # //BAT 温度モニタ 1
1049 WAIT_SEC 2
1050 DH. ATMC_EACH_DIS 0x2D # //BAT 温度モニタ 2
1051 WAIT_SEC 2
1052 DH. ATMC_EACH_DIS 0x2E # // PCD User Request Error Clear
1053 WAIT_SEC 2
1054 DH. ATMC_EACH_DIS 0x2F # // BAT CV Timer Reset
1055 WAIT_SEC 2
1056 DH. ATMC_EACH_DIS 0x30 # //MDP-DPU1 プログラム起動異常検出
1057 WAIT_SEC 2
1058 DH. ATMC_EACH_DIS 0x31 # //MDP-DPU2 プログラム起動異常検出
1059 WAIT_SEC 2
1060 DH. ATMC_EACH_DIS 0x32 # //MDP-DPU1 重大エラー発生検出
1061 WAIT_SEC 2
1062 DH. ATMC_EACH_DIS 0x33 # //MDP-DPU2 重大エラー発生検出
1063 WAIT_SEC 2
1064 DH. ATMC_EACH_DIS 0x34 # //MDP-DPU1 DMC 通信切れ発生検出 1
1065 WAIT_SEC 2
1066 DH. ATMC_EACH_DIS 0x35 # //MDP-DPU1 DMC 通信切れ発生検出 2
1067 WAIT_SEC 2
1068 DH. ATMC_EACH_DIS 0x36 # //MDP-DPU2 DMC 通信切れ発生検出 1
1069 WAIT_SEC 2
1070 DH. ATMC_EACH_DIS 0x37 # //MDP-DPU2 DMC 通信切れ発生検出 2
1071 WAIT_SEC 2
1072 DH. ATMC_EACH_DIS 0x38 # //MDP-DPU1 冗長時 重大エラー発生検出
1073 WAIT_SEC 2
1074 DH. ATMC_EACH_DIS 0x39 # //MDP-DPU2 冗長時 重大エラー発生検出
1075 WAIT_SEC 2
1076 DH. ATMC_EACH_DIS 0x3A # //MDP-DPU1 冗長時 DMC 通信切れ発生検出 1
1077 WAIT_SEC 2
1078 DH. ATMC_EACH_DIS 0x3B # //MDP-DPU1 冗長時 DMC 通信切れ発生検出 2
1079 WAIT_SEC 2
1080 DH. ATMC_EACH_DIS 0x3C # //MDP-DPU2 冗長時 DMC 通信切れ発生検出 1
1081 WAIT_SEC 2
1082 DH. ATMC_EACH_DIS 0x3D # //MDP-DPU2 冗長時 DMC 通信切れ発生検出 2
1083 WAIT_SEC 2
1084 DH. ATMC_EACH_DIS 0x3E # // MDP1 User Request Error Clear
1085 WAIT_SEC 2
1086 DH. ATMC_EACH_DIS 0x3F # // MDP2 User Request Error Clear
1087 #
1088 # < CHECK >

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|      |                               |
|------|-------------------------------|
| 1089 | CHECK DH. ATMC_ENA_DIS00 =DIS |
| 1090 | CHECK DH. ATMC_ENA_DIS01 =ENA |
| 1091 | CHECK DH. ATMC_ENA_DIS02 =DIS |
| 1092 | CHECK DH. ATMC_ENA_DIS03 =DIS |
| 1093 | CHECK DH. ATMC_ENA_DIS04 =DIS |
| 1094 | CHECK DH. ATMC_ENA_DIS05 =DIS |
| 1095 | CHECK DH. ATMC_ENA_DIS06 =DIS |
| 1096 | CHECK DH. ATMC_ENA_DIS07 =DIS |
| 1097 | CHECK DH. ATMC_ENA_DIS08 =DIS |
| 1098 | CHECK DH. ATMC_ENA_DIS09 =ENA |
| 1099 | CHECK DH. ATMC_ENA_DIS0A =DIS |
| 1100 | CHECK DH. ATMC_ENA_DIS0B =DIS |
| 1101 | CHECK DH. ATMC_ENA_DIS0C =DIS |
| 1102 | CHECK DH. ATMC_ENA_DIS0D =DIS |
| 1103 | CHECK DH. ATMC_ENA_DIS0E =DIS |
| 1104 | CHECK DH. ATMC_ENA_DIS0F =DIS |
| 1105 | CHECK DH. ATMC_ENA_DIS10 =DIS |
| 1106 | CHECK DH. ATMC_ENA_DIS11 =DIS |
| 1107 | CHECK DH. ATMC_ENA_DIS12 =DIS |
| 1108 | CHECK DH. ATMC_ENA_DIS13 =DIS |
| 1109 | CHECK DH. ATMC_ENA_DIS14 =DIS |
| 1110 | CHECK DH. ATMC_ENA_DIS15 =DIS |
| 1111 | CHECK DH. ATMC_ENA_DIS16 =DIS |
| 1112 | CHECK DH. ATMC_ENA_DIS17 =DIS |
| 1113 | CHECK DH. ATMC_ENA_DIS18 =DIS |
| 1114 | CHECK DH. ATMC_ENA_DIS19 =DIS |
| 1115 | CHECK DH. ATMC_ENA_DIS1A =DIS |
| 1116 | CHECK DH. ATMC_ENA_DIS1B =DIS |
| 1117 | CHECK DH. ATMC_ENA_DIS1C =DIS |
| 1118 | CHECK DH. ATMC_ENA_DIS1D =DIS |
| 1119 | CHECK DH. ATMC_ENA_DIS1E =DIS |
| 1120 | CHECK DH. ATMC_ENA_DIS1F =DIS |
| 1121 | CHECK DH. ATMC_ENA_DIS20 =DIS |
| 1122 | CHECK DH. ATMC_ENA_DIS21 =DIS |
| 1123 | CHECK DH. ATMC_ENA_DIS22 =DIS |
| 1124 | CHECK DH. ATMC_ENA_DIS23 =DIS |
| 1125 | CHECK DH. ATMC_ENA_DIS24 =DIS |
| 1126 | CHECK DH. ATMC_ENA_DIS25 =DIS |
| 1127 | CHECK DH. ATMC_ENA_DIS26 =DIS |
| 1128 | CHECK DH. ATMC_ENA_DIS27 =DIS |
| 1129 | CHECK DH. ATMC_ENA_DIS28 =DIS |
| 1130 | CHECK DH. ATMC_ENA_DIS29 =DIS |
| 1131 | CHECK DH. ATMC_ENA_DIS2A =DIS |
| 1132 | CHECK DH. ATMC_ENA_DIS2B =ENA |
| 1133 | CHECK DH. ATMC_ENA_DIS2C =DIS |
| 1134 | CHECK DH. ATMC_ENA_DIS2D =DIS |
| 1135 | CHECK DH. ATMC_ENA_DIS2E =DIS |
| 1136 | CHECK DH. ATMC_ENA_DIS2F =DIS |
| 1137 | CHECK DH. ATMC_ENA_DIS30 =DIS |
| 1138 | CHECK DH. ATMC_ENA_DIS31 =DIS |
| 1139 | CHECK DH. ATMC_ENA_DIS32 =DIS |
| 1140 | CHECK DH. ATMC_ENA_DIS33 =DIS |

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1141 CHECK DH. ATMC_ENA_DIS34 =DIS
1142 CHECK DH. ATMC_ENA_DIS35 =DIS
1143 CHECK DH. ATMC_ENA_DIS36 =DIS
1144 CHECK DH. ATMC_ENA_DIS37 =DIS
1145 CHECK DH. ATMC_ENA_DIS38 =DIS
1146 CHECK DH. ATMC_ENA_DIS39 =DIS
1147 CHECK DH. ATMC_ENA_DIS3A =DIS
1148 CHECK DH. ATMC_ENA_DIS3B =DIS
1149 CHECK DH. ATMC_ENA_DIS3C =DIS
1150 CHECK DH. ATMC_ENA_DIS3D =DIS
1151 CHECK DH. ATMC_ENA_DIS3E =DIS
1152 CHECK DH. ATMC_ENA_DIS3F =DIS
1153 #
1154 #
1155 . # << RQMC_ENA/DIS >>
1156 . #
1157 . DH. RQMC_EACH_DIS 0x00 # N/A
1158 WAIT_SEC 2
1159 DH. RQMC_EACH_DIS 0x01 # // BAT CEL UVC
1160 WAIT_SEC 2
1161 DH. RQMC_EACH_DIS 0x02 # N/A
1162 WAIT_SEC 2
1163 DH. RQMC_EACH_DIS 0x03 # N/A
1164 WAIT_SEC 2
1165 DH. RQMC_EACH_DIS 0x04 # N/A
1166 WAIT_SEC 2
1167 DH. RQMC_EACH_DIS 0x05 # N/A
1168 WAIT_SEC 2
1169 DH. RQMC_EACH_DIS 0x06 # N/A
1170 WAIT_SEC 2
1171 DH. RQMC_EACH_DIS 0x07 # N/A
1172 WAIT_SEC 2
1173 DH. RQMC_EACH_DIS 0x08 # // MEA1-PSU-OFF
1174 WAIT_SEC 2
1175 DH. RQMC_EACH_DIS 0x09 # // MEA2-PSU-OFF
1176 WAIT_SEC 2
1177 DH. RQMC_EACH_DIS 0x0A # // MIA-PSU-OFF
1178 WAIT_SEC 2
1179 DH. RQMC_EACH_DIS 0x0B # // MSA-PSU-OFF
1180 WAIT_SEC 2
1181 DH. RQMC_EACH_DIS 0x0C # N/A
1182 WAIT_SEC 2
1183 DH. RQMC_EACH_DIS 0x0D # // HEP-I-PSU-OFF
1184 WAIT_SEC 2
1185 DH. RQMC_EACH_DIS 0x0E # // ENA-PSU-OFF
1186 WAIT_SEC 2
1187 DH. RQMC_EACH_DIS 0x0F # N/A
1188 WAIT_SEC 2
1189 DH. RQMC_EACH_DIS 0x10 # // スピンレート制御正常終了 (Req A)
1190 WAIT_SEC 2
1191 DH. RQMC_EACH_DIS 0x11 # // 太陽角監視異常 (Req C)
1192 WAIT_SEC 2

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1193 DH.RQMC_EACH_DIS 0x12 # // 太陽角制御異常 (Req D)
1194 WAIT_SEC 2
1195 DH.RQMC_EACH_DIS 0x13 # // Sun Pulse チェック異常時 (Req E)
1196 WAIT_SEC 2
1197 DH.RQMC_EACH_DIS 0x14 # // 分離時 スピニングアップ異常時 (Req 初期 F/Req F)
1198 WAIT_SEC 2
1199 DH.RQMC_EACH_DIS 0x15 # // ADM 回転異常 (Req G)
1200 WAIT_SEC 2
1201 DH.RQMC_EACH_DIS 0x16 # // RCS 圧力センサ異常 (Req H)
1202 WAIT_SEC 2
1203 DH.RQMC_EACH_DIS 0x17 # // 定常時 CPU リセット (Req I)
1204 WAIT_SEC 2
1205 DH.RQMC_EACH_DIS 0x18 # N/A
1206 WAIT_SEC 2
1207 DH.RQMC_EACH_DIS 0x19 # N/A
1208 WAIT_SEC 2
1209 DH.RQMC_EACH_DIS 0x1A # N/A
1210 WAIT_SEC 2
1211 DH.RQMC_EACH_DIS 0x1B # N/A
1212 WAIT_SEC 2
1213 DH.RQMC_EACH_DIS 0x1C # N/A
1214 WAIT_SEC 2
1215 DH.RQMC_EACH_DIS 0x1D # N/A
1216 WAIT_SEC 2
1217 DH.RQMC_EACH_DIS 0x1E # N/A
1218 WAIT_SEC 2
1219 DH.RQMC_EACH_DIS 0x1F # N/A
1220 #
1221 # < CHECK >
1222 CHECK DH.RQMC_ENA_DIS00 =DIS
1223 CHECK DH.RQMC_ENA_DIS01 =DIS
1224 CHECK DH.RQMC_ENA_DIS02 =DIS
1225 CHECK DH.RQMC_ENA_DIS03 =DIS
1226 CHECK DH.RQMC_ENA_DIS04 =DIS
1227 CHECK DH.RQMC_ENA_DIS05 =DIS
1228 CHECK DH.RQMC_ENA_DIS06 =DIS
1229 CHECK DH.RQMC_ENA_DIS07 =DIS
1230 CHECK DH.RQMC_ENA_DIS08 =DIS
1231 CHECK DH.RQMC_ENA_DIS09 =DIS
1232 CHECK DH.RQMC_ENA_DIS0A =DIS
1233 CHECK DH.RQMC_ENA_DIS0B =DIS
1234 CHECK DH.RQMC_ENA_DIS0C =DIS
1235 CHECK DH.RQMC_ENA_DIS0D =DIS
1236 CHECK DH.RQMC_ENA_DIS0E =DIS
1237 CHECK DH.RQMC_ENA_DIS0F =DIS
1238 CHECK DH.RQMC_ENA_DIS10 =DIS
1239 CHECK DH.RQMC_ENA_DIS11 =DIS
1240 CHECK DH.RQMC_ENA_DIS12 =DIS
1241 CHECK DH.RQMC_ENA_DIS13 =DIS
1242 CHECK DH.RQMC_ENA_DIS14 =DIS
1243 CHECK DH.RQMC_ENA_DIS15 =DIS
1244 CHECK DH.RQMC_ENA_DIS16 =DIS

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1245 CHECK DH.RQMC_ENA_DIS17 =DIS
1246 CHECK DH.RQMC_ENA_DIS18 =DIS
1247 CHECK DH.RQMC_ENA_DIS19 =DIS
1248 CHECK DH.RQMC_ENA_DIS1A =DIS
1249 CHECK DH.RQMC_ENA_DIS1B =DIS
1250 CHECK DH.RQMC_ENA_DIS1C =DIS
1251 CHECK DH.RQMC_ENA_DIS1D =DIS
1252 CHECK DH.RQMC_ENA_DIS1E =DIS
1253 CHECK DH.RQMC_ENA_DIS1F =DIS
1254 #
1255 #
1256 . # << SYSTME_ENA/DIS >>
1257 . #
1258 . DH.SYSTMR_EACH_DIS 0x00 # //XSW2 クロス時 Command 受信間隔 Timer
1259 WAIT_SEC 2
1260 DH.SYSTMR_EACH_DIS 0x01 # //XSW2 ストレート時 Command 受信間隔 Timer
1261 WAIT_SEC 2
1262 DH.SYSTMR_EACH_DIS 0x02 # //MDP-DPU1 系 Mission 機器 OFF
1263 WAIT_SEC 2
1264 DH.SYSTMR_EACH_DIS 0x03 # //MDP-DPU2 系 Mission 機器 OFF
1265 WAIT_SEC 2
1266 DH.SYSTMR_EACH_DIS 0x04 # //分離時 Backup Timer
1267 WAIT_SEC 2
1268 DH.SYSTMR_EACH_DIS 0x05 # //Mission 機器 ALL OFF
1269 WAIT_SEC 2
1270 DH.SYSTMR_EACH_DIS 0x06 # //分離時 Sun Acquisition Start
1271 WAIT_SEC 2
1272 DH.SYSTMR_EACH_DIS 0x07 # //分離時 可視時間終了後通信系 OFF
1273 #
1274 . # < CHECK >
1275 CHECK DH.SYSTMR_ENA_DIS0 =DIS
1276 CHECK DH.SYSTMR_ENA_DIS1 =DIS
1277 CHECK DH.SYSTMR_ENA_DIS2 =DIS
1278 CHECK DH.SYSTMR_ENA_DIS3 =DIS
1279 CHECK DH.SYSTMR_ENA_DIS4 =DIS
1280 CHECK DH.SYSTMR_ENA_DIS5 =DIS
1281 CHECK DH.SYSTMR_ENA_DIS6 =DIS
1282 CHECK DH.SYSTMR_ENA_DIS7 =DIS
1283 . #
1284 . # # AT 機能を ENA にする
1285 . DH.ATMC_ENA
1286 . #
1287 . # < CHECK >
1288 CHECK DH.ATMC_ENA_DIS =ENA
1289 . #
1290 . # # RQ waiting に設定する。
1291 . DH.RQMC_ENA
1292 . #
1293 . # < CHECK >
1294 CHECK DH.RQMC_ENA_DIS =ENA
1295 . #
1296 . # # システムタイマー機能を有効にする。

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1297 . DH.SYSTMR_ENA
1298 . #
1299 . # < CHECK >
1300 . CHECK DH.SYSTMR_ENA_DIS =ENA
1301 . #
1302 . # QL          Check-Item          Spec          Check
1303 . # -----+-----+-----+-----
1304 . # CMD 出力装置 Message          STATUS CHECK 結果 OK          G N
1305 . #
1306 . # =====
1307 . # 1.4. SI 機器初期チェックアウト時設定
1308 . # =====
1309 . #
1310 . # 【SI 系初期 CO 試験時の探査機コンフィギュレーション】
1311 . # [COM/DH]
1312 . # XTRP: ON & TX OFF, TWTA: OFF
1313 . #
1314 . # [DMCS/DRU]
1315 . # DMC: ON Dual Mode, 1553B 通信
1316 . # DRU: ON, Heater: NMNL
1317 . #
1318 . # [ACS]
1319 . # RCS: DRV OFF, APM: DRV OFF, ADMCE: OFF
1320 . # SSAS: OFF, SSC: OFF
1321 . # 疑似パルス出力
1322 . #
1323 . # [PCS]
1324 . # APR: OFF, PCD: ON
1325 . # BAT: INT
1326 . # BAT 充放電: 不要
1327 . #
1328 . # [SI]
1329 . # MDP: ON
1330 . # その他 SI 機器: CO 対象機器について ON
1331 . # 初期 CO 試験用 機器 ON/OFF コンフィギュレーション確認
1332 . #
1333 . # < CHECK >
1334 . CHECK PCD.XTRP_A_ON_OFF          =ON
1335 . CHECK PCD.XTRP_B_ON_OFF          =ON
1336 . CHECK PCD.EPC_A_ON_OFF           =OFF
1337 . CHECK PCD.EPC_B_ON_OFF           =OFF
1338 . CHECK DH.XTRP_A_TX_ON_OFF        =OFF
1339 . CHECK DH.XTRP_B_TX_ON_OFF        =OFF
1340 . CHECK PCD.EPC_A_ON_OFF           =OFF
1341 . CHECK PCD.EPC_B_ON_OFF           =OFF
1342 . CHECK PCD.DRU_PSU_A_ON_OFF        =ON
1343 . CHECK PCD.DRU_PSU_B_ON_OFF        =OFF
1344 . CHECK TCS.MODE_ST                 =NMNL
1345 . CHECK ACS_SYS.RCS_A_B_ST         =OFF
1346 . CHECK ACS_SYS.STS_DRUAPM_ON      =OFF
1347 . CHECK PCD.ADMCE_A_ON_OFF         =OFF
1348 . CHECK PCD.ADMCE_B_ON_OFF         =OFF

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1349 CHECK ACS_SYS.STS_SSASA_PWR =OFF
1350 CHECK ACS_SYS.STS_SSASB_PWR =OFF
1351 CHECK ACS_SYS.SSC_PWR =OFF
1352 CHECK PCD.APR1_ON_OFF =OFF
1353 CHECK PCD.APR2_ON_OFF =ON
1354 CHECK PCD.PSU_MDP1_ON_OFF =OFF
1355 CHECK PCD.PSU_MDP2_ON_OFF =OFF
1356 CHECK PCD.PSU_MORTOR_A_ON_OFF =OFF
1357 CHECK PCD.PSU_MORTOR_B_ON_OFF =OFF
1358 CHECK PCD.PSU_ENA_ON_OFF =OFF
1359 CHECK PCD.PSU_MEA1_ON_OFF =OFF
1360 CHECK PCD.PSU_MEA2_ON_OFF =OFF
1361 CHECK PCD.PSU_MSA_ON_OFF =OFF
1362 CHECK PCD.PSU_MIA_ON_OFF =OFF
1363 CHECK PCD.PSU_HEP_E_ON_OFF =OFF
1364 CHECK PCD.PSU_HEP_I_ON_OFF =OFF
1365 CHECK PCD.PSU_PME_ON_OFF =OFF
1366 CHECK PCD.PSU_MGF_I_ON_OFF =OFF
1367 CHECK PCD.MDM_E_ON_OFF =OFF
1368 CHECK PCD.MSASI_ON_OFF =OFF
1369 CHECK PCD.SURVIVAL_HTR_ON_OFF =ON
1370 #
1371 # QL Check-Item Spec Check
1372 # -----+-----+-----+-----
1373 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1374 #
1375 # *****
1376 # 2. MDP 立ち上げ
1377 # *****
1378 #
1379 . DH.MC_EACH_EXE 0x002C # 1553B TLM mode 0 (SYS HK 1)
1380 #: << MACRO >> 内容について以下に示す(DH.DUMMYは省略)
1381 #: 0x00 DH.DMP_PERIOD 0x04
1382 #: 0x04 ACS.ACS_PKT_FMT_1
1383 #: 0x05 DH.OPE_MODE 0x0C 0x0A 0x00 0x00 0x000000000000000000000000
1384 #: 0x06 DH.TLMFMT1553B_CHG 0x00
1385 WAIT_SEC 40
1386 #
1387 # < CHECK >
1388 CHECK DH.DMP_PERIOD =SEC_16
1389 CHECK DH.CATEGORY_TBL_NO 0x00
1390 CHECK DH.DWNLNK_RATE =BPS16384
1391 CHECK DH.TLM1553BFMTNO 0x00
1392 #
1393 # MDP-DPU1,2(A系)のLINK Enableを確認
1394 #
1395 # < CHECK >
1396 CHECK DH.SPW_POTMD_L1 =ENA
1397 CHECK DH.SPW_POTMD_L2 =ENA
1398 #
1399 . # MDP-DPU1,2を電源ONする。(HW_STATE1は16秒以内にチェックすること。)
1400 #

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1401 # <<< MDP-DPU1/2 Power ON >>>
1402 #
1403 . DH. MDP_DPU12_POWER_ON # DH. MC_EACH_EXE 0x099
1404 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
1405 #: 0x00 PCD. ON_OFF_ENA
1406 #: 0x01 PCD. PSU_MDP1_ON
1407 #: 0x02 PCD. PSU_MDP2_ON
1408 #: 0x03 PCD. ON_OFF_DIS
1409 #
1410 #
1411 # < CHECK >
1412 CHECK MD1F.DPU_HW_STATE1 =DPU_RESET
1413 CHECK MD2F.DPU_HW_STATE1 =DPU_RESET
1414 CHECK PCD.ON_OFF_ENA_DIS =DIS
1415 CHECK PCD.PSU_MDP1_ON_OFF =ON
1416 CHECK PCD.PSU_MDP2_ON_OFF =ON
1417 CHECK DH.SPW_LNKST_L1 =RUN
1418 CHECK DH.SPW_LNKST_L2 =RUN
1419 #
1420 # *****
1421 # WAIT 16 sec (HW Initialization of DPU1 & DPU2)
1422 # *****
1423 WAIT_SEC 40
1424 #
1425 # <<< MDP-DPU Initial HK check (2): 16 sec after PWR-ON >>>
1426 #
1427 # < CHECK >
1428 CHECK MD1F.DPU_HW_STATE1 =DPU_RUN
1429 CHECK MD1F.DPU_HW_STATE2 =CPUOFF_DPUBUS
1430 CHECK MD1F.DMC_IF_RMAPERR =na
1431 CHECK MD1F.DMC_IF_2BITERR =na
1432 CHECK MD1F.DMC_IF_BTOERR =na
1433 CHECK MD1F.BTO_ERR =na
1434 CHECK MD1F.WDT_ERR =na
1435 CHECK MD1F.DPU_SELECT =DPU1
1436 CHECK MD2F.DPU_HW_STATE1 =DPU_RUN
1437 CHECK MD2F.DPU_HW_STATE2 =CPUOFF_DPUBUS
1438 CHECK MD2F.DMC_IF_RMAPERR =na
1439 CHECK MD2F.DMC_IF_2BITERR =na
1440 CHECK MD2F.DMC_IF_BTOERR =na
1441 CHECK MD2F.BTO_ERR =na
1442 CHECK MD2F.WDT_ERR =na
1443 CHECK MD2F.DPU_SELECT =DPU2
1444 #
1445 # <<< MDP-DPU CPU_ON / RUN >>>
1446 DH. MDP_DPU12_CPU_RUN # DH. MC_EACH_EXE 0x009A
1447 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
1448 #: 0x00 DH. ATMC_EACH_ENA 0x34
1449 #: 0x01 DH. SPW_RTEFDIR_SET 0x1E
1450 #: 0x05 DH. ATMC_EACH_ENA 0x36
1451 #: 0x06 MD1F. CPU_ON
1452 #: 0x07 MD2F. CPU_ON

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1453 #:          0x08 MD1F.CPU_RUN
1454 #:          0x09 MD2F.CPU_RUN
1455 #:          0x0A DH.ATMC_EACH_ENA 0x30
1456 #:          0x0F DH.ATMC_EACH_ENA 0x31
1457 WAIT_SEC 180
1458 # ここで 180 秒待機 (MDP MiddleWare による初期化終了待ち)
1459 #
1460 # < CHECK >
1461 CHECK DH.SPW_FDIR_ENA_MDP1      =ENA
1462 CHECK DH.SPW_FDIR_ENA_MDP2      =ENA
1463 CHECK DH.SPW_FDIR_ENA_PCD       =ENA
1464 CHECK DH.SPW_FDIR_ENA_RMTRTR    =ENA
1465 CHECK MD1F.DPU_HW_STATE2        =CPU_RUN
1466 CHECK MD2F.DPU_HW_STATE2        =CPU_RUN
1467 #
1468 #
1469 # NECP で-updatewait は使用できないのでコメントアウト
1470 #
1471 # < MDP1 SATELITE TIME CHECK (0x200/s) >
1472 #
1473 # CHECK MD1F.SAT_TIME -updatewait 180
1474 # LET LocalA0 = {MD1F.SAT_TIME} & 0xFFFFFFFF
1475 # CHECK MD1F.SAT_TIME -updatewait 4
1476 # LET LocalA1 = {MD1F.SAT_TIME} & 0xFFFFFFFF
1477 # LET LocalA1 = LocalA1 - LocalA0
1478 # SYSTEM test {LocalA1} = 512
1479 #
1480 # < MDP1 SUNPULSE TIME CHECK (0x800-0xB00/4s) >
1481 #
1482 # CHECK MD1F.SUNPULSE_TIME -updatewait 8
1483 # LET LocalA0 = {MD1F.SUNPULSE_TIME} & 0xFFFFFFFF
1484 # CHECK MD1F.SUNPULSE_TIME -updatewait 8
1485 # LET LocalA1 = {MD1F.SUNPULSE_TIME} & 0xFFFFFFFF
1486 # LET LocalA1 = LocalA1 - LocalA0
1487 # SYSTEM test {LocalA1} -ge 2048 -a {LocalA1} -le 2816
1488 #
1489 # < MDP2 SATELITE TIME CHECK (0x200/s) >
1490 #
1491 # CHECK MD2F.SAT_TIME -updatewait 8
1492 # LET LocalA0 = {MD2F.SAT_TIME} & 0xFFFFFFFF
1493 # CHECK MD2F.SAT_TIME -updatewait 4
1494 # LET LocalA1 = {MD2F.SAT_TIME} & 0xFFFFFFFF
1495 # LET LocalA1 = LocalA1 - LocalA0
1496 # SYSTEM test {LocalA1} = 512
1497 #
1498 # < MDP2 SUNPULSE TIME CHECK (0x800-0xB00/4s) >
1499 #
1500 # CHECK MD2F.SUNPULSE_TIME -updatewait 8
1501 # LET LocalA0 = {MD2F.SUNPULSE_TIME} & 0xFFFFFFFF
1502 # CHECK MD2F.SUNPULSE_TIME -updatewait 8
1503 # LET LocalA1 = {MD2F.SUNPULSE_TIME} & 0xFFFFFFFF
1504 # LET LocalA1 = LocalA1 - LocalA0

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1505 # SYSTEM test {LocalA1} -ge 2048 -a {LocalA1} -le 2816
1506 #
1507 #
1508 # <<< MDP-DPU Initial HK check (3): after the Initialization >>>
1509 # CPU_RUN 時、確率 7/8 で SLOT_ERR 起きるが問題ない(起動時の SLOT 番号齟齬)
1510 #
1511 # < CHECK >
1512 CHECK MD1F.DPU_HW_STATE1 =DPU_RUN
1513 CHECK MD1F.DPU_HW_STATE2 =CPU_RUN
1514 CHECK MD1F.DMC_IF_RMAPERR =na
1515 CHECK MD1F.DMC_IF_2BITERR =na
1516 CHECK MD1F.DMC_IF_BTOERR =na
1517 CHECK MD1F.BTO_ERR =na
1518 CHECK MD1F.WDT_ERR =na
1519 CHECK MD1F.DPU_SELECT =DPU1
1520 CHECK MD1F.DPU_SW_STATE 2
1521 CHECK MD1F.SW_CMD_CODE 0
1522 CHECK MD1F.SW_CMD_CNT 0
1523 CHECK MD1F.SW_CMD_REJ_CNT 0
1524 CHECK MD1F.SW_CMD_REJ_CODE 0
1525 CHECK MD1F.MS_CMD_PRIO =DMC
1526 CHECK MD1F.RDN_MODE =OFF
1527 CHECK MD1F.SUNPULSE_MODE =NML
1528 CHECK MD1F.LOAD_DUMP_DIS =DIS
1529 CHECK MD1F.MEMLOAD_BUSY =OFF
1530 CHECK MD1F.MEMLOAD_NODE 0
1531 CHECK MD1F.MEMDUMP_BUSY =OFF
1532 CHECK MD1F.MEMDUMP_NODE 0
1533 CHECK MD1F.TASK_STAT_AP01 =OFF
1534 CHECK MD1F.TASK_STAT_AP02 =OFF
1535 CHECK MD1F.TASK_STAT_AP03 =OFF
1536 CHECK MD1F.TASK_STAT_AP04 =OFF
1537 CHECK MD1F.TASK_STAT_AP05 =OFF
1538 CHECK MD1F.TASK_STAT_AP06 =OFF
1539 CHECK MD1F.TASK_STAT_AP07 =OFF
1540 CHECK MD1F.TASK_STAT_AP08 =OFF
1541 CHECK MD1F.TASK_STAT_AP09 =OFF
1542 CHECK MD1F.TASK_STAT_AP10 =OFF
1543 CHECK MD1F.TASK_STAT_AP11 =OFF
1544 CHECK MD1F.TASK_STAT_AP12 =OFF
1545 CHECK MD1F.TASK_STAT_AP13 =OFF
1546 CHECK MD1F.TASK_STAT_AP14 =OFF
1547 CHECK MD1F.TASK_STAT_AP15 =OFF
1548 CHECK MD1F.TASK_STAT_AP16 =OFF
1549 CHECK MD1F.UNEXPECT_CNT 0
1550 CHECK MD1F.UNEXPECT_CODE 0
1551 CHECK MD1F.DMC_IF_ERR =na
1552 CHECK MD1F.TIME_ERR =na
1553 CHECK MD1F.INST_CACHE_ERR =na
1554 CHECK MD1F.DATA_CACHE_ERR =na
1555 CHECK MD1F.SRAM_1BERR =na
1556 CHECK MD1F.CMD_BUFF_1BERR =na

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|      |                            |          |
|------|----------------------------|----------|
| 1557 | CHECK MD1F.CMD_BUFF_2BERR  | =na      |
| 1558 | CHECK MD1F.TLM_BUFF_1BERR  | =na      |
| 1559 | CHECK MD1F.TLM_BUFF_2BERR  | =na      |
| 1560 | CHECK MD1F.COMP_1BERR      | =na      |
| 1561 | CHECK MD1F.DS_1BERR        | =na      |
| 1562 | CHECK MD1F.DS_2BERR        | =na      |
| 1563 | CHECK MD1F.RDN_LINK_STAT   | =ON      |
| 1564 | CHECK MD1F.RIF_LINK_ERR    | =na      |
| 1565 | CHECK MD1F.RIF_ERR         | =na      |
| 1566 | CHECK MD1F.MS0_LINK_STAT   | =OFF     |
| 1567 | CHECK MD1F.MS1_LINK_STAT   | =OFF     |
| 1568 | CHECK MD1F.MS2_LINK_STAT   | =OFF     |
| 1569 | CHECK MD1F.MS3_LINK_STAT   | =OFF     |
| 1570 | CHECK MD1F.MS4_LINK_STAT   | =OFF     |
| 1571 | CHECK MD1F.MS5_LINK_STAT   | =OFF     |
| 1572 | CHECK MD1F.MS6_LINK_STAT   | =OFF     |
| 1573 | CHECK MD1F.MS7_LINK_STAT   | =OFF     |
| 1574 | CHECK MD1F.MS0_DAT_CLCT    | =OFF     |
| 1575 | CHECK MD1F.MS1_DAT_CLCT    | =OFF     |
| 1576 | CHECK MD1F.MS2_DAT_CLCT    | =OFF     |
| 1577 | CHECK MD1F.MS3_DAT_CLCT    | =OFF     |
| 1578 | CHECK MD1F.MS4_DAT_CLCT    | =OFF     |
| 1579 | CHECK MD1F.MS5_DAT_CLCT    | =OFF     |
| 1580 | CHECK MD1F.MS6_DAT_CLCT    | =OFF     |
| 1581 | CHECK MD1F.MS7_DAT_CLCT    | =OFF     |
| 1582 | CHECK MD1F.MS0_BUFF_SEL    | =L       |
| 1583 | CHECK MD1F.MS1_BUFF_SEL    | =L       |
| 1584 | CHECK MD1F.MS2_BUFF_SEL    | =L       |
| 1585 | CHECK MD1F.MS3_BUFF_SEL    | =L       |
| 1586 | CHECK MD1F.MS4_BUFF_SEL    | =L       |
| 1587 | CHECK MD1F.MS5_BUFF_SEL    | =L       |
| 1588 | CHECK MD1F.MS6_BUFF_SEL    | =L       |
| 1589 | CHECK MD1F.MS7_BUFF_SEL    | =L       |
| 1590 | CHECK MD1F.MS0_SGL_CMD_ERR | =na      |
| 1591 | CHECK MD1F.MS1_SGL_CMD_ERR | =na      |
| 1592 | CHECK MD1F.MS2_SGL_CMD_ERR | =na      |
| 1593 | CHECK MD1F.MS3_SGL_CMD_ERR | =na      |
| 1594 | CHECK MD1F.MS4_SGL_CMD_ERR | =na      |
| 1595 | CHECK MD1F.MS5_SGL_CMD_ERR | =na      |
| 1596 | CHECK MD1F.MS6_SGL_CMD_ERR | =na      |
| 1597 | CHECK MD1F.MS7_SGL_CMD_ERR | =na      |
| 1598 | CHECK MD1F.MS0_MTI_CMD_ERR | =na      |
| 1599 | CHECK MD1F.MS1_MTI_CMD_ERR | =na      |
| 1600 | CHECK MD1F.MS2_MTI_CMD_ERR | =na      |
| 1601 | CHECK MD1F.MS3_MTI_CMD_ERR | =na      |
| 1602 | CHECK MD1F.MS4_MTI_CMD_ERR | =na      |
| 1603 | CHECK MD1F.MS5_MTI_CMD_ERR | =na      |
| 1604 | CHECK MD1F.MS6_MTI_CMD_ERR | =na      |
| 1605 | CHECK MD1F.MS7_MTI_CMD_ERR | =na      |
| 1606 | CHECK MD2F.DPU_HW_STATE1   | =DPU_RUN |
| 1607 | CHECK MD2F.DPU_HW_STATE2   | =CPU_RUN |
| 1608 | CHECK MD2F.DMC_IF_RMAPERR  | =na      |

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|      |                            |       |
|------|----------------------------|-------|
| 1609 | CHECK MD2F.DMC_IF_2BITERR  | =na   |
| 1610 | CHECK MD2F.DMC_IF_BTOERR   | =na   |
| 1611 | CHECK MD2F.BTO_ERR         | =na   |
| 1612 | CHECK MD2F.WDT_ERR         | =na   |
| 1613 | CHECK MD2F.DPU_SELECT      | =DPU2 |
| 1614 | CHECK MD2F.DPU_SW_STATE    | 2     |
| 1615 | CHECK MD2F.SW_CMD_CODE     | 0     |
| 1616 | CHECK MD2F.SW_CMD_CNT      | 0     |
| 1617 | CHECK MD2F.SW_CMD_REJ_CNT  | 0     |
| 1618 | CHECK MD2F.SW_CMD_REJ_CODE | 0     |
| 1619 | CHECK MD2F.MS_CMD_PRIO     | =DMC  |
| 1620 | CHECK MD2F.RDN_MODE        | =OFF  |
| 1621 | CHECK MD2F.SUNPULSE_MODE   | =NML  |
| 1622 | CHECK MD2F.LOAD_DUMP_DIS   | =DIS  |
| 1623 | CHECK MD2F.MEMLOAD_BUSY    | =OFF  |
| 1624 | CHECK MD2F.MEMLOAD_NODE    | 0     |
| 1625 | CHECK MD2F.MEMDUMP_BUSY    | =OFF  |
| 1626 | CHECK MD2F.MEMDUMP_NODE    | 0     |
| 1627 | CHECK MD2F.TASK_STAT_AP01  | =OFF  |
| 1628 | CHECK MD2F.TASK_STAT_AP02  | =OFF  |
| 1629 | CHECK MD2F.TASK_STAT_AP03  | =OFF  |
| 1630 | CHECK MD2F.TASK_STAT_AP04  | =OFF  |
| 1631 | CHECK MD2F.TASK_STAT_AP05  | =OFF  |
| 1632 | CHECK MD2F.TASK_STAT_AP06  | =OFF  |
| 1633 | CHECK MD2F.TASK_STAT_AP07  | =OFF  |
| 1634 | CHECK MD2F.TASK_STAT_AP08  | =OFF  |
| 1635 | CHECK MD2F.TASK_STAT_AP09  | =OFF  |
| 1636 | CHECK MD2F.TASK_STAT_AP10  | =OFF  |
| 1637 | CHECK MD2F.TASK_STAT_AP11  | =OFF  |
| 1638 | CHECK MD2F.TASK_STAT_AP12  | =OFF  |
| 1639 | CHECK MD2F.TASK_STAT_AP13  | =OFF  |
| 1640 | CHECK MD2F.TASK_STAT_AP14  | =OFF  |
| 1641 | CHECK MD2F.TASK_STAT_AP15  | =OFF  |
| 1642 | CHECK MD2F.TASK_STAT_AP16  | =OFF  |
| 1643 | CHECK MD2F.UNEXPECT_CNT    | 0     |
| 1644 | CHECK MD2F.UNEXPECT_CODE   | 0     |
| 1645 | CHECK MD2F.DMC_IF_ERR      | =na   |
| 1646 | CHECK MD2F.TIME_ERR        | =na   |
| 1647 | CHECK MD2F.INST_CACHE_ERR  | =na   |
| 1648 | CHECK MD2F.DATA_CACHE_ERR  | =na   |
| 1649 | CHECK MD2F.SRAM_1BERR      | =na   |
| 1650 | CHECK MD2F.CMD_BUFF_1BERR  | =na   |
| 1651 | CHECK MD2F.CMD_BUFF_2BERR  | =na   |
| 1652 | CHECK MD2F.TLM_BUFF_1BERR  | =na   |
| 1653 | CHECK MD2F.TLM_BUFF_2BERR  | =na   |
| 1654 | CHECK MD2F.COMP_1BERR      | =na   |
| 1655 | CHECK MD2F.DS_1BERR        | =na   |
| 1656 | CHECK MD2F.DS_2BERR        | =na   |
| 1657 | CHECK MD2F.RDN_LINK_STAT   | =ON   |
| 1658 | CHECK MD2F.RIF_LINK_ERR    | =na   |
| 1659 | CHECK MD2F.RIF_ERR         | =na   |
| 1660 | CHECK MD2F.MSO_LINK_STAT   | =OFF  |

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1661 CHECK MD2F.MS1_LINK_STAT =OFF
1662 CHECK MD2F.MS2_LINK_STAT =OFF
1663 CHECK MD2F.MS3_LINK_STAT =OFF
1664 CHECK MD2F.MS4_LINK_STAT =OFF
1665 CHECK MD2F.MS5_LINK_STAT =OFF
1666 CHECK MD2F.MS6_LINK_STAT =OFF
1667 CHECK MD2F.MS7_LINK_STAT =OFF
1668 CHECK MD2F.MS1_DAT_CLCT =OFF
1669 CHECK MD2F.MS3_DAT_CLCT =OFF
1670 CHECK MD2F.MS5_DAT_CLCT =OFF
1671 CHECK MD2F.MS6_DAT_CLCT =OFF
1672 CHECK MD2F.MS7_DAT_CLCT =OFF
1673 CHECK MD2F.MS1_BUFF_SEL =L
1674 CHECK MD2F.MS3_BUFF_SEL =L
1675 CHECK MD2F.MS5_BUFF_SEL =L
1676 CHECK MD2F.MS6_BUFF_SEL =L
1677 CHECK MD2F.MS7_BUFF_SEL =L
1678 CHECK MD2F.MS0_SGL_CMD_ERR =na
1679 CHECK MD2F.MS1_SGL_CMD_ERR =na
1680 CHECK MD2F.MS2_SGL_CMD_ERR =na
1681 CHECK MD2F.MS3_SGL_CMD_ERR =na
1682 CHECK MD2F.MS4_SGL_CMD_ERR =na
1683 CHECK MD2F.MS5_SGL_CMD_ERR =na
1684 CHECK MD2F.MS6_SGL_CMD_ERR =na
1685 CHECK MD2F.MS7_SGL_CMD_ERR =na
1686 CHECK MD2F.MS0_MTI_CMD_ERR =na
1687 CHECK MD2F.MS1_MTI_CMD_ERR =na
1688 CHECK MD2F.MS2_MTI_CMD_ERR =na
1689 CHECK MD2F.MS3_MTI_CMD_ERR =na
1690 CHECK MD2F.MS4_MTI_CMD_ERR =na
1691 CHECK MD2F.MS5_MTI_CMD_ERR =na
1692 CHECK MD2F.MS6_MTI_CMD_ERR =na
1693 CHECK MD2F.MS7_MTI_CMD_ERR =na
1694 #
1695 #
1696 . # # Mission Data を 1553B 通信の半分の帯域 (約 4kbps) で出力するモードとする
1697 . DH.MC_EACH_EXE 0x0036 # 1553B TLM mode 10 (SYS HK 1/16, BUS User HK 1/8, DUMP 1/16,
1698 # # MDP User HK 1/8, Mission User HK 1/8, Mission Data 1/2)
1699 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
1700 #: 0x00 DH.DMP_PERIOD 0x04
1701 #: 0x01 DH.DR_PTLOGCYC_SET 0x00
1702 #: 0x02 ACS.ACS_PKT_FMT_1
1703 #: 0x05 DH.OPE_MODE 0x0C 0x3E 0x2C 0x04 0x00000000000000000000000000000000
1704 #: 0x06 DH.TLMFMT1553B_CHG 0x0A
1705 #
1706 WAIT_SEC 40
1707 #
1708 CHECK DH.DMP_PERIOD =SEC_16
1709 CHECK DH.CATEGORY_TBL_NO 0x2C
1710 CHECK DH.DWNLNK_RATE =BPS16384
1711 CHECK DH.TLM1553BFMTNO 0x0A
1712 CHECK ACS.F_ACS_PKT_FMT =ACSHK1

```

```

1713 #
1714 #
1715 # < MDP-DPU User-Task RUN : MDP-DPU1/2 APL START >
1716 . DH.MDP_DPU12_APL_START # DH.MC_EACH_EXE 0x009B
1717 #:          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
1718 #:          0x00 DH.ATMC_EACH_ENA 0x32
1719 #:          0x02 MD1F.APP01_START
1720 #:          0x03 MD1F.APP02_START
1721 #:          0x04 MD1F.APP03_START
1722 #:          0x05 MD1F.APP04_START
1723 #:          0x06 MD1F.APP05_START
1724 #:          0x07 DH.ATMC_EACH_ENA 0x33
1725 #:          0x08 MD2F.APP01_START
1726 #:          0x09 MD2F.APP02_START
1727 #:          0x0A MD2F.APP13_START
1728 #:          0x0B MD2F.APP14_START
1729 #:          0x0C MD2F.APP05_START
1730 #
1731 WAIT_SEC 40
1732 #
1733 #
1734 # < CHECK >
1735 CHECK DH.ATMC_ENA_DIS32          =ENA
1736 CHECK DH.ATMC_ENA_DIS33          =ENA
1737 #
1738 # QL          Check-Item          Spec          Check
1739 #-----+-----+-----+-----+
1740 # MD1F        << DPU1 S/W HK >>
1741 #             SWCMD_CNT            (+5 increment)      N/A
1742 #             SWREJ_CNT            (no change)         N/A
1743 #             << APPLICATIONS >>
1744 #
1745 # < CHECK >
1746 CHECK MD1F.TASK_STAT_AP01        =ON
1747 CHECK MD1F.TASK_STAT_AP02        =ON
1748 CHECK MD1F.TASK_STAT_AP03        =ON
1749 CHECK MD1F.TASK_STAT_AP04        =ON
1750 CHECK MD1F.TASK_STAT_AP05        =ON
1751 #
1752 # QL          Check-Item          Spec          Check
1753 #-----+-----+-----+-----+
1754 # MD2F        << DPU2 S/W HK >>
1755 #             SWCMD_CNT            (+5 increment)      N/A
1756 #             SWREJ_CNT            (no change)         N/A
1757 #             << APPLICATIONS >>
1758 #
1759 # < CHECK >
1760 CHECK MD2F.TASK_STAT_AP01        =ON
1761 CHECK MD2F.TASK_STAT_AP02        =ON
1762 CHECK MD2F.TASK_STAT_AP13        =ON
1763 CHECK MD2F.TASK_STAT_AP14        =ON
1764 CHECK MD2F.TASK_STAT_AP05        =ON

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1765 #
1766 #
1767 # < MDP USER-TASK: Initial Sequence - MDP-DPU-INITSET>
1768 #
1769 #
1770 . DH.MDP_DPU12_INIT_SET          # DH.MC_EACH_EXE 0x080
1771 #:                               << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
1772 #:                               0x00 MD1U.HK_SET 0xFF 0x08
1773 #:                               0x01 MD1U.HK_MODE_CHK
1774 #:                               0x02 MD1U.TLM_CMP 0xFF 0x00 0x11
1775 #:                               0x03 MD2U.HK_SET 0xFF 0x08
1776 #:                               0x04 MD2U.HK_MODE_CHK
1777 #:                               0x05 MD2U.TLM_CMP 0xFF 0x00 0x11
1778 #:                               0x0E MD1U.WDT_ENA
1779 #:                               0x0F MD2U.WDT_ENA
1780 WAIT_SEC 5
1781 MD1U.HK_SET 0xFF 0x04
1782 WAIT_SEC 2
1783 MD2U.HK_SET 0xFF 0x04
1784 WAIT_SEC 40
1785 #
1786 # QL          Check-Item          Spec          Check
1787 #-----+-----+-----+-----+
1788 # MD1U        U_CmdDmc_GetCNT      (increment: +5)          N/A
1789 #             U_CmdDmc_ExecCNT      (increment: +5)          N/A
1790 #             U_App03_Load           (checked by MDP)
1791 #-----+-----+-----+-----+
1792 # MD2U        U_CmdDmc_GetCNT      (increment: +5)          N/A
1793 #             U_CmdDmc_ExecCNT      (increment: +5)          N/A
1794 #             U_App13_Load           (checked by MDP)
1795 #
1796 # < CHECK >
1797 CHECK MD1U.U_WdtEna                =ENA
1798 CHECK MD1U.U_HkMode                 =CHK
1799 CHECK MD1U.U_Hk_RepInt_DPU1         4 4
1800 CHECK MD1U.U_Hk_RepInt_ENA          4 4
1801 CHECK MD1U.U_Hk_RepInt_HEPE         4 4
1802 CHECK MD1U.U_Hk_RepInt_HEPI         4 4
1803 CHECK MD1U.U_Hk_RepInt_MEA1         4 4
1804 CHECK MD1U.U_Hk_RepInt_MEA2         4 4
1805 CHECK MD1U.U_Hk_RepInt_MGFO         4 4
1806 CHECK MD1U.U_Hk_RepInt_MIA          4 4
1807 CHECK MD1U.U_Hk_RepInt_MSA          4 4
1808 CHECK MD1U.U_TlmML_CMP_DPU1         =CMP
1809 CHECK MD1U.U_TlmML_CMP_ENA          =USR
1810 CHECK MD1U.U_TlmML_CMP_HEPE         =ARO
1811 CHECK MD1U.U_TlmML_CMP_HEPI         =ARO
1812 CHECK MD1U.U_TlmML_CMP_MEA1         =USR
1813 CHECK MD1U.U_TlmML_CMP_MEA2         =USR
1814 CHECK MD1U.U_TlmML_CMP_MGFO         =ARO
1815 CHECK MD1U.U_TlmML_CMP_MIA          =USR
1816 CHECK MD1U.U_TlmML_CMP_MSA          =USR

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1817 CHECK MD1U.U_TImH_CMP_DPU1 =CMP
1818 CHECK MD1U.U_TImH_CMP_HEPE =ARO
1819 CHECK MD1U.U_TImH_CMP_HEPI =ARO
1820 CHECK MD1U.U_TImH_CMP_MEA1 =USR
1821 CHECK MD1U.U_TImH_CMP_MEA2 =USR
1822 CHECK MD1U.U_TImH_CMP_MGFO =ARO
1823 CHECK MD1U.U_TImH_CMP_MIA =USR
1824 CHECK MD1U.U_TImH_CMP_MSA =USR
1825 CHECK MD2U.U_WdtEna =ENA
1826 CHECK MD2U.U_HkMode =CHK
1827 CHECK MD2U.U_Hk_RepInt_DPU2 4 4
1828 CHECK MD2U.U_Hk_RepInt_EWO 4 4
1829 CHECK MD2U.U_Hk_RepInt_MEF 4 4
1830 CHECK MD2U.U_Hk_RepInt_MGFI 4 4
1831 CHECK MD2U.U_Hk_RepInt_MSAS 4 4
1832 CHECK MD2U.U_Hk_RepInt_MWE 4 4
1833 CHECK MD2U.U_TImML_CMP_DPU2 =CMP
1834 CHECK MD2U.U_TImML_CMP_AM2P =ARO
1835 CHECK MD2U.U_TImML_CMP_EFD =ARO
1836 CHECK MD2U.U_TImML_CMP_EWOB =ARO
1837 CHECK MD2U.U_TImML_CMP_EWOE =ARO
1838 CHECK MD2U.U_TImML_CMP_MDM =ARO
1839 CHECK MD2U.U_TImML_CMP_MGFI =ARO
1840 CHECK MD2U.U_TImML_CMP_MSAS =ARO
1841 CHECK MD2U.U_TImML_CMP_SOR =ARO
1842 CHECK MD2U.U_TImH_CMP_DPU2 =CMP
1843 CHECK MD2U.U_TImH_CMP_EFD =ARO
1844 CHECK MD2U.U_TImH_CMP_EWOB =USR
1845 CHECK MD2U.U_TImH_CMP_EWOE =USR
1846 CHECK MD2U.U_TImH_CMP_MGFI =ARO
1847 #
1848 # QL Check-Item Spec Check
1849 # -----+-----+-----+-----
1850 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1851 #
1852 #
1853 # *****
1854 # 3. MEA1 初期 CO
1855 # *****
1856 # =====
1857 # 3.1. MEA1 function check
1858 # =====
1859 # -----
1860 # 3.1.1. MEA1 Startup
1861 # <<M15-1 Initial Power ON Duration:10 min Day:35>>
1862 # -----
1863 #
1864 # # MEA1 の電源を ON する。
1865 # Power on MEA1 by switching on.
1866 #
1867 # PCD.ON_OFF_ENA
1868 # WAIT_SEC 40

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1869 #
1870 # < CHECK >
1871 CHECK PCD.ON_OFF_ENA_DIS =ENA
1872 #
1873 PCD.PSU_MEA1_ON
1874 WAIT_SEC 40
1875 #
1876 #
1877 # < CHECK >
1878 CHECK PCD.PSU_MEA1_ON_OFF =ON
1879 #
1880 PCD.ON_OFF_DIS
1881 WAIT_SEC 40
1882 #
1883 # < CHECK >
1884 CHECK PCD.ON_OFF_ENA_DIS =DIS
1885 CHECK MD1F.MS1_LINK_STAT =ON
1886 #
1887 # # SW Error clear
1888 MD1F.DPU_SW_ERR_CLR
1889 WAIT_SEC 40
1890 #
1891 # < CHECK >
1892 CHECK MD1F.MS1_LINK_ERR =na
1893 CHECK MD1F.SLOT_ERR =na
1894 #
1895 #
1896 #
1897 WAIT_SEC 300
1898 #
1899 #
1900 # # MEA1 テレメトリ表示の確認(LINK 確立から 8 秒以上経過後に実施)
1901 # Confirmation of the MEA1 telemetry indication
1902 # (More than 8 minutes after from LINK establishment)
1903 #
1904 # < CHECK >
1905 CHECK MEA1.HK_I_P5V <110 mA
1906 CHECK MEA1.HK_I_P12V <17 # mA
1907 CHECK MEA1.HK_MCP 0 # V
1908 CHECK MEA1.I_MCP 0 # uA
1909 CHECK MEA1.HK_TMP_FPG <30 # deg.
1910 CHECK MEA1.HK_TMP_MCP <30 # deg.
1911 CHECK MEA1.Status_reg 0x80 # (initial value)
1912 CHECK MEA1.l1_cmd_exe =0xFFFF
1913 CHECK MEA1.l2_cmd_exe =0xFFFF
1914 #
1915 #
1916 # QL Check-Item Spec Check
1917 # -----+-----+-----+-----
1918 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1919 #
1920 # -----

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1921 . # 3.1.2. MEM_DUMP
1922 # <<M15-2 Cal Check Duration:40 min Day:35>>
1923 #
1924 # CHECK ALL HV MEMORY TABLE in EEPROM to prevent errors, "corruption, delete..."
1925 #
1926 # -----
1927 #
1928 # # HV Tableの確認 HV Table Verification
1929 # # MDP send a CMD MEA DUMP memory 0x2F 0x04 : HV Table 3eV -> 3KeV
1930 # (transfert between EEPROM HV Table (n∞4) to SRAM Spacewire before
1931 # the MDP RMAP dump packet)
1932 #
1933 . MEA1.Scan_OF
1934 WAIT_SEC 40
1935 #
1936 . MD1F.MEA1_CLCT_STOP
1937 WAIT_SEC 40
1938 #
1939 # < CHECK >
1940 CHECK MD1F.MS1_DAT_CLCT =OFF
1941 #
1942 #
1943 # DUMP 1st HV table : Table 3eV-300eV
1944 # 1-Send Command to read EEPROM and transfer HV table in RAM1008
1945 #
1946 . MEA1.Dump_EEP 0x00
1947 WAIT_SEC 40
1948 #
1949 #
1950 # < CHECK >
1951 CHECK MEA1.I1_cmd_exe =0x2F00
1952 . #
1953 . DH.OPE_MODE 0x0C 0x3E 0x2C 0x04 0x0000000000000000000000000000
1954 DH.DMP_PERIOD 0x02
1955 WAIT_SEC 40
1956 CHECK DH.DMP_PERIOD =SEC_4
1957 CHECK DH.CATEGORY_TBL_NO 0x2C
1958 CHECK DH.DWNLNK_RATE =BPS16384
1959 #
1960 #
1961 . DH.TLMFMT1553B_CHG 0x06
1962 WAIT_SEC 40
1963 #
1964 # < CHECK >
1965 CHECK DH.TLM1553BFMTNO 0x06
1966 #
1967 # # MDP Read DUMP memory 1st packet 512Bytes
1968 # MDP から MEA1 ROM を 512byte DUMP
1969 #
1970 # 2-Dump RAM to MDP (Adresse : 0x600 and size=512B
1971 #
1972 . MD1F.MEM_DUMP 0x01 0x05 0x00000600 0x000200

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1973 WAIT_SEC 300
1974 #
1975 . # # MEA 側にてダンプ結果を確認
1976 #
1977 # 3-Check the 1st HV Table
1978 #
1979 #
1980 # DUMP 2nd HV table : Table 3eV-25KeV
1981 # 1-Send Commant to read EEPROM and transfert HV table in RAM
1982 #
1983 . MEA1.Dump_EEP 0x01
1984 WAIT_SEC 40
1985 #
1986 . DH.TLMFMT1553B_CHG 0x0A
1987 WAIT_SEC 40
1988 #
1989 # < CHECK >
1990 CHECK DH.TLM1553BFMTNO 0x0A
1991 CHECK MEA1.l1_cmd_exe =0x2F01
1992 CHECK MEA1.l2_cmd_exe =0x2F00
1993 #
1994 . DH.TLMFMT1553B_CHG 0x06
1995 WAIT_SEC 40
1996 #
1997 # < CHECK >
1998 CHECK DH.TLM1553BFMTNO 0x06
1999 #
2000 # # MDP Read DUMP memory 1st packet 512Bytes
2001 # MDP から MEA1 ROM を 512byte DUMP
2002 #
2003 # 2-Dump RAM to MDP (Adresse : 0x600 and size=512B
2004 # => need 2 times (1st time is not a good packet, 2nd time is OK)
2005 #
2006 . MD1F.MEM_DUMP 0x01 0x05 0x00000600 0x000200
2007 . MD1F.MEM_DUMP 0x01 0x05 0x00000600 0x000200
2008 WAIT_SEC 300
2009 #
2010 . # # MEA 側にてダンプ結果を確認
2011 #
2012 # 3-Check the 2nd HV Table
2013 #
2014 #
2015 #
2016 #
2017 # DUMP 3rd HV table : Table 3eV-3KeV
2018 # 1-Send Commant to read EEPROM and transfert HV table in RAM
2019 #
2020 . MEA1.Dump_EEP 0x02
2021 WAIT_SEC 40
2022 #
2023 . DH.TLMFMT1553B_CHG 0x0A
2024 WAIT_SEC 40

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2025 #
2026 # < CHECK >
2027 CHECK DH. TLM1553BFMTNO 0x0A
2028 CHECK MEA1. I1_cmd_exe =0x2F02
2029 CHECK MEA1. I2_cmd_exe =0x2F01
2030 #
2031 . DH. TLMFMT1553B_CHG 0x06
2032 WAIT_SEC 40
2033 #
2034 # < CHECK >
2035 CHECK DH. TLM1553BFMTNO 0x06
2036 #
2037 # # MDP Read DUMP memory 1st packet 512Bytes
2038 # MDP から MEA1 ROM を 512byte DUMP
2039 #
2040 # 2-Dump RAM to MDP (Adresse : 0x600 and size=512B
2041 # => need 2 times (1st time is not a good packet, 2nd time is OK)
2042 #
2043 . MD1F.MEM_DUMP 0x01 0x05 0x00000600 0x000200
2044 . MD1F.MEM_DUMP 0x01 0x05 0x00000600 0x000200
2045 WAIT_SEC 300
2046 #
2047 . # # MEA 側にてダンプ結果を確認
2048 #
2049 # 3-Check the 3rd HV Table
2050 #
2051 #
2052 #
2053 #
2054 # DUMP 4th HV table : Table 3KeV-25KeV
2055 # 1-Send Commant to read EEPROM and transfert HV table in RAM
2056 #
2057 . MEA1.Dump_EEP 0x03
2058 WAIT_SEC 40
2059 #
2060 . DH. TLMFMT1553B_CHG 0x0A
2061 WAIT_SEC 40
2062 #
2063 # < CHECK >
2064 CHECK DH. TLM1553BFMTNO 0x0A
2065 CHECK MEA1. I1_cmd_exe =0x2F03
2066 CHECK MEA1. I2_cmd_exe =0x2F02
2067 #
2068 . DH. TLMFMT1553B_CHG 0x06
2069 WAIT_SEC 40
2070 #
2071 # < CHECK >
2072 CHECK DH. TLM1553BFMTNO 0x06
2073 #
2074 # # MDP Read DUMP memory 1st packet 512Bytes
2075 # MDP から MEA1 ROM を 512byte DUMP
2076 #

```

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2077 # 2-Dump RAM to MDP (Adresse : 0x600 and size=512B
2078 # => need 2 times (1st time is not a good packet, 2nd time is OK)
2079 #
2080 . MD1F.MEM_DUMP 0x01 0x05 0x00000600 0x000200
2081 . MD1F.MEM_DUMP 0x01 0x05 0x00000600 0x000200
2082 WAIT_SEC 300
2083 #
2084 . # # MEA 側にてダンプ結果を確認
2085 #
2086 # 3-Check the 4th HV Table
2087 #
2088 #
2089 . MD1F.MEA1_CLCT_START
2090 #
2091 WAIT_SEC 40
2092 #
2093 # < CHECK >
2094 CHECK MD1F.MS1_DAT_CLCT =ON
2095 #
2096 #
2097 . # # Mission Data を 1553B 通信の半分の帯域 (約 4kbps)で出力するモードとする
2098 . DH.MC_EACH_EXE 0x0036 # 1553B TLM mode 10 (SYS HK 1/16, BUS User HK 1/8, DUMP 1/16,
2099 # # MDP User HK 1/8, Mission User HK 1/8, Mission Data 1/2)
2100 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
2101 #: 0x00 DH.DMP_PERIOD 0x04
2102 #: 0x01 DH.DR_PTLOGCYC_SET 0x00
2103 #: 0x02 ACS.ACS_PKT_FMT_1
2104 #: 0x05 DH.OPE_MODE 0x0C 0x3E 0x2C 0x04 0x00000000000000000000000000000000
2105 #: 0x06 DH.TLMFMT1553B_CHG 0x0A
2106 #
2107 WAIT_SEC 40
2108 #
2109 # < CHECK >
2110 CHECK DH.DMP_PERIOD =SEC_16
2111 CHECK DH.CATEGORY_TBL_NO 0x2C
2112 CHECK DH.DWNLNK_RATE =BPS16384
2113 CHECK DH.TLM1553BFMTNO 0x0A
2114 CHECK ACS.F_ACS_PKT_FMT =ACSHK1
2115 #
2116 #
2117 # QL Check-Item Spec Check
2118 # -----+-----+-----+-----
2119 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2120 #
2121 #
2122 # -----
2123 . # 3.1.3. SWITCH ON SCAN, 10 min
2124 #
2125 # Start the scan of the sphere and test mode in order to verify the noise on A111
2126 #
2127 # -----
2128 #

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2129 # Start the scan and verify if the instrument is synchronized or not ?
2130 #
2131 . MEA1.Scan_ON
2132 WAIT_SEC 300
2133 #
2134 . # <CHECK>
2135 CHECK MEA1.l1_cmd_exe =0x2500
2136 #
2137 # Select the sun pulse synchronization
2138 . MEA1.Md_S_Auto
2139 WAIT_SEC 40
2140 #
2141 # <CHECK>
2142 CHECK MEA1.l1_cmd_exe =0x3100
2143 #
2144 # Select energies mode : mode 32 energies/32 sweeps
2145 # -> HV table n° 2, 3eV-3KeV (~300V maxi on the analyzer)"
2146 #
2147 . MEA1.mode_ROM_4 0x02
2148 WAIT_SEC 40
2149 #
2150 # <CHECK>
2151 CHECK MEA1.l1_cmd_exe =0x0302
2152 #
2153 #
2154 WAIT_SEC 220
2155 #
2156 . # <CHECK>
2157 # Check HKP TmSStp = HKP TmSStp_dft
2158 #
2159 ## *****
2160 # WAIT until (5 min maximum) HKP TmSStp = TmSStp_dft to indicate the synchronization
2161 # between sun plus and MEA.
2162 # If not, do not execute the next steps!!"
2163 # Probably, sun pulse signal have a problem (cf previous test at ISAS)!!"
2164 # If already executed, you can pass at the next step."
2165 ## *****
2166 #
2167 #
2168 # QL          Check-Item          Spec          Check
2169 # -----+-----+-----+-----+
2170 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2171 #
2172 #
2173 # -----
2174 . # 3.1.4. TEST MODE ON,15 min
2175 # -----
2176 #
2177 #
2178 # Swtich ON the test mode for the Amplifier A111
2179 # to verify the good working of this
2180 . MEA1.Md_Tst_ON

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2181 WAIT_SEC 300
2182 #
2183 # <CHECK>
2184 CHECK MEA1.l1_cmd_exe =0x3900
2185 CHECK MEA1.Status_reg 0x81 # (bit n° 0 = 1)
2186 #
2187 # - wait 15 min to verify the Science Data
2188 # Check the science Data and compare with the previous value
2189 #
2190 #
2191 # QL          Check-Item          Spec          Check
2192 # -----+-----+-----+-----+
2193 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2194 #
2195 #
2196 # -----+-----+-----+-----+
2197 # 3.1.5. TEST MODE OFF, 10 min
2198 # -----+-----+-----+-----+
2199 #
2200 #
2201 # Stop the A111 test Mode
2202 MEA1.Md_Tst_OF
2203 WAIT_SEC 600
2204 #
2205 # <CHECK>
2206 CHECK MEA1.l1_cmd_exe =0x3800
2207 CHECK MEA1.Status_reg 0x80 # (bit n° 0 = 0)
2208 #
2209 # - wait 10 min to verify the Science Data are return to zero
2210 # Check the science Data and compare with the previous value
2211 #
2212 #
2213 # QL          Check-Item          Spec          Check
2214 # -----+-----+-----+-----+
2215 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2216 #
2217 #
2218 # -----+-----+-----+-----+
2219 # 3.1.6. SWITCH OFF the SCAN (5 minutes)
2220 # -----+-----+-----+-----+
2221 #
2222 #
2223 # SWITCH OFF THE SCAN
2224 MEA1.Scan_OF
2225 WAIT_SEC 300
2226 #
2227 # <CHECK>
2228 CHECK MEA1.l1_cmd_exe =0x2400
2229 #
2230 #
2231 # QL          Check-Item          Spec          Check
2232 # -----+-----+-----+-----+

```

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2233 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2234 #
2235 # =====
2236 # 3.2. MEA1 SHUTDOWN
2237 # =====
2238 #
2239 # PCD.ON_OFF_ENA
2240 # WAIT_SEC 40
2241 #
2242 # < CHECK >
2243 # CHECK PCD.ON_OFF_ENA_DIS =ENA
2244 #
2245 # MD1F.MEA1_CLCT_STOP
2246 # WAIT_SEC 40
2247 #
2248 # < CHECK >
2249 # CHECK MD1F.MS1_DAT_CLCT =OFF
2250 #
2251 # PCD.PSU_MEA1_OFF
2252 # WAIT_SEC 40
2253 #
2254 # < CHECK >
2255 # CHECK PCD.PSU_MEA1_ON_OFF =OFF
2256 # CHECK MD1F.MS1_LINK_STAT =OFF
2257 #
2258 # PCD.ON_OFF_DIS
2259 # WAIT_SEC 40
2260 #
2261 # < CHECK >
2262 # CHECK PCD.ON_OFF_ENA_DIS =DIS
2263 #
2264 # QL Check-Item Spec Check
2265 # -----+-----+-----+-----
2266 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2267 #
2268 #
2269 # *****
2270 # 4. MEA2 初期 CO
2271 # *****
2272 # =====
2273 # 4.1. MEA2 function check
2274 # =====
2275 # -----
2276 # 4.1.1. MEA2 Startup
2277 # <<M16-1 Initial Power ON Duration:10 min Day:35>>
2278 # -----
2279 #
2280 # # MEA2 の電源を ON する。
2281 # Power on MEA2 by switching on.
2282 #
2283 # PCD.ON_OFF_ENA
2284 # WAIT_SEC 40

```

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2285 #
2286 # < CHECK >
2287 CHECK PCD.ON_OFF_ENA_DIS =ENA
2288 #
2289 . PCD.PSU_MEA2_ON
2290 WAIT_SEC 40
2291 #
2292 #
2293 # < CHECK >
2294 CHECK PCD.PSU_MEA2_ON_OFF =ON
2295 #
2296 . PCD.ON_OFF_DIS
2297 WAIT_SEC 40
2298 #
2299 # < CHECK >
2300 CHECK PCD.ON_OFF_ENA_DIS =DIS
2301 CHECK MD1F.MS2_LINK_STAT =ON
2302 #
2303 # # SW Error clear
2304 . MD1F.DPU_SW_ERR_CLR
2305 WAIT_SEC 40
2306 #
2307 # < CHECK >
2308 CHECK MD1F.MS2_LINK_ERR =na
2309 CHECK MD1F.SLOT_ERR =na
2310 #
2311 #
2312 #
2313 WAIT_SEC 560
2314 #
2315 #
2316 # # MEA2 テレメトリ表示の確認(LINK 確立から 8 分以上経過後に実施)
2317 # Confirmation of the MEA2 telemetry indication
2318 # (More than 8 minutes after from LINK establishment)
2319 #
2320 . # < CHECK >
2321 CHECK MEA2.HK_I_P5V <110 mA
2322 CHECK MEA2.HK_I_P12V <17 # mA
2323 CHECK MEA2.HK_MCP 0 # V
2324 CHECK MEA2.I_MCP 0 # uA
2325 CHECK MEA2.HK_TMP_FPG <30 # deg. (depend on the environment,
maximum 85° C)
2326 CHECK MEA2.HK_TMP_MCP <30 # deg. (depend on the environment,
maximum 80° C)
2327 CHECK MEA2.Status_reg 0x80 # (initial value)
2328 CHECK MEA2.l1_cmd_exe =0xFFFF
2329 CHECK MEA2.l2_cmd_exe =0xFFFF
2330 #
2331 #
2332 #
2333 # QL Check-Item Spec Check
2334 # -----+-----+-----+-----

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2335 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2336 #
2337 #
2338 # -----
2339 # 4.1.2. MEM_DUMP
2340 # <<M16-2 Cal Check Duration:40 min Day:35>>
2341 #
2342 # CHECK ALL HV MEMORY TABLE in EEPROM to prevent errors, "corruption, delete..."
2343 #
2344 # -----
2345 #
2346 # # HV Table の確認 HV Table Verification
2347 # # MDP send a CMD MEA DUMP memory 0x2F 0x04 : HV Table 3eV -> 3KeV
2348 # (transfert between EEPROM HV Table (n∞4) to SRAM Spacewire before
2349 # the MDP RMAP dump packet)
2350 #
2351 # MEA2.Scan_OF
2352 # WAIT_SEC 40
2353 #
2354 # MD1F.MEA2_CLCT_STOP
2355 # WAIT_SEC 40
2356 #
2357 # < CHECK >
2358 # CHECK MD1F.MS2_DAT_CLCT =OFF
2359 #
2360 #
2361 #
2362 # DUMP 1st HV table : Table 3eV-300eV
2363 # 1-Send Command to read EEPROM and transfer HV table in RAM1008
2364 #
2365 # MEA2.Dump_EEP 0x00
2366 # WAIT_SEC 40
2367 #
2368 #
2369 # DH.OPE_MODE 0x0C 0x3E 0x2C 0x04 0x000000000000000000000000
2370 # DH.DMP_PERIOD 0x02
2371 # WAIT_SEC 40
2372 # CHECK DH.DMP_PERIOD =SEC_4
2373 # CHECK DH.CATEGORY_TBL_NO 0x2C
2374 # CHECK DH.DWNLNK_RATE =BPS16384
2375 #
2376 #
2377 # DH.TLMFMT1553B_CHG 0x06
2378 # WAIT_SEC 40
2379 #
2380 # < CHECK >
2381 # CHECK DH.TLM1553BFMTNO 0x06
2382 #
2383 # # MDP Read DUMP memory 1st packet 512Bytes
2384 # MDP から MEA2 ROM を 512byte DUMP
2385 #
2386 # 2-Dump RAM to MDP (Adresse : 0x600 and size=512B

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2387 #
2388 . MD1F.MEM_DUMP 0x01 0x06 0x00000600 0x000200
2389 WAIT_SEC 300
2390 #
2391 . # # MEA 側にてダンプ結果を確認
2392 #
2393 # 3-Check the 1st HV Table
2394 #
2395 #
2396 #
2397 #
2398 # DUMP 2nd HV table : Table 3eV-25KeV
2399 # 1-Send Commant to read EEPROM and transfert HV table in RAM
2400 #
2401 . MEA2.Dump_EEP 0x01
2402 WAIT_SEC 40
2403 #
2404 . DH.TLMFMT1553B_CHG 0x0A
2405 WAIT_SEC 40
2406 #
2407 # < CHECK >
2408 CHECK DH.TLM1553BFMTNO 0x0A
2409 CHECK MEA2.I1_cmd_exe =0x2F01
2410 CHECK MEA2.I2_cmd_exe =0x2F00
2411 #
2412 . DH.TLMFMT1553B_CHG 0x06
2413 WAIT_SEC 40
2414 #
2415 # < CHECK >
2416 CHECK DH.TLM1553BFMTNO 0x06
2417 #
2418 # # MDP Read DUMP memory 1st packet 512Bytes
2419 # MDP から MEA2 ROM を 512byte DUMP
2420 #
2421 # 2-Dump RAM to MDP (Adresse : 0x600 and size=512B
2422 # => need 2 times (1st time is not a good packet, 2nd time is OK)
2423 #
2424 . MD1F.MEM_DUMP 0x01 0x06 0x00000600 0x000200
2425 . MD1F.MEM_DUMP 0x01 0x06 0x00000600 0x000200
2426 WAIT_SEC 300
2427 #
2428 . # # MEA 側にてダンプ結果を確認
2429 #
2430 # 3-Check the 2nd HV Table
2431 #
2432 #
2433 #
2434 #
2435 # DUMP 3rd HV table : Table 3eV-3KeV
2436 # 1-Send Commant to read EEPROM and transfert HV table in RAM
2437 #
2438 . MEA2.Dump_EEP 0x02

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2439 WAIT_SEC 40
2440 #
2441 . DH. TLMFMT1553B_CHG 0x0A
2442 WAIT_SEC 40
2443 #
2444 # < CHECK >
2445 CHECK DH. TLM1553BFMTNO 0x0A
2446 CHECK MEA2. l1_cmd_exe =0x2F02
2447 CHECK MEA2. l2_cmd_exe =0x2F01
2448 #
2449 . DH. TLMFMT1553B_CHG 0x06
2450 WAIT_SEC 40
2451 #
2452 # < CHECK >
2453 CHECK DH. TLM1553BFMTNO 0x06
2454 #
2455 # # MDP Read DUMP memory 1st packet 512Bytes
2456 # MDP から MEA2 ROM を 512byte DUMP
2457 #
2458 # 2-Dump RAM to MDP (Adresse : 0x600 and size=512B
2459 # => need 2 times (1st time is not a good packet, 2nd time is OK)
2460 #
2461 . MD1F.MEM_DUMP 0x01 0x06 0x00000600 0x000200
2462 . MD1F.MEM_DUMP 0x01 0x06 0x00000600 0x000200
2463 WAIT_SEC 300
2464 #
2465 . # # MEA 側にてダンプ結果を確認
2466 #
2467 # 3-Check the 3rd HV Table
2468 #
2469 #
2470 #
2471 #
2472 # DUMP 4th HV table : Table 3KeV-25KeV
2473 # 1-Send Commant to read EEPROM and transfert HV table in RAM
2474 #
2475 . MEA2. Dump_EEP 0x03
2476 WAIT_SEC 40
2477 #
2478 . DH. TLMFMT1553B_CHG 0x0A
2479 WAIT_SEC 40
2480 #
2481 # < CHECK >
2482 CHECK DH. TLM1553BFMTNO 0x0A
2483 CHECK MEA2. l1_cmd_exe =0x2F03
2484 CHECK MEA2. l2_cmd_exe =0x2F02
2485 #
2486 . DH. TLMFMT1553B_CHG 0x06
2487 WAIT_SEC 40
2488 #
2489 # # MDP Read DUMP memory 1st packet 512Bytes
2490 # MDP から MEA2 ROM を 512byte DUMP

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2491 #
2492 # 2-Dump RAM to MDP (Adresse : 0x600 and size=512B
2493 # => need 2 times (1st time is not a good packet, 2nd time is OK)
2494 #
2495 . MD1F.MEM_DUMP 0x01 0x06 0x00000600 0x000200
2496 . MD1F.MEM_DUMP 0x01 0x06 0x00000600 0x000200
2497 WAIT_SEC 300
2498 #
2499 . # # MEA 側にてダンプ結果を確認
2500 #
2501 # 3-Check the 4th HV Table
2502 #
2503 #
2504 #
2505 #
2506 . MD1F.MEA2_CLCT_START
2507 #
2508 WAIT_SEC 40
2509 #
2510 # < CHECK >
2511 CHECK MD1F.MS2_DAT_CLCT =ON
2512 #
2513 . # # Mission Data を 1553B 通信の半分の帯域 (約 4kbps) で出力するモードとする
2514 . DH.MC_EACH_EXE 0x0036 # 1553B TLM mode 10 (SYS HK 1/16, BUS User HK 1/8, DUMP 1/16,
2515 # # MDP User HK 1/8, Mission User HK 1/8, Mission Data 1/2)
2516 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
2517 #: 0x00 DH.DMP_PERIOD 0x04
2518 #: 0x01 DH.DR_PTLOGCYC_SET 0x00
2519 #: 0x02 ACS.ACS_PKT_FMT_1
2520 #: 0x05 DH.OPE_MODE 0x0C 0x3E 0x2C 0x04 0x00000000000000000000000000000000
2521 #: 0x06 DH.TLMFMT1553B_CHG 0x0A
2522 #
2523 WAIT_SEC 40
2524 #
2525 # < CHECK >
2526 CHECK DH.DMP_PERIOD =SEC_16
2527 CHECK DH.CATEGORY_TBL_NO 0x2C
2528 CHECK DH.DWNLNK_RATE =BPS16384
2529 CHECK DH.TLM1553BFMTNO 0x0A
2530 CHECK ACS.F_ACS_PKT_FMT =ACSHK1
2531 #
2532 #
2533 # QL Check-Item Spec Check
2534 # -----+-----+-----+-----
2535 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2536 #
2537 #
2538 # -----
2539 . # 4.1.3. SWITCH ON SCAN, 10 min
2540 #
2541 # Start the scan of the sphere and test mode to verify the noise on A111
2542 #

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2543 # -----
2544 #
2545 # Start the scan and verify if the instrument is synchronized or not ?
2546 #
2547 . MEA2.Scan_ON
2548 WAIT_SEC 300
2549 #
2550 . # <CHECK>
2551 CHECK MEA2.l1_cmd_exe =0x2500
2552 #
2553 # Select the sun pulse synchronization
2554 . MEA2.Md_S_Auto
2555 WAIT_SEC 40
2556 #
2557 # <CHECK>
2558 CHECK MEA2.l1_cmd_exe =0x3100
2559 #
2560 # Select energies mode : mode 32 energies/32 sweeps
2561 # -> HV table n° 2, 3eV-3KeV (~300V maxi on the analyzer)"
2562 #
2563 . MEA2.mode_ROM_4 0x02
2564 WAIT_SEC 40
2565 #
2566 # <CHECK>
2567 CHECK MEA2.l1_cmd_exe =0x0302
2568 #
2569 #
2570 WAIT_SEC 220
2571 #
2572 . # <CHECK>
2573 . # Check HKP TmSStp = HKP TmSStp_dft
2574 #
2575 ## *****
2576 # WAIT until (5 min maximum) HKP TmSStp = TmSStp_dft to indicate the synchronization
2577 # between sun plus and MEA.
2578 # If not, do not execute the next steps!!"
2579 # Probably, sun pulse signal have a problem (cf previous test at ISAS)!!"
2580 # If already executed, you can pass at the next step."
2581 ## *****
2582 #
2583 #
2584 # QL          Check-Item          Spec          Check
2585 # -----+-----+-----+-----
2586 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2587 #
2588 #
2589 # -----
2590 . # 4.1.4. TEST MODE ON,15 min
2591 # -----
2592 #
2593 #
2594 # Swtich ON the test mode for the Amplifier A111

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2595 # to verify the good working of this
2596 . MEA2.Md_Tst_ON
2597 WAIT_SEC 300
2598 #
2599 . # <CHECK>
2600 CHECK MEA2.I1_cmd_exe =0x3900
2601 CHECK MEA2.Status_reg 0x81 # (bit n° 0 = 1)
2602 #
2603 # - wait 15 min to verify the Science Data
2604 # Check the science Data and compare with the previous value
2605 #
2606 #
2607 #
2608 # QL Check-Item Spec Check
2609 # -----+-----+-----+-----
2610 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2611 #
2612 #
2613 # -----
2614 . # 4.1.5. TEST MODE OFF, 10 min
2615 # -----
2616 #
2617 #
2618 # Stop the A111 test Mode
2619 . MEA2.Md_Tst_OF
2620 WAIT_SEC 600
2621 #
2622 . # <CHECK>
2623 CHECK MEA2.I1_cmd_exe =0x3800
2624 CHECK MEA2.Status_reg 0x80 # (bit n° 0 = 0)
2625 #
2626 # - wait 10 min to verify the Science Data are return to zero
2627 # Check the science Data and compare with the previous value
2628 #
2629 #
2630 # QL Check-Item Spec Check
2631 # -----+-----+-----+-----
2632 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2633 #
2634 #
2635 # -----
2636 . # 4.1.6. SWITCH OFF the SCAN (5 minutes)
2637 # -----
2638 #
2639 #
2640 # SWITCH OFF THE SCAN
2641 . MEA2.Scan_OF
2642 WAIT_SEC 300
2643 #
2644 . # <CHECK>
2645 CHECK MEA2.I1_cmd_exe =0x2400
2646 #

```

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2647 #
2648 # QL          Check-Item          Spec          Check
2649 # -----+-----+-----+-----+
2650 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2651 #
2652 #
2653 # =====
2654 # 4. 2. MEA2 SHUTDOWN
2655 # =====
2656 #
2657 # PCD.ON_OFF_ENA
2658 # WAIT_SEC 40
2659 #
2660 # < CHECK >
2661 # CHECK PCD.ON_OFF_ENA_DIS          =ENA
2662 #
2663 # MD1F.MEA2_CLCT_STOP
2664 # WAIT_SEC 40
2665 #
2666 # < CHECK >
2667 # CHECK MD1F.MS2_DAT_CLCT          =OFF
2668 #
2669 # PCD.PSU_MEA2_OFF
2670 # WAIT_SEC 40
2671 #
2672 # < CHECK >
2673 # CHECK PCD.PSU_MEA2_ON_OFF          =OFF
2674 # CHECK MD1F.MS2_LINK_STAT          =OFF
2675 #
2676 # PCD.ON_OFF_DIS
2677 # WAIT_SEC 40
2678 #
2679 # < CHECK >
2680 # CHECK PCD.ON_OFF_ENA_DIS          =DIS
2681 #
2682 #
2683 #
2684 # QL          Check-Item          Spec          Check
2685 # -----+-----+-----+-----+
2686 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2687 #
2688 #
2689 # *****
2690 # 5. 立ち下げ手順
2691 # *****
2692 # =====
2693 # 5. 1. MDP 立ち下げ
2694 # =====
2695 #
2696 # <<< MDP-DPU RST/OFF >>>
2697 # DH.MDP_DPU12_CPU_OFF          # DH.MC_EACH_EXE 0x00B1
2698 #:          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)

```

```

2699 #: 0x00 DH. ATMC_EACH_DIS 0x32
2700 #: 0x03 DH. ATMC_EACH_DIS 0x33
2701 #: 0x06 DH. ATMC_EACH_DIS 0x30
2702 #: 0x09 DH. ATMC_EACH_DIS 0x31
2703 #: 0x0A MD1F. CPU_RESET_ENA
2704 #: 0x0B MD1F. CPU_RESET
2705 #: 0x0C MD1F. CPU_OFF
2706 #: 0x0D MD2F. CPU_RESET_ENA
2707 #: 0x0E MD2F. CPU_RESET
2708 #: 0x0F MD2F. CPU_OFF
2709 #
2710 # < CHECK >
2711 CHECK DH. ATMC_ENA_DIS30 =DIS
2712 CHECK DH. ATMC_ENA_DIS31 =DIS
2713 CHECK DH. ATMC_ENA_DIS32 =DIS
2714 CHECK DH. ATMC_ENA_DIS33 =DIS
2715 #
2716 # QL Check-Item Spec Check
2717 # -----+-----+-----+-----
2718 # MD1F_OBS_HK << DPU1 H/W-HK >>
2719 # HW_STATE2 CPU_RST_ENA (short) N/A
2720 # HWCMD_CODE 0x0801 (short) N/A
2721 # HW_STATE2 STANDBY (short) N/A
2722 # HWCMD_CODE 0x0702 (short) N/A
2723 #
2724 # < CHECK >
2725 CHECK MD1F. DPU_HW_STATE2 =CPUOFF_DPUBUS
2726 CHECK MD1F. DPU_CMD_CODE 0x0202
2727 #
2728 # QL Check-Item Spec Check
2729 # -----+-----+-----+-----
2730 # MD2F_OBS_HK << DPU2 H/W-HK >>
2731 # HW_STATE2 CPU_RST_ENA (short) N/A
2732 # HWCMD_CODE 0x0801 (short) N/A
2733 # HW_STATE2 STANDBY (short) N/A
2734 # HWCMD_CODE 0x0702 (short) N/A
2735 #
2736 # < CHECK >
2737 CHECK MD2F. DPU_HW_STATE2 =CPUOFF_DPUBUS
2738 CHECK MD2F. DPU_CMD_CODE 0x0202
2739 #
2740 DH. MC_EACH_EXE 0x002C # 1553B TLM mode 0 (SYS HK 1)
2741 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
2742 #: 0x00 DH. DMP_PERIOD 0x04
2743 #: 0x04 ACS. ACS_PKT_FMT_1
2744 #: 0x05 DH. OPE_MODE 0x0C 0x0A 0x00 0x00 0x00000000000000000000000000000000
2745 #: 0x06 DH. TLMFMT1553B_CHG 0x00
2746 #
2747 # <CHECK>
2748 CHECK DH. DMP_PERIOD =SEC_16
2749 CHECK DH. CATEGORY_TBL_NO 0x00
2750 CHECK DH. DWNLNK_RATE =BPS16384

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2751 CHECK DH. TLM1553BFMTNO 0x00
2752 #
2753 . # <<< MDP-DPU1/2 Power OFF >>>
2754 . DH. MDP_DPU12_OFF # DH. MC_EACH_EXE 0x00B0
2755 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
2756 #: 0x00 DH. ATMC_EACH_DIS 0x34
2757 #: 0x01 DH. SPW_RTEFDIR_SET 0x18
2758 #: 0x03 DH. ATMC_EACH_DIS 0x36
2759 #: 0x06 DH. ATMC_EACH_DIS 0x32
2760 #: 0x09 DH. ATMC_EACH_DIS 0x33
2761 #: 0x0A PCD. ON_OFF_ENA
2762 #: 0x0B PCD. PSU_MDP1_OFF
2763 #: 0x0C DH. ATMC_EACH_DIS 0x30
2764 #: 0x0D PCD. PSU_MDP2_OFF
2765 #: 0x0E PCD. ON_OFF_DIS
2766 #: 0x0F DH. ATMC_EACH_DIS 0x31
2767 #
2768 . # < CHECK >
2769 # CHECK DH. ATMC_ENA_DIS30 =DIS
2770 # CHECK DH. ATMC_ENA_DIS31 =DIS
2771 # CHECK DH. ATMC_ENA_DIS32 =DIS
2772 # CHECK DH. ATMC_ENA_DIS33 =DIS
2773 # CHECK DH. ATMC_ENA_DIS34 =DIS
2774 # CHECK DH. ATMC_ENA_DIS36 =DIS
2775 CHECK DH. SPW_FDIR_ENA_MDP1 =DIS
2776 CHECK DH. SPW_FDIR_ENA_MDP2 =DIS
2777 CHECK DH. SPW_FDIR_ENA_PCD =ENA
2778 CHECK DH. SPW_FDIR_ENA_RMTRTR =ENA
2779 CHECK PCD. PSU_MDP1_ON_OFF =OFF
2780 CHECK PCD. PSU_MDP2_ON_OFF =OFF
2781 CHECK PCD. ON_OFF_ENA_DIS =DIS
2782 #
2783 . # QL Check-Item Spec Check
2784 # -----+-----+-----+-----
2785 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2786 #
2787 # =====
2788 . # 5.2. TCS 立ち下げ (HTR CTRL OFF/DRU_OFF)
2789 # =====
2790 . #
2791 . DH. MC_EACH_EXE 0x0032 # 1553B TLM mode 6 (SYS HK 1/2, BUS User HK 1/4, DUMP 1/4)
2792 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
2793 #: 0x00 DH. DMP_PERIOD 0x02
2794 #: 0x02 ACS. ACS_PKT_FMT_1
2795 #: 0x05 DH. OPE_MODE 0x0C 0x26 0x28 0x03 0x00000000000000000000000000000000
2796 #: 0x06 DH. TLMFMT1553B_CHG 0x06
2797 #
2798 WAIT_SEC 40
2799 CHECK DH. DMP_PERIOD =SEC_4
2800 CHECK DH. CATEGORY_TBL_NO 0x28
2801 CHECK DH. DWNLNK_RATE =BPS16384
2802 CHECK DH. TLM1553BFMTNO 0x06

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2803 CHECK ACS.F_ACS_PKT_FMT =ACSHK1
2804 #
2805 . # < CHECK >
2806 CHECK DH.ATMC_ENA_DIS30 =DIS
2807 CHECK DH.ATMC_ENA_DIS31 =DIS
2808 CHECK DH.ATMC_ENA_DIS32 =DIS
2809 CHECK DH.ATMC_ENA_DIS33 =DIS
2810 CHECK DH.ATMC_ENA_DIS34 =DIS
2811 CHECK DH.ATMC_ENA_DIS35 =DIS
2812 CHECK DH.ATMC_ENA_DIS36 =DIS
2813 CHECK DH.ATMC_ENA_DIS37 =DIS
2814 . #
2815 . DH.ATMC_EACH_DIS 0x09 # //分離前 CPU リセット時 TCS モード再度 NMNL 制御
2816 #
2817 . # < CHECK >
2818 CHECK DH.ATMC_ENA_DIS09 =DIS
2819 . #
2820 . TCS.MODE_STBY
2821 #
2822 . # < CHECK >
2823 CHECK TCS.MODE_ST =STBY
2824 . #
2825 . TCS.CTRL_HTR_P1_DIS
2826 WAIT_SEC 2
2827 TCS.CTRL_HTR_P2_DIS
2828 WAIT_SEC 2
2829 TCS.CTRL_HTR_P3_DIS
2830 WAIT_SEC 2
2831 TCS.CTRL_HTR_P4_DIS
2832 #
2833 . # < CHECK >
2834 CHECK TCS.HTR01_P_VOL_ST =OFF
2835 CHECK TCS.HTR09_P_VOL_ST =OFF
2836 CHECK TCS.HTR17_P_VOL_ST =OFF
2837 CHECK TCS.HTR25_P_VOL_ST =OFF
2838 . #
2839 . ACS.DRU_SEL_NA
2840 #
2841 . # < CHECK >
2842 CHECK ACS.DRU_SEL_PT =NA
2843 CHECK ACS_SYS.DRU_SEL_PT =NA
2844 CHECK ACS.BIT_DRU_SEL_PT =NA
2845 . #
2846 . PCD.ON_OFF_ENA
2847 #
2848 . # < CHECK >
2849 CHECK PCD.ON_OFF_ENA_DIS =ENA
2850 . #
2851 . PCD.DRU_PSU_A_OFF
2852 #
2853 . # < CHECK >
2854 CHECK PCD.DRU_PSU_A_ON_OFF =OFF

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2855 . #
2856 . PCD.ON_OFF_DIS
2857 . #
2858 . # < CHECK >
2859 . CHECK PCD.ON_OFF_ENA_DIS =DIS
2860 . #
2861 . # QL          Check-Item          Spec          Check
2862 . # -----+-----+-----+-----
2863 . # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2864 . #
2865 . # =====
2866 . # 5.3. MC_DIS
2867 . # =====
2868 . #
2869 . # << PCS 系個別 ATMC DIS >>
2870 . #
2871 . DH.ATMC_EACH_DIS 0x01 # // BAT UVC in Cruise mode
2872 . DH.ATMC_EACH_DIS 0x2B # //PCD NIC リセットモニタ
2873 . #
2874 . # < CHECK >
2875 . CHECK DH.ATMC_ENA_DIS01 =DIS
2876 . CHECK DH.ATMC_ENA_DIS2B =DIS
2877 . #
2878 . #
2879 . DH.ATMC_DIS
2880 . #
2881 . # < CHECK >
2882 . CHECK DH.ATMC_ENA_DIS =DIS
2883 . #
2884 . DH.RQMC_DIS
2885 . #
2886 . # < CHECK >
2887 . CHECK DH.RQMC_ENA_DIS =DIS
2888 . #
2889 . DH.SYSTMR_DIS
2890 . #
2891 . # < CHECK >
2892 . CHECK DH.SYSTMR_ENA_DIS =DIS
2893 . #
2894 . DH.ALL_MC_STOP
2895 . #
2896 . # < CHECK >
2897 . CHECK DH.MC_ENA_DIS =DIS
2898 . #
2899 . # QL          Check-Item          Spec          Check
2900 . # -----+-----+-----+-----
2901 . # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2902 . #
2903 . # =====
2904 . # 5.4. 衛星立ち下げ
2905 . # =====
2906 . #

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| 2907 | # QL                        | Check-Item                                        | Spec                               | Check |
|------|-----------------------------|---------------------------------------------------|------------------------------------|-------|
| 2908 | #                           |                                                   |                                    |       |
| 2909 | # PCS1                      | PCD. CHG_LR1_ON_OFF                               | OFF                                | G N   |
| 2910 | #                           | PCD. CHG_LR2_ON_OFF                               | OFF                                | G N   |
| 2911 | #                           | DMC-PSU-A, -B, XTRP-A, B, SV-Heater 以外が OFF であること |                                    | G N   |
| 2912 | # TCIU                      | DH. XTRP_A_TX_ON_OFF                              | OFF                                | G N   |
| 2913 | #                           | DH. XTRP_B_TX_ON_OFF                              | OFF                                | G N   |
| 2914 | # 局管制                       | Uplink                                            | OFF                                | G N   |
| 2915 | #                           |                                                   |                                    |       |
| 2916 | #                           |                                                   |                                    |       |
| 2917 | # 【確認】                      |                                                   |                                    | Check |
| 2918 | # BUS-V > BAT-V であることを確認する。 |                                                   |                                    | G N   |
| 2919 | #                           |                                                   |                                    |       |
| 2920 | #                           | PCD. INT_EXT_ENA                                  |                                    |       |
| 2921 | #                           | PCD. BAT_EXT                                      |                                    |       |
| 2922 | #                           |                                                   |                                    |       |
| 2923 | # < CHECK >                 |                                                   |                                    |       |
| 2924 | CHECK                       | PCD. INT_EXT_ENA_DIS                              | =DIS                               |       |
| 2925 | CHECK                       | PCD. BAT_INT_EXT                                  | =EXT                               |       |
| 2926 | #                           |                                                   |                                    |       |
| 2927 | #                           | PCD. CHG_BYPS_DIS                                 |                                    |       |
| 2928 | #                           |                                                   |                                    |       |
| 2929 | # < CHECK >                 |                                                   |                                    |       |
| 2930 | CHECK                       | PCD. CHG_BYPS_ENA_DIS                             | 255                                |       |
| 2931 | CHECK                       | PCD. SA_V                                         | -                                  |       |
| 2932 | CHECK                       | PCD. SA_I                                         | -                                  |       |
| 2933 | CHECK                       | PCD. APR1_I                                       | -                                  |       |
| 2934 | CHECK                       | PCD. APR2_I                                       | -                                  |       |
| 2935 | CHECK                       | PCD. BUS_V                                        | -                                  |       |
| 2936 | CHECK                       | PCD. LOAD_I                                       | -                                  |       |
| 2937 | CHECK                       | PCD. CHG_I                                        | -                                  |       |
| 2938 | CHECK                       | PCD. DCHG_I                                       | -                                  |       |
| 2939 | CHECK                       | PCD. BAT_V                                        | -                                  |       |
| 2940 | CHECK                       | PCD. CEL1_V                                       | -                                  |       |
| 2941 | CHECK                       | PCD. CEL2_V                                       | -                                  |       |
| 2942 | CHECK                       | PCD. CEL3_V                                       | -                                  |       |
| 2943 | CHECK                       | PCD. CEL4_V                                       | -                                  |       |
| 2944 | CHECK                       | PCD. CEL5_V                                       | -                                  |       |
| 2945 | CHECK                       | PCD. CEL6_V                                       | -                                  |       |
| 2946 | CHECK                       | PCD. CEL7_V                                       | -                                  |       |
| 2947 | CHECK                       | PCD. CEL8_V                                       | -                                  |       |
| 2948 | CHECK                       | PCD. CEL9_V                                       | -                                  |       |
| 2949 | CHECK                       | PCD. CEL10_V                                      | -                                  |       |
| 2950 | CHECK                       | PCD. CEL11_V                                      | -                                  |       |
| 2951 | CHECK                       | PCD. CEL12_V                                      | -                                  |       |
| 2952 | CHECK                       | PCD. BAT_CV                                       | -                                  |       |
| 2953 | CHECK                       | PCD. BAT_CV_ON_OFF                                | -                                  |       |
| 2954 | #                           |                                                   |                                    |       |
| 2955 | #                           | PCD. CEL_V_MONI_OFF                               | # 【BAT 管理手順書 (TPP-7S8238-03K) へ記録】 |       |
| 2956 | #                           |                                                   |                                    |       |
| 2957 | # < CHECK >                 |                                                   |                                    |       |
| 2958 | CHECK                       | PCD. CEL_V_MONI_ON_OFF                            | =OFF                               |       |

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2959 CHECK PCD.BAT_CV -
2960 CHECK PCD.BAT_CV_ON_OFF -
2961 #
2962 . # 【MIS コマンド操作】
2963 . # MIS 電源出力 A 系 OFF
2964 . # "SYSTEM Control → PFE → Modules[2] → PRI → ON → YES"
2965 . #
2966 . #
2967 . # main2-7851 に続く
2968 . #
2969 . # *****
2970 . # END OF PROCEDURE
2971 . # *****
```

[illegible]

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0050 . # (9) Enabled Selected Categories ボタンをクリック
0051 . # (ログ取得開始)
0052 . # (10) System Control 画面で“CCI”をクリック
0053 . # (11) Interface Management:
0054 . #     Open Interface
0055 . #     TM Distribution: Enable
0056 . #     TC Handling: Enable
0057 . #     をクリック
0058 . # (12) I/F 変換装置の Operation Log 画面で以下のメッセージを確認する
0059 . #     Socket opened CCS/MIS-EGSE 3000
0060 . #     Socket opened CCS/MIS-EGSE 3001
0061 . # (13) System Control 画面で“MFE”をクリック
0062 . # (14) Bus profile ボタンをクリック
0063 . # (15) Polling Sequence Table Selection and Verification において
0064 . #     ファイル BCMIS_MM0 PST defs\BCMIS_MM0.PST を選択し、Verify ボタンを
0065 . #     クリック → load ボタンをクリック
0066 . # (16) Polling Sequence Table Control において、start ボタンをクリック
0067 . #     (1553B バスへの Sync メッセージ出力開始)
0068 . # (17) System Control 画面で“PFE”をクリック
0069 . # (18) Power Control Bus A の“OFF”をクリック
0070 . #     “OFF”→“ON”になる
0071 . # (19) External Bus Power Supply の、DC VOLTS 表示 = 28.00 を確認する
0072 . # (20) PFE Platform の、Under Voltage の LED=消灯を確認する
0073 . # (21) System Control → DFE → Aquisition Control → Start をクリック
0074 . #
0075 . # 【MIS 操作】 System Control→MFE→Platform→Bus:A 選択→Apply
0076 . #
0077 . # 【操作】
0078 . # 下記手順に従い、MIS にて 1553B バスモニタを開始する。
0079 . # (1)start 1553 BM client アイコンをダブルクリック
0080 . #     Bus Monitor が起動する
0081 . # (2)Main Menu: Mode → On-Line Mode クリック
0082 . # (3)Main Menu: Mode → Start New Acquisition クリック
0083 . #
0084 . # 【局管制 設定】
0085 . # 初期設定          : INIT_MM0_RF. → INIT_MM0_RD.
0086 . #
0087 . # 【衛星管制 設定】
0088 . # 接続 CMD          : ccs-mis-if1
0089 . # 接続 TLM          : ccs-mis-if1
0090 . # プリチェック      : OFF
0091 . # センドベリファイ  : 強制 Type-B
0092 . # サクセスベリファイ: OFF
0093 . # RF/LINE 切換え     : RF
0094 . #
0095 . # 実運用(ESOC からの運用 or MP0 MTL による運用)を想定し、
0096 . # プリチェック OFF、センドベリファイ 強制 Type-B、サクセスベリファイ OFF とする。
0097 . #
0098 . # 【地上装置設定】
0099 . # EXT-PS CV: 90.0V
0100 . #     CC: 6.0A
0101 . # OVP     54.0V

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0102  # UVP      32.0V
0103  #
0104  . # 【共通 QL 設定】
0105  # MPO_RL_X 選択
0106  #
0107  # =====
0108  . # 1.2. 衛星立ち上げ
0109  # =====
0110  # -----
0111  . # 1.2.1. CNV-A MAIN MODE 選択
0112  # -----
0113  #
0114  . # MIS にて “HPC_Command1(MIS 出力 ch37)”を出力し、CNV-A の MODE を“MAIN”へ
0115  # 切り替えること。
0116  #
0117  . # 【MIS コマンド操作】
0118  # “SYSTEM Control → DFE → IO Activation Control → IO Number → 37
0119  # → Activate”
0120  #
0121  . # 【MIS ステータス確認操作】
0122  # “View → TM/TC Data Monitor → FE Data Filter
0123  # → DFE TM Packet をチェック → Data Category List 右クリック
0124  # → Track Latest Entry → Datafield Decode → Acquisition Data
0125  # → Relay Status 参照”
0126  #
0127  . # MIS          Check-Item          Spec          Check
0128  # -----+-----+-----+-----
0129  # Relay Status  CNV-A MAIN (RLY6)      SHORT:32767(dec) 以下      G  N
0130  #                CNV-A HTR  (RLY7)      OPEN :32768(dec) 以上      G  N
0131  #
0132  # -----
0133  . # 1.2.2. CNV-B MAIN MODE 選択
0134  # -----
0135  #
0136  . # MIS にて “HPC_Command3(MIS 出力 ch39)”を出力し、CNV-B の MODE を“MAIN”へ
0137  # 切り替えること。
0138  #
0139  . # 【MIS コマンド操作】
0140  # “SYSTEM Control → DFE → IO Activation Control → IO Number → 39
0141  # → Activate”
0142  #
0143  . # 【MIS ステータス確認操作】
0144  # “View → TM/TC Data Monitor → FE Data Filter
0145  # → DFE TM Packet をチェック → Data Category List 右クリック
0146  # → Track Latest Entry → Datafield Decode → Acquisition Data
0147  # → Relay Status 参照”
0148  #
0149  . # MIS          Check-Item          Spec          Check
0150  # -----+-----+-----+-----
0151  # Relay Status  CNV-B MAIN (RLY13)     SHORT :32767(dec) 以下      G  N
0152  #                CNV-B HTR  (RLY14)     OPEN  :32768(dec) 以上      G  N
0153  #

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0154  # -----
0155  # 1.2.3. 衛星立ち上げ(CNV)
0156  # -----
0157  #
0158  # 【MIS コマンド操作】
0159  # MIS 電源出力 A 系 ON(SV-HTR)
0160  # "SYSTEM Control → PFE → Modules[2] → PRI → ON → YES"
0161  #
0162  # -----
0163  # 1.2.4. CMD/TLM LINK 確認(1553B)
0164  # -----
0165  #
0166  # < CHECK >
0167  CHECK DH. CATEGORY_TBL_NO          0x00
0168  CHECK DH. DWNLNK_RATE              =BPS32768
0169  CHECK DH. ATMC_FMT_ERR              =OK
0170  CHECK DH. ATMC_SUM_STAT             =OK
0171  CHECK DH. MC_FMT_ERR                =OK
0172  CHECK DH. MC_SUM_STAT               =OK
0173  CHECK DH. RQMC_FMT_ERR              =OK
0174  CHECK DH. RQMC_SUM_STAT             =OK
0175  CHECK DH. SYSTM_R_FMT_ERR           =OK
0176  CHECK DH. SYSTM_R_SUM_STAT          =OK
0177  CHECK DH. TL_FMT_ERR                =OK
0178  CHECK DH. TL_SUM_STAT               =OK
0179  CHECK DH. CATGRY_SUM_STAT           =OK
0180  CHECK DH. TM1TBL_SUM_STAT           =OK
0181  CHECK DH. TM2TBL_SUM_STAT           =OK
0182  CHECK DH. ONOFKY_SUMERR             =OK
0183  CHECK DH. CPU_CMD_RESET             =NON
0184  CHECK DH. CPUA_ECC_2BE              =NON
0185  CHECK DH. CPUA_SW_RST               =NON
0186  CHECK DH. CPUA_WDT_OVF              =NON
0187  CHECK DH. CPUB_ECC_2BE              =NON
0188  CHECK DH. CPUB_SW_RST               =NON
0189  CHECK DH. CPUB_WDT_OVF              =NON
0190  CHECK DH. CPU_AB                    =CPU_A
0191  CHECK DH. HK_ACFIS_VALID            =ENA
0192  CHECK DH. HK_DHFS_VALID             =ENA
0193  CHECK DH. HK_SPW_VALID              =ENA
0194  CHECK DH. HK_TCFIS_VALID            =ENA
0195  CHECK DH. NML_IPL                   =NML
0196  CHECK DH. TC1U_ECC_2BE              =NON
0197  CHECK DH. DR_SUM_STAT               =OK
0198  CHECK PCD. APR1_ON_OFF              =OFF
0199  CHECK PCD. APR2_ON_OFF              =ON
0200  #
0201  # < CHECK >
0202  CHECK DH. ENA_TEMP                  -
0203  CHECK DH. TANK_TEMP                 -
0204  CHECK DH. ADM_TEMP                  -
0205  CHECK DH. APM_TEMP                  -

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|------|---------------------------|-------|
| 0206 | CHECK DH. UP_PNL2_TEMP    | -     |
| 0207 | CHECK DH. UP_PNL3_TEMP    | -     |
| 0208 | CHECK DH. UP_PNL6_TEMP    | -     |
| 0209 | CHECK DH. UP_PNL7_TEMP    | -     |
| 0210 | CHECK DH. UP_PNL8_TEMP    | -     |
| 0211 | CHECK DH. LO_PNL1_TEMP    | -     |
| 0212 | CHECK DH. LO_PNL2_TEMP    | -     |
| 0213 | CHECK DH. LO_PNL5_TEMP    | -     |
| 0214 | CHECK DH. LO_PNL6_TEMP    | -     |
| 0215 | CHECK DH. LO_PNL8_TEMP    | -     |
| 0216 | CHECK DH. SSAS_TEMP       | -     |
| 0217 | CHECK DH. THR_T1T2_TEMP   | -     |
| 0218 | CHECK DH. SSC_TEMP1       | -     |
| 0219 | CHECK DH. ACM1_TEMP       | -     |
| 0220 | CHECK DH. THR_T3T4_TEMP   | -     |
| 0221 | CHECK DH. ACM2_TEMP       | -     |
| 0222 | CHECK DH. THR_A1_TEMP     | -     |
| 0223 | CHECK DH. THR_A2_TEMP     | -     |
| 0224 | CHECK DH. VLV_M_TEMP      | -     |
| 0225 | CHECK DH. EPC_A_TEMP      | -     |
| 0226 | CHECK DH. EPC_B_TEMP      | -     |
| 0227 | CHECK DH. TWT_A_TEMP      | -     |
| 0228 | CHECK DH. TWT_B_TEMP      | -     |
| 0229 | CHECK DH. XTRP_A_TEMP     | -     |
| 0230 | CHECK DH. XTRP_B_TEMP     | -     |
| 0231 | CHECK DH. BAT_TEMP1       | -     |
| 0232 | CHECK DH. BAT_TEMP2       | -     |
| 0233 | CHECK DH. SSC_TEMP2       | -     |
| 0234 | CHECK DH. SIDE1_TEMP      | -     |
| 0235 | CHECK DH. SIDE2_TEMP      | -     |
| 0236 | CHECK DH. SIDE3_TEMP      | -     |
| 0237 | CHECK DH. SIDE4_TEMP      | -     |
| 0238 | CHECK DH. UP_PNL1_TEMP    | -     |
| 0239 | CHECK DH. UP_PNL5_TEMP    | -     |
| 0240 | CHECK DH. MAST_MGF_TEMP   | -     |
| 0241 | CHECK DH. LO_PNL3_TEMP    | -     |
| 0242 | CHECK DH. LO_PNL7_TEMP    | -     |
| 0243 | CHECK DH. SIDE5_TEMP      | -     |
| 0244 | CHECK DH. SIDE6_TEMP      | -     |
| 0245 | CHECK DH. SIDE7_TEMP      | -     |
| 0246 | CHECK DH. SIDE8_TEMP      | -     |
| 0247 | . #                       |       |
| 0248 | DH. MC_START              |       |
| 0249 | . #                       |       |
| 0250 | . # < CHECK >             |       |
| 0251 | CHECK DH. MC_ENA_DIS      | =ENA  |
| 0252 | CHECK PCD. ON_OFF_ENA_DIS | =DIS  |
| 0253 | CHECK PCD. BUS_V          | 48 52 |
| 0254 | . #                       |       |
| 0255 | . # 【衛星管制設定】              |       |
| 0256 | . # プリチェック : OFF          |       |
| 0257 | . # センドベリファイ : 強制 TYPE-B  |       |

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0258 . # サクセスベリファイ: OFF
0259 . # COP N(S)値設定
0260 . #
0261 . # QL                      Check-Item                      Spec                      Check
0262 . # -----+-----+-----+-----
0263 . # CMD 出力装置      Message                      STATUS CHECK 結果 OK          G  N
0264 . #
0265 . # -----
0266 . # 1.2.5. BAT INT
0267 . # -----
0268 . #
0269 . PCD. BAT_OT_DIS
0270 . WAIT_SEC 2
0271 . PCD. BAT_OT_ON_SET 0xBD    # 28degC
0272 . WAIT_SEC 2
0273 . DH. BAT_SM_EXE
0274 . WAIT_SEC 2
0275 . PCD. BAT_OT_OFF_SET 0xC5   # 26degC
0276 . WAIT_SEC 2
0277 . DH. BAT_SM_EXE
0278 . WAIT_SEC 2
0279 . PCD. CEL_V_MONI_ON        # 【BAT 管理手順書 (TPP-7S8238-03K) へ記録】
0280 . #
0281 . # < CHECK >
0282 . CHECK PCD. CEL1_V          >3. 705
0283 . CHECK PCD. CEL2_V          >3. 705
0284 . CHECK PCD. CEL3_V          >3. 705
0285 . CHECK PCD. CEL4_V          >3. 705
0286 . CHECK PCD. CEL5_V          >3. 705
0287 . CHECK PCD. CEL6_V          >3. 705
0288 . CHECK PCD. CEL7_V          >3. 705
0289 . CHECK PCD. CEL8_V          >3. 705
0290 . CHECK PCD. CEL9_V          >3. 705
0291 . CHECK PCD. CEL10_V         >3. 705
0292 . CHECK PCD. CEL11_V         >3. 705
0293 . CHECK PCD. CEL12_V         >3. 705
0294 . CHECK PCD. BAT_INT_EXT     =EXT
0295 . CHECK PCD. BUS_SW_ON_OFF    =OFF
0296 . CHECK PCD. BAT_OT_ENA_DIS   =DIS
0297 . #
0298 . # < CHECK >
0299 . CHECK PCD. OT_ON_SET        27.5 28.5
0300 . CHECK PCD. OT_OFF_SET      25.5 26.5
0301 . #
0302 . PCD. BAT_OT_ENA
0303 . #
0304 . # < CHECK >
0305 . CHECK PCD. BAT_OT_ENA_DIS    =ENA
0306 . CHECK PCD. BAT_OT_ON_OFF     =OFF
0307 . #
0308 . # 【確認】                      Check
0309 . # BUS-V > BAT-V であることを確認する。          G  N

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0310 #
0311 . PCD. INT_EXT_ENA
0312 #
0313 . # < CHECK >
0314 CHECK PCD. INT_EXT_ENA_DIS =ENA
0315 . #
0316 . PCD. BAT_INT
0317 #
0318 . # < CHECK >
0319 CHECK PCD. INT_EXT_ENA_DIS =DIS
0320 CHECK PCD. BAT_INT_EXT =INT
0321 CHECK PCD. BUS_SW_ENA_DIS =DIS
0322 CHECK PCD. BUS_SW_ON_OFF =OFF
0323 CHECK PCD. CHG_I -0.1 0.1
0324 CHECK PCD. DCHG_I -0.2 0.2
0325 #
0326 . #
0327 . PCD. BAT_K1_SET 0xA7 # K1 167
0328 WAIT_SEC 2
0329 DH. BAT_SM_EXE
0330 WAIT_SEC 2
0331 PCD. BAT_K2_SET 0xA5 # K2 165
0332 WAIT_SEC 2
0333 DH. BAT_SM_EXE
0334 WAIT_SEC 2
0335 PCD. BAT_CD_SET 0x80 # 10.56Ah
0336 WAIT_SEC 2
0337 DH. BAT_SM_EXE
0338 . #
0339 . # < CHECK >
0340 CHECK PCD. BAT_K1 167
0341 CHECK PCD. BAT_K2 165
0342 CHECK PCD. BAT_CD 10.55 10.57
0343 CHECK PCD. BAT_CV -
0344 #
0345 . # QL Check-Item Spec Check
0346 # -----+-----+-----+-----
0347 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0348 #
0349 # -----
0350 . # 1.2.6. インデックスパルス出力
0351 # -----
0352 . #
0353 . DH. MC_EACH_EXE 0x0032 # 1553B TLM mode 6 (SYS HK 1/2, BUS User HK 1/4, DUMP 1/4,
0354 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0355 #: 0x00 0x00 DH. DMP_PERIOD 0x02
0356 #: 0x02 0x02 ACS. ACS_PKT_FMT_1
0357 #: 0x05 0x05 DH. OPE_MODE 0x0C 0x26 0x28 0x03 0x00000000000000000000000000000000
0358 #: 0x06 0x06 DH. TLMFMT1553B_CHG 0x06
0359 #
0360 WAIT_SEC 40
0361 #

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0362 CHECK DH. DMP_PERIOD =SEC_4
0363 CHECK DH. CATEGORY_TBL_NO 0x28
0364 CHECK DH. DWNLNK_RATE =BPS16384
0365 CHECK DH. TLM1553BFMTNO 0x06
0366 CHECK ACS. F_ACS_PKT_FMT =ACSHK1
0367 #
0368 . # < CHECK >
0369 CHECK ACS. F_ACS_PKT_FMT =ACSHK1
0370 CHECK ACS. STS_IP_SRC =NULL
0371 CHECK ACS. STS_IP_OUT =DIS
0372 CHECK ACS. STS_IP_GNRT =DIS
0373 CHECK ACS. I_IPCHK =RST
0374 CHECK ACS. I_IP_GNRT =SELF
0375 CHECK ACS_SYS. I_IPCHK =RST
0376 CHECK ACS_SYS. I_IP_GNRT =SELF
0377 CHECK ACS. BIT_I_IPCHK =RST
0378 #
0379 . # SSAS を擬似モードにする
0380 . ACS. SSAS_SEL_IMT
0381 #
0382 # 疑似サンパルスを発生
0383 . # (SIN:2.5V, COS:5.0V, START:0x80, COARSE:0x10, Period:4.138sec)
0384 . ACS. DUM_SP_ON 2047 4095 128 16 8475
0385 #
0386 . # < CHECK >
0387 CHECK ACS. SSAS_ST_PT =IMT
0388 CHECK ACS. SSAS_ST =IMT
0389 CHECK ACS. F_SPN_ERR_SEL =OFF
0390 CHECK ACS. P_SPIN_SSAS_SEL 4.137 4.139
0391 CHECK ACS. THT_CRS_SEL 0x10
0392 CHECK ACS. SUN_ANG_COS 4.999 5.001
0393 CHECK ACS. SUN_ANG_SIN 2.499 2.501
0394 CHECK ACS. SUN_ANG_SEL -2 2
0395 CHECK ACS_SYS. SSAS_ST =IMT
0396 CHECK ACS_SYS. P_SPIN_SSAS_SEL 4.137 4.139
0397 CHECK ACS_SYS. THT_CRS_SEL 0x10
0398 CHECK ACS_SYS. SUN_ANG_SEL -2 2
0399 CHECK ACS. BIT_SSAS_ST =IMT
0400 CHECK ACS. BIT_P_SPIN_SSAS_SEL 4.013 4.263
0401 CHECK ACS. BIT_SUN_ANG_SEL -0.125 0.125
0402 #
0403 . # ACS HK パケットフォーマットを ACS_HK4 に設定
0404 . ACS. ACS_PKT_FMT_4
0405 #
0406 . # < CHECK >
0407 CHECK ACS. SSAS_IMT_SP_STS =ON
0408 CHECK ACS. E_SSAS_SPIN 0.0999 0.1001
0409 CHECK ACS. N_SPINRATE_AVE 5 5
0410 CHECK ACS_SYS. F_ECL =OFF
0411 CHECK ACS_SYS. I_ATT =STBY
0412 CHECK ACS_SYS. P_B_AVE 4.137 4.139
0413 #

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0414 . # IP フィードバックゲインを設定する。
0415 . ACS. IP_K_DT 0x3C9BA5E3 0x3C9BA5E3
0416 #
0417 . # < CHECK >
0418 CHECK ACS. K_DT0 0.018999 0.019001
0419 CHECK ACS. K_DT1 0.018999 0.019001
0420 #
0421 . # IP 源泉を IP にする
0422 . ACS. IP_SRC_IP
0423 # IP 生成機能を ON にする
0424 WAIT_SEC 2
0425 ACS. IP_GNRT_ON
0426 # MDP への IP 出力を開始する
0427 WAIT_SEC 2
0428 ACS. IP_OUT_ENA
0429 # ACS HK パケットフォーマットを ACS_HK1 に設定
0430 WAIT_SEC 2
0431 ACS. ACS_PKT_FMT_1
0432 #
0433 . # < CHECK >
0434 CHECK ACS. STS_IP_SRC =IP
0435 CHECK ACS. STS_IP_OUT =ENA
0436 CHECK ACS. STS_IP_GNRT =ENA
0437 CHECK ACS. I_IP_GNRT =SSAS
0438 CHECK ACS_SYS. I_IP_GNRT =SSAS
0439 #
0440 . # << DR STATUS CHECK >>
0441 #
0442 . # < CHECK >
0443 CHECK DH. DR_STATE =STANDBY
0444 CHECK DH. DR_CHK_ON_OFF =OFF
0445 #
0446 . DH. DR_ERRCLR
0447 . # QL Check-Item Spec Check
0448 # -----+-----+-----+-----+
0449 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0450 #
0451 # -----+-----+-----+-----+
0452 . # 1.2.7. TCFS 設定
0453 # -----+-----+-----+-----+
0454 #
0455 . PCD. ON_OFF_ENA
0456 #
0457 . # < CHECK >
0458 CHECK PCD. ON_OFF_ENA_DIS =ENA
0459 #
0460 . PCD. DRU_PSU_A_ON
0461 #
0462 . # < CHECK >
0463 CHECK PCD. DRU_PSU_A_ON_OFF =ON
0464 #
0465 . PCD. ON_OFF_DIS

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0466 . #
0467 . # < CHECK >
0468 CHECK PCD.ON_OFF_ENA_DIS =DIS
0469 . #
0470 . ACS.DRU_SEL_A
0471 . #
0472 . # < CHECK >
0473 CHECK ACS.DRU_SEL_PT =A
0474 CHECK ACS_SYS.DRU_SEL_PT =A
0475 CHECK ACS.BIT_DRU_SEL_PT =A
0476 . #
0477 . ACS.ACS_IOPERR_CLR
0478 . #
0479 . # < CHECK >
0480 CHECK ACS.DRU_A_ONOFF_ST =ON
0481 CHECK ACS.DRU_B_ONOFF_ST =OFF
0482 CHECK ACS.DRU_ACIMIF_A_ST =ON
0483 CHECK ACS.DRU_ACIMIF_B_ST =OFF
0484 CHECK ACS.SRAM1_MEMST =MEMO
0485 CHECK ACS.SRAM2_MEMST =MEMO
0486 CHECK ACS.C_DRU_CMD_ECC2 0
0487 CHECK ACS.C_DRU_TLM_ECC2 0
0488 CHECK ACS.C_LBUSU_ECC1 0
0489 CHECK ACS.C_LBUSL_ECC1 0
0490 CHECK ACS.C_DRU_CMD_Prt 0
0491 CHECK ACS.C_DRU_TLM_Prt 0
0492 CHECK ACS.DATA_ERR_STATUS =OK
0493 CHECK ACS.STS_DRUAPM_ON =OFF
0494 CHECK ACS.I_ADMCE_A_PWR =OFF
0495 CHECK ACS.I_ADMCE_B_PWR =OFF
0496 CHECK ACS.ADM_SEL_PORT1 =A
0497 CHECK ACS.SRAM_ST =SRAM1
0498 CHECK ACS_SYS.DRU_A_ONOFF_ST =ON
0499 CHECK ACS_SYS.DRU_B_ONOFF_ST =OFF
0500 CHECK ACS_SYS.DRU_ACIMIF_A_ST =ON
0501 CHECK ACS_SYS.DRU_ACIMIF_B_ST =OFF
0502 CHECK ACS_SYS.SRAM1_MEMST =MEMO
0503 CHECK ACS_SYS.SRAM2_MEMST =MEMO
0504 CHECK ACS_SYS.C_DRU_CMD_ECC2 0
0505 CHECK ACS_SYS.C_DRU_TLM_ECC2 0
0506 CHECK ACS_SYS.C_LBUSU_ECC1 0
0507 CHECK ACS_SYS.C_LBUSL_ECC1 0
0508 CHECK ACS_SYS.C_DRU_CMD_Prt 0
0509 CHECK ACS_SYS.C_DRU_TLM_Prt 0
0510 CHECK ACS_SYS.DATA_ERR_STATUS =OK
0511 CHECK ACS_SYS.STS_DRUAPM_ON =OFF
0512 CHECK ACS_SYS.I_ADMCE_A_PWR =OFF
0513 CHECK ACS_SYS.I_ADMCE_B_PWR =OFF
0514 CHECK ACS_SYS.ADM_SEL_PORT1 =A
0515 CHECK ACS_SYS.SRAM_ST =SRAM1
0516 CHECK ACS.BIT_DRU_A_ONOFF_ST =ON
0517 CHECK ACS.BIT_DRU_B_ONOFF_ST =OFF

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|------|------------------------------------------|--------------------------|
| 0518 | CHECK ACS. BIT_I_ADMCE_A_PWR             | =OFF                     |
| 0519 | CHECK ACS. BIT_I_ADMCE_B_PWR             | =OFF                     |
| 0520 | . #                                      |                          |
| 0521 | . ACS. ACS_PKT_FMT_4                     |                          |
| 0522 | . #                                      |                          |
| 0523 | . # < CHECK >                            |                          |
| 0524 | CHECK ACS. F_ACS_PKT_FMT                 | =ACSHK4                  |
| 0525 | CHECK ACS. I_ECCERR_S_D                  | 0                        |
| 0526 | CHECK ACS. I_ECCERR_S_L                  | 0                        |
| 0527 | CHECK ACS. I_DRU_ACIMIF1                 | 0                        |
| 0528 | CHECK ACS. I_DRU_ACIMIF2                 | 0                        |
| 0529 | CHECK ACS. I_DRU_ACIMIF3                 | 0                        |
| 0530 | CHECK ACS. I_DRU_ACIMIF4                 | 0                        |
| 0531 | CHECK ACS. DRU_CMD_ECC2                  | 0                        |
| 0532 | CHECK ACS. DRU_TLM_ECC2                  | 0                        |
| 0533 | CHECK ACS. DRU_CMD_Parity1               | 0                        |
| 0534 | CHECK ACS. DRU_CMD_Parity2               | 0                        |
| 0535 | CHECK ACS. DRU_TLM_Parity1               | 0                        |
| 0536 | CHECK ACS. DRU_TLM_Parity2               | 0                        |
| 0537 | CHECK ACS. I_CMD_FLAG1                   | 0x02ec0c00               |
| 0538 | CHECK ACS. I_CMD_FLAG2                   | 0                        |
| 0539 | . #                                      |                          |
| 0540 | . ACS. ACS_PKT_FMT_1                     |                          |
| 0541 | . #                                      |                          |
| 0542 | . # < CHECK >                            |                          |
| 0543 | CHECK ACS. F_ACS_PKT_FMT                 | =ACSHK1                  |
| 0544 | . #                                      |                          |
| 0545 | . TCS. SET_TBL_ENA                       |                          |
| 0546 | . #                                      |                          |
| 0547 | . # < CHECK >                            |                          |
| 0548 | CHECK TCS. SET_TBL_ST                    | =ENA                     |
| 0549 | . #                                      |                          |
| 0550 | . # < BKUP DTY TBL1 >                    |                          |
| 0551 | . TCS. SET_BKUP_DTY_TBL 0x00 0x00 0x0020 | # BKUP DTY TBL1 CH01: 32 |
| 0552 | WAIT_SEC 2                               |                          |
| 0553 | . TCS. SET_BKUP_DTY_TBL 0x00 0x01 0x0020 | # BKUP DTY TBL1 CH02: 32 |
| 0554 | WAIT_SEC 2                               |                          |
| 0555 | . TCS. SET_BKUP_DTY_TBL 0x00 0x02 0x0020 | # BKUP DTY TBL1 CH03: 32 |
| 0556 | WAIT_SEC 2                               |                          |
| 0557 | . TCS. SET_BKUP_DTY_TBL 0x00 0x03 0x0020 | # BKUP DTY TBL1 CH04: 32 |
| 0558 | WAIT_SEC 2                               |                          |
| 0559 | . TCS. SET_BKUP_DTY_TBL 0x00 0x04 0x0020 | # BKUP DTY TBL1 CH05: 32 |
| 0560 | WAIT_SEC 2                               |                          |
| 0561 | . TCS. SET_BKUP_DTY_TBL 0x00 0x05 0x0020 | # BKUP DTY TBL1 CH06: 32 |
| 0562 | WAIT_SEC 2                               |                          |
| 0563 | . TCS. SET_BKUP_DTY_TBL 0x00 0x06 0x0020 | # BKUP DTY TBL1 CH07: 32 |
| 0564 | WAIT_SEC 2                               |                          |
| 0565 | . TCS. SET_BKUP_DTY_TBL 0x00 0x07 0x0020 | # BKUP DTY TBL1 CH08: 32 |
| 0566 | WAIT_SEC 2                               |                          |
| 0567 | . TCS. SET_BKUP_DTY_TBL 0x00 0x08 0x0020 | # BKUP DTY TBL1 CH09: 32 |
| 0568 | WAIT_SEC 2                               |                          |
| 0569 | . TCS. SET_BKUP_DTY_TBL 0x00 0x09 0x0020 | # BKUP DTY TBL1 CH10: 32 |

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| 0570 | WAIT_SEC 2                            |                          |
| 0571 | TCS.SET_BKUP_DTY_TBL 0x00 0x0A 0x0020 | # BKUP DTY TBL1 CH11: 32 |
| 0572 | WAIT_SEC 2                            |                          |
| 0573 | TCS.SET_BKUP_DTY_TBL 0x00 0x0B 0x0020 | # BKUP DTY TBL1 CH12: 32 |
| 0574 | WAIT_SEC 2                            |                          |
| 0575 | TCS.SET_BKUP_DTY_TBL 0x00 0x0C 0x0020 | # BKUP DTY TBL1 CH13: 32 |
| 0576 | WAIT_SEC 2                            |                          |
| 0577 | TCS.SET_BKUP_DTY_TBL 0x00 0x0D 0x0000 | # BKUP DTY TBL1 CH14: 0  |
| 0578 | WAIT_SEC 2                            |                          |
| 0579 | TCS.SET_BKUP_DTY_TBL 0x00 0x0E 0x0000 | # BKUP DTY TBL1 CH15: 0  |
| 0580 | WAIT_SEC 2                            |                          |
| 0581 | TCS.SET_BKUP_DTY_TBL 0x00 0x0F 0x0000 | # BKUP DTY TBL1 CH16: 0  |
| 0582 | WAIT_SEC 2                            |                          |
| 0583 | TCS.SET_BKUP_DTY_TBL 0x00 0x10 0x0020 | # BKUP DTY TBL1 CH17: 32 |
| 0584 | WAIT_SEC 2                            |                          |
| 0585 | TCS.SET_BKUP_DTY_TBL 0x00 0x11 0x0020 | # BKUP DTY TBL1 CH18: 32 |
| 0586 | WAIT_SEC 2                            |                          |
| 0587 | TCS.SET_BKUP_DTY_TBL 0x00 0x12 0x0020 | # BKUP DTY TBL1 CH19: 32 |
| 0588 | WAIT_SEC 2                            |                          |
| 0589 | TCS.SET_BKUP_DTY_TBL 0x00 0x13 0x0020 | # BKUP DTY TBL1 CH20: 32 |
| 0590 | WAIT_SEC 2                            |                          |
| 0591 | TCS.SET_BKUP_DTY_TBL 0x00 0x14 0x0020 | # BKUP DTY TBL1 CH21: 32 |
| 0592 | WAIT_SEC 2                            |                          |
| 0593 | TCS.SET_BKUP_DTY_TBL 0x00 0x15 0x0020 | # BKUP DTY TBL1 CH22: 32 |
| 0594 | WAIT_SEC 2                            |                          |
| 0595 | TCS.SET_BKUP_DTY_TBL 0x00 0x16 0x0020 | # BKUP DTY TBL1 CH23: 32 |
| 0596 | WAIT_SEC 2                            |                          |
| 0597 | TCS.SET_BKUP_DTY_TBL 0x00 0x17 0x0020 | # BKUP DTY TBL1 CH24: 32 |
| 0598 | WAIT_SEC 2                            |                          |
| 0599 | TCS.SET_BKUP_DTY_TBL 0x00 0x18 0x0020 | # BKUP DTY TBL1 CH25: 32 |
| 0600 | WAIT_SEC 2                            |                          |
| 0601 | TCS.SET_BKUP_DTY_TBL 0x00 0x19 0x0020 | # BKUP DTY TBL1 CH26: 32 |
| 0602 | WAIT_SEC 2                            |                          |
| 0603 | TCS.SET_BKUP_DTY_TBL 0x00 0x1A 0x0020 | # BKUP DTY TBL1 CH27: 32 |
| 0604 | WAIT_SEC 2                            |                          |
| 0605 | TCS.SET_BKUP_DTY_TBL 0x00 0x1B 0x0020 | # BKUP DTY TBL1 CH28: 32 |
| 0606 | WAIT_SEC 2                            |                          |
| 0607 | TCS.SET_BKUP_DTY_TBL 0x00 0x1C 0x0020 | # BKUP DTY TBL1 CH29: 32 |
| 0608 | WAIT_SEC 2                            |                          |
| 0609 | TCS.SET_BKUP_DTY_TBL 0x00 0x1D 0x0000 | # BKUP DTY TBL1 CH30: 0  |
| 0610 | WAIT_SEC 2                            |                          |
| 0611 | TCS.SET_BKUP_DTY_TBL 0x00 0x1E 0x0000 | # BKUP DTY TBL1 CH31: 0  |
| 0612 | WAIT_SEC 2                            |                          |
| 0613 | TCS.SET_BKUP_DTY_TBL 0x00 0x1F 0x0000 | # BKUP DTY TBL1 CH32: 0  |
| 0614 | #                                     |                          |
| 0615 | # < NMNL DTY TBL1 >                   |                          |
| 0616 | TCS.SET_NMNL_DTY_TBL 0x00 0x00 0x0040 | # NMNL DTY TBL1 CH01: 64 |
| 0617 | WAIT_SEC 2                            |                          |
| 0618 | TCS.SET_NMNL_DTY_TBL 0x00 0x01 0x0040 | # NMNL DTY TBL1 CH02: 64 |
| 0619 | WAIT_SEC 2                            |                          |
| 0620 | TCS.SET_NMNL_DTY_TBL 0x00 0x02 0x0040 | # NMNL DTY TBL1 CH03: 64 |
| 0621 | WAIT_SEC 2                            |                          |

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| 0622 | TCS.SET_NMNL_DTY_TBL 0x00 0x03 0x0040 | # NMNL DTY TBL1 CH04: 64 |
| 0623 | WAIT_SEC 2                            |                          |
| 0624 | TCS.SET_NMNL_DTY_TBL 0x00 0x04 0x0040 | # NMNL DTY TBL1 CH05: 64 |
| 0625 | WAIT_SEC 2                            |                          |
| 0626 | TCS.SET_NMNL_DTY_TBL 0x00 0x05 0x0040 | # NMNL DTY TBL1 CH06: 64 |
| 0627 | WAIT_SEC 2                            |                          |
| 0628 | TCS.SET_NMNL_DTY_TBL 0x00 0x06 0x0040 | # NMNL DTY TBL1 CH07: 64 |
| 0629 | WAIT_SEC 2                            |                          |
| 0630 | TCS.SET_NMNL_DTY_TBL 0x00 0x07 0x0040 | # NMNL DTY TBL1 CH08: 64 |
| 0631 | WAIT_SEC 2                            |                          |
| 0632 | TCS.SET_NMNL_DTY_TBL 0x00 0x08 0x0040 | # NMNL DTY TBL1 CH09: 64 |
| 0633 | WAIT_SEC 2                            |                          |
| 0634 | TCS.SET_NMNL_DTY_TBL 0x00 0x09 0x0040 | # NMNL DTY TBL1 CH10: 64 |
| 0635 | WAIT_SEC 2                            |                          |
| 0636 | TCS.SET_NMNL_DTY_TBL 0x00 0x0A 0x0040 | # NMNL DTY TBL1 CH11: 64 |
| 0637 | WAIT_SEC 2                            |                          |
| 0638 | TCS.SET_NMNL_DTY_TBL 0x00 0x0B 0x0040 | # NMNL DTY TBL1 CH12: 64 |
| 0639 | WAIT_SEC 2                            |                          |
| 0640 | TCS.SET_NMNL_DTY_TBL 0x00 0x0C 0x0040 | # NMNL DTY TBL1 CH13: 64 |
| 0641 | WAIT_SEC 2                            |                          |
| 0642 | TCS.SET_NMNL_DTY_TBL 0x00 0x0D 0x0000 | # NMNL DTY TBL1 CH14: 0  |
| 0643 | WAIT_SEC 2                            |                          |
| 0644 | TCS.SET_NMNL_DTY_TBL 0x00 0x0E 0x0000 | # NMNL DTY TBL1 CH15: 0  |
| 0645 | WAIT_SEC 2                            |                          |
| 0646 | TCS.SET_NMNL_DTY_TBL 0x00 0x0F 0x0000 | # NMNL DTY TBL1 CH16: 0  |
| 0647 | WAIT_SEC 2                            |                          |
| 0648 | TCS.SET_NMNL_DTY_TBL 0x00 0x10 0x0040 | # NMNL DTY TBL1 CH17: 64 |
| 0649 | WAIT_SEC 2                            |                          |
| 0650 | TCS.SET_NMNL_DTY_TBL 0x00 0x11 0x0040 | # NMNL DTY TBL1 CH18: 64 |
| 0651 | WAIT_SEC 2                            |                          |
| 0652 | TCS.SET_NMNL_DTY_TBL 0x00 0x12 0x0040 | # NMNL DTY TBL1 CH19: 64 |
| 0653 | WAIT_SEC 2                            |                          |
| 0654 | TCS.SET_NMNL_DTY_TBL 0x00 0x13 0x0040 | # NMNL DTY TBL1 CH20: 64 |
| 0655 | WAIT_SEC 2                            |                          |
| 0656 | TCS.SET_NMNL_DTY_TBL 0x00 0x14 0x0040 | # NMNL DTY TBL1 CH21: 64 |
| 0657 | WAIT_SEC 2                            |                          |
| 0658 | TCS.SET_NMNL_DTY_TBL 0x00 0x15 0x0040 | # NMNL DTY TBL1 CH22: 64 |
| 0659 | WAIT_SEC 2                            |                          |
| 0660 | TCS.SET_NMNL_DTY_TBL 0x00 0x16 0x0040 | # NMNL DTY TBL1 CH23: 64 |
| 0661 | WAIT_SEC 2                            |                          |
| 0662 | TCS.SET_NMNL_DTY_TBL 0x00 0x17 0x0040 | # NMNL DTY TBL1 CH24: 64 |
| 0663 | WAIT_SEC 2                            |                          |
| 0664 | TCS.SET_NMNL_DTY_TBL 0x00 0x18 0x0040 | # NMNL DTY TBL1 CH25: 64 |
| 0665 | WAIT_SEC 2                            |                          |
| 0666 | TCS.SET_NMNL_DTY_TBL 0x00 0x19 0x0040 | # NMNL DTY TBL1 CH26: 64 |
| 0667 | WAIT_SEC 2                            |                          |
| 0668 | TCS.SET_NMNL_DTY_TBL 0x00 0x1A 0x0040 | # NMNL DTY TBL1 CH27: 64 |
| 0669 | WAIT_SEC 2                            |                          |
| 0670 | TCS.SET_NMNL_DTY_TBL 0x00 0x1B 0x0040 | # NMNL DTY TBL1 CH28: 64 |
| 0671 | WAIT_SEC 2                            |                          |
| 0672 | TCS.SET_NMNL_DTY_TBL 0x00 0x1C 0x0040 | # NMNL DTY TBL1 CH29: 64 |
| 0673 | WAIT_SEC 2                            |                          |

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0674 TCS.SET_NMNL_DTY_TBL 0x00 0x1D 0x0000 # NMNL DTY TBL1 CH30: 0
0675 WAIT_SEC 2
0676 TCS.SET_NMNL_DTY_TBL 0x00 0x1E 0x0000 # NMNL DTY TBL1 CH31: 0
0677 WAIT_SEC 2
0678 TCS.SET_NMNL_DTY_TBL 0x00 0x1F 0x0000 # NMNL DTY TBL1 CH32: 0
0679 #
0680 # < NMNL TBL1 ヒータ ON/OFF スレッシュホールド設定 >
0681 TCS.SET_NMNL_TMP_TBL 0x00 0x00 0x9E 0xA2 # NMNL TBL1 CH01 ON: 27.94 OFF: 30
0682 WAIT_SEC 2
0683 TCS.SET_NMNL_TMP_TBL 0x00 0x01 0x9C 0xA0 # NMNL TBL1 CH02 ON: 27.94 OFF: 30
0684 WAIT_SEC 2
0685 TCS.SET_NMNL_TMP_TBL 0x00 0x02 0x9B 0x9F # NMNL TBL1 CH03 ON: 27.94 OFF: 30
0686 WAIT_SEC 2
0687 TCS.SET_NMNL_TMP_TBL 0x00 0x03 0x98 0x9C # NMNL TBL1 CH04 ON: 27.94 OFF: 30
0688 WAIT_SEC 2
0689 TCS.SET_NMNL_TMP_TBL 0x00 0x04 0x9C 0xA0 # NMNL TBL1 CH05 ON: 27.94 OFF: 30
0690 WAIT_SEC 2
0691 TCS.SET_NMNL_TMP_TBL 0x00 0x05 0x9B 0x9F # NMNL TBL1 CH06 ON: 27.94 OFF: 30
0692 WAIT_SEC 2
0693 TCS.SET_NMNL_TMP_TBL 0x00 0x06 0x9C 0xA0 # NMNL TBL1 CH07 ON: 27.94 OFF: 30
0694 WAIT_SEC 2
0695 TCS.SET_NMNL_TMP_TBL 0x00 0x07 0x83 0x84 # NMNL TBL1 CH08 ON: 28.46 OFF: 29.49
0696 WAIT_SEC 2
0697 TCS.SET_NMNL_TMP_TBL 0x00 0x08 0x9D 0xA1 # NMNL TBL1 CH09 ON: 27.94 OFF: 30
0698 WAIT_SEC 2
0699 TCS.SET_NMNL_TMP_TBL 0x00 0x09 0x9C 0xA0 # NMNL TBL1 CH10 ON: 27.94 OFF: 30
0700 WAIT_SEC 2
0701 TCS.SET_NMNL_TMP_TBL 0x00 0x0A 0x9A 0x9E # NMNL TBL1 CH11 ON: 27.75 OFF: 29.8
0702 WAIT_SEC 2
0703 TCS.SET_NMNL_TMP_TBL 0x00 0x0B 0x9A 0x9E # NMNL TBL1 CH12 ON: 27.94 OFF: 30
0704 WAIT_SEC 2
0705 TCS.SET_NMNL_TMP_TBL 0x00 0x0C 0x9B 0x9F # NMNL TBL1 CH13 ON: 27.94 OFF: 30
0706 WAIT_SEC 2
0707 TCS.SET_NMNL_TMP_TBL 0x00 0x0D 0x00 0x00 # NMNL TBL1 CH14 ON: -50.41 OFF: -50.41
0708 WAIT_SEC 2
0709 TCS.SET_NMNL_TMP_TBL 0x00 0x0E 0x00 0x00 # NMNL TBL1 CH15 ON: -50.41 OFF: -50.41
0710 WAIT_SEC 2
0711 TCS.SET_NMNL_TMP_TBL 0x00 0x0F 0x00 0x00 # NMNL TBL1 CH16 ON: -50.41 OFF: -50.41
0712 WAIT_SEC 2
0713 TCS.SET_NMNL_TMP_TBL 0x00 0x10 0x9E 0xA2 # NMNL TBL1 CH17 ON: 27.94 OFF: 30
0714 WAIT_SEC 2
0715 TCS.SET_NMNL_TMP_TBL 0x00 0x11 0x9D 0xA1 # NMNL TBL1 CH18 ON: 27.94 OFF: 30
0716 WAIT_SEC 2
0717 TCS.SET_NMNL_TMP_TBL 0x00 0x12 0x9C 0xA0 # NMNL TBL1 CH19 ON: 27.94 OFF: 30
0718 WAIT_SEC 2
0719 TCS.SET_NMNL_TMP_TBL 0x00 0x13 0x9C 0xA0 # NMNL TBL1 CH20 ON: 27.94 OFF: 30
0720 WAIT_SEC 2
0721 TCS.SET_NMNL_TMP_TBL 0x00 0x14 0x9D 0xA1 # NMNL TBL1 CH21 ON: 27.94 OFF: 30
0722 WAIT_SEC 2
0723 TCS.SET_NMNL_TMP_TBL 0x00 0x15 0x9B 0x9F # NMNL TBL1 CH22 ON: 27.94 OFF: 30
0724 WAIT_SEC 2
0725 TCS.SET_NMNL_TMP_TBL 0x00 0x16 0x9A 0x9E # NMNL TBL1 CH23 ON: 27.94 OFF: 30

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| 0726 | WAIT_SEC 2                                                                       |  |
| 0727 | TCS.SET_NMNL_TMP_TBL 0x00 0x17 0x9F 0xA3 # NMNL TBL1 CH24 ON: 27.94 OFF: 30      |  |
| 0728 | WAIT_SEC 2                                                                       |  |
| 0729 | TCS.SET_NMNL_TMP_TBL 0x00 0x18 0x9A 0x9E # NMNL TBL1 CH25 ON: 27.94 OFF: 30      |  |
| 0730 | WAIT_SEC 2                                                                       |  |
| 0731 | TCS.SET_NMNL_TMP_TBL 0x00 0x19 0x9C 0xA0 # NMNL TBL1 CH26 ON: 27.94 OFF: 30      |  |
| 0732 | WAIT_SEC 2                                                                       |  |
| 0733 | TCS.SET_NMNL_TMP_TBL 0x00 0x1A 0x9B 0x9F # NMNL TBL1 CH27 ON: 27.94 OFF: 30      |  |
| 0734 | WAIT_SEC 2                                                                       |  |
| 0735 | TCS.SET_NMNL_TMP_TBL 0x00 0x1B 0x99 0x9D # NMNL TBL1 CH28 ON: 27.95 OFF: 30      |  |
| 0736 | WAIT_SEC 2                                                                       |  |
| 0737 | TCS.SET_NMNL_TMP_TBL 0x00 0x1C 0x9B 0x9F # NMNL TBL1 CH29 ON: 27.94 OFF: 30      |  |
| 0738 | WAIT_SEC 2                                                                       |  |
| 0739 | TCS.SET_NMNL_TMP_TBL 0x00 0x1D 0x00 0x00 # NMNL TBL1 CH30 ON: -50.41 OFF: -50.41 |  |
| 0740 | WAIT_SEC 2                                                                       |  |
| 0741 | TCS.SET_NMNL_TMP_TBL 0x00 0x1E 0x00 0x00 # NMNL TBL1 CH31 ON: -50.41 OFF: -50.41 |  |
| 0742 | WAIT_SEC 2                                                                       |  |
| 0743 | TCS.SET_NMNL_TMP_TBL 0x00 0x1F 0x00 0x00 # NMNL TBL1 CH32 ON: -50.41 OFF: -50.41 |  |
| 0744 | #                                                                                |  |
| 0745 | # <ヒータ ENA/DIS テーブル設定>                                                           |  |
| 0746 | TCS.SET_HTR_ENA 0x05 0x00 # ヒータ CH06: BAT1-1 DIS                                 |  |
| 0747 | WAIT_SEC 2                                                                       |  |
| 0748 | TCS.SET_HTR_ENA 0x0C 0x00 # ヒータ CH13: BAT2-1 DIS                                 |  |
| 0749 | WAIT_SEC 2                                                                       |  |
| 0750 | TCS.SET_HTR_ENA 0x15 0x00 # ヒータ CH22: BAT1-2 DIS                                 |  |
| 0751 | WAIT_SEC 2                                                                       |  |
| 0752 | TCS.SET_HTR_ENA 0x1C 0x00 # ヒータ CH29: BAT2-2 DIS                                 |  |
| 0753 | #                                                                                |  |
| 0754 | TCS.ONESHOT_TLM_0x11                                                             |  |
| 0755 | TCS.ONESHOT_TLM_0x19                                                             |  |
| 0756 | TCS.ONESHOT_TLM_0x1F                                                             |  |
| 0757 | TCS.ONESHOT_TLM_0x25                                                             |  |
| 0758 | #                                                                                |  |
| 0759 | # < CHECK >                                                                      |  |
| 0760 | CHECK TCS.BK_DTY_TBL1_01CH 32                                                    |  |
| 0761 | CHECK TCS.BK_DTY_TBL1_02CH 32                                                    |  |
| 0762 | CHECK TCS.BK_DTY_TBL1_03CH 32                                                    |  |
| 0763 | CHECK TCS.BK_DTY_TBL1_04CH 32                                                    |  |
| 0764 | CHECK TCS.BK_DTY_TBL1_05CH 32                                                    |  |
| 0765 | CHECK TCS.BK_DTY_TBL1_06CH 32                                                    |  |
| 0766 | CHECK TCS.BK_DTY_TBL1_07CH 32                                                    |  |
| 0767 | CHECK TCS.BK_DTY_TBL1_08CH 32                                                    |  |
| 0768 | CHECK TCS.BK_DTY_TBL1_09CH 32                                                    |  |
| 0769 | CHECK TCS.BK_DTY_TBL1_10CH 32                                                    |  |
| 0770 | CHECK TCS.BK_DTY_TBL1_11CH 32                                                    |  |
| 0771 | CHECK TCS.BK_DTY_TBL1_12CH 32                                                    |  |
| 0772 | CHECK TCS.BK_DTY_TBL1_13CH 32                                                    |  |
| 0773 | CHECK TCS.BK_DTY_TBL1_14CH 0                                                     |  |
| 0774 | CHECK TCS.BK_DTY_TBL1_15CH 0                                                     |  |
| 0775 | CHECK TCS.BK_DTY_TBL1_16CH 0                                                     |  |
| 0776 | CHECK TCS.BK_DTY_TBL1_17CH 32                                                    |  |
| 0777 | CHECK TCS.BK_DTY_TBL1_18CH 32                                                    |  |

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| 0778 | CHECK TCS. BK_DTY_TBL1_19CH | 32        |
| 0779 | CHECK TCS. BK_DTY_TBL1_20CH | 32        |
| 0780 | CHECK TCS. BK_DTY_TBL1_21CH | 32        |
| 0781 | CHECK TCS. BK_DTY_TBL1_22CH | 32        |
| 0782 | CHECK TCS. BK_DTY_TBL1_23CH | 32        |
| 0783 | CHECK TCS. BK_DTY_TBL1_24CH | 32        |
| 0784 | CHECK TCS. BK_DTY_TBL1_25CH | 32        |
| 0785 | CHECK TCS. BK_DTY_TBL1_26CH | 32        |
| 0786 | CHECK TCS. BK_DTY_TBL1_27CH | 32        |
| 0787 | CHECK TCS. BK_DTY_TBL1_28CH | 32        |
| 0788 | CHECK TCS. BK_DTY_TBL1_29CH | 32        |
| 0789 | CHECK TCS. BK_DTY_TBL1_30CH | 0         |
| 0790 | CHECK TCS. BK_DTY_TBL1_31CH | 0         |
| 0791 | CHECK TCS. BK_DTY_TBL1_32CH | 0         |
| 0792 | CHECK TCS. NM_DTY_TBL1_01CH | 64        |
| 0793 | CHECK TCS. NM_DTY_TBL1_02CH | 64        |
| 0794 | CHECK TCS. NM_DTY_TBL1_03CH | 64        |
| 0795 | CHECK TCS. NM_DTY_TBL1_04CH | 64        |
| 0796 | CHECK TCS. NM_DTY_TBL1_05CH | 64        |
| 0797 | CHECK TCS. NM_DTY_TBL1_06CH | 64        |
| 0798 | CHECK TCS. NM_DTY_TBL1_07CH | 64        |
| 0799 | CHECK TCS. NM_DTY_TBL1_08CH | 64        |
| 0800 | CHECK TCS. NM_DTY_TBL1_09CH | 64        |
| 0801 | CHECK TCS. NM_DTY_TBL1_10CH | 64        |
| 0802 | CHECK TCS. NM_DTY_TBL1_11CH | 64        |
| 0803 | CHECK TCS. NM_DTY_TBL1_12CH | 64        |
| 0804 | CHECK TCS. NM_DTY_TBL1_13CH | 64        |
| 0805 | CHECK TCS. NM_DTY_TBL1_14CH | 0         |
| 0806 | CHECK TCS. NM_DTY_TBL1_15CH | 0         |
| 0807 | CHECK TCS. NM_DTY_TBL1_16CH | 0         |
| 0808 | CHECK TCS. NM_DTY_TBL1_17CH | 64        |
| 0809 | CHECK TCS. NM_DTY_TBL1_18CH | 64        |
| 0810 | CHECK TCS. NM_DTY_TBL1_19CH | 64        |
| 0811 | CHECK TCS. NM_DTY_TBL1_20CH | 64        |
| 0812 | CHECK TCS. NM_DTY_TBL1_21CH | 64        |
| 0813 | CHECK TCS. NM_DTY_TBL1_22CH | 64        |
| 0814 | CHECK TCS. NM_DTY_TBL1_23CH | 64        |
| 0815 | CHECK TCS. NM_DTY_TBL1_24CH | 64        |
| 0816 | CHECK TCS. NM_DTY_TBL1_25CH | 64        |
| 0817 | CHECK TCS. NM_DTY_TBL1_26CH | 64        |
| 0818 | CHECK TCS. NM_DTY_TBL1_27CH | 64        |
| 0819 | CHECK TCS. NM_DTY_TBL1_28CH | 64        |
| 0820 | CHECK TCS. NM_DTY_TBL1_29CH | 64        |
| 0821 | CHECK TCS. NM_DTY_TBL1_30CH | 0         |
| 0822 | CHECK TCS. NM_DTY_TBL1_31CH | 0         |
| 0823 | CHECK TCS. NM_DTY_TBL1_32CH | 0         |
| 0824 | CHECK TCS. NM_OFF_TBL1_01CH | 29.5 30.5 |
| 0825 | CHECK TCS. NM_OFF_TBL1_02CH | 29.5 30.5 |
| 0826 | CHECK TCS. NM_OFF_TBL1_03CH | 29.5 30.5 |
| 0827 | CHECK TCS. NM_OFF_TBL1_04CH | 29.5 30.5 |
| 0828 | CHECK TCS. NM_OFF_TBL1_05CH | 29.5 30.5 |
| 0829 | CHECK TCS. NM_OFF_TBL1_06CH | 29.5 30.5 |

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| 0830 | CHECK TCS. NM_OFF_TBL1_07CH | 29.5 30.5   |
| 0831 | CHECK TCS. NM_OFF_TBL1_08CH | 28.99 29.99 |
| 0832 | CHECK TCS. NM_OFF_TBL1_09CH | 29.5 30.5   |
| 0833 | CHECK TCS. NM_OFF_TBL1_10CH | 29.5 30.5   |
| 0834 | CHECK TCS. NM_OFF_TBL1_11CH | 29.3 30.3   |
| 0835 | CHECK TCS. NM_OFF_TBL1_12CH | 29.5 30.5   |
| 0836 | CHECK TCS. NM_OFF_TBL1_13CH | 29.5 30.5   |
| 0837 | CHECK TCS. NM_OFF_TBL1_17CH | 29.5 30.5   |
| 0838 | CHECK TCS. NM_OFF_TBL1_18CH | 29.5 30.5   |
| 0839 | CHECK TCS. NM_OFF_TBL1_19CH | 29.5 30.5   |
| 0840 | CHECK TCS. NM_OFF_TBL1_20CH | 29.5 30.5   |
| 0841 | CHECK TCS. NM_OFF_TBL1_21CH | 29.5 30.5   |
| 0842 | CHECK TCS. NM_OFF_TBL1_22CH | 29.5 30.5   |
| 0843 | CHECK TCS. NM_OFF_TBL1_23CH | 29.5 30.5   |
| 0844 | CHECK TCS. NM_OFF_TBL1_24CH | 29.5 30.5   |
| 0845 | CHECK TCS. NM_OFF_TBL1_25CH | 29.5 30.5   |
| 0846 | CHECK TCS. NM_OFF_TBL1_26CH | 29.5 30.5   |
| 0847 | CHECK TCS. NM_OFF_TBL1_27CH | 29.5 30.5   |
| 0848 | CHECK TCS. NM_OFF_TBL1_28CH | 29.5 30.5   |
| 0849 | CHECK TCS. NM_OFF_TBL1_29CH | 29.5 30.5   |
| 0850 | CHECK TCS. NM_ON_TBL1_01CH  | 27.44 28.44 |
| 0851 | CHECK TCS. NM_ON_TBL1_02CH  | 27.44 28.44 |
| 0852 | CHECK TCS. NM_ON_TBL1_03CH  | 27.44 28.44 |
| 0853 | CHECK TCS. NM_ON_TBL1_04CH  | 27.44 28.44 |
| 0854 | CHECK TCS. NM_ON_TBL1_05CH  | 27.44 28.44 |
| 0855 | CHECK TCS. NM_ON_TBL1_06CH  | 27.44 28.44 |
| 0856 | CHECK TCS. NM_ON_TBL1_07CH  | 27.44 28.44 |
| 0857 | CHECK TCS. NM_ON_TBL1_08CH  | 27.96 28.96 |
| 0858 | CHECK TCS. NM_ON_TBL1_09CH  | 27.44 28.44 |
| 0859 | CHECK TCS. NM_ON_TBL1_10CH  | 27.44 28.44 |
| 0860 | CHECK TCS. NM_ON_TBL1_11CH  | 27.25 28.25 |
| 0861 | CHECK TCS. NM_ON_TBL1_12CH  | 27.44 28.44 |
| 0862 | CHECK TCS. NM_ON_TBL1_13CH  | 27.44 28.44 |
| 0863 | CHECK TCS. NM_ON_TBL1_17CH  | 27.44 28.44 |
| 0864 | CHECK TCS. NM_ON_TBL1_18CH  | 27.44 28.44 |
| 0865 | CHECK TCS. NM_ON_TBL1_19CH  | 27.44 28.44 |
| 0866 | CHECK TCS. NM_ON_TBL1_20CH  | 27.44 28.44 |
| 0867 | CHECK TCS. NM_ON_TBL1_21CH  | 27.44 28.44 |
| 0868 | CHECK TCS. NM_ON_TBL1_22CH  | 27.44 28.44 |
| 0869 | CHECK TCS. NM_ON_TBL1_23CH  | 27.44 28.44 |
| 0870 | CHECK TCS. NM_ON_TBL1_24CH  | 27.44 28.44 |
| 0871 | CHECK TCS. NM_ON_TBL1_25CH  | 27.44 28.44 |
| 0872 | CHECK TCS. NM_ON_TBL1_26CH  | 27.44 28.44 |
| 0873 | CHECK TCS. NM_ON_TBL1_27CH  | 27.44 28.44 |
| 0874 | CHECK TCS. NM_ON_TBL1_28CH  | 27.45 28.45 |
| 0875 | CHECK TCS. NM_ON_TBL1_29CH  | 27.44 28.44 |
| 0876 | CHECK TCS. HTR_ENADIS_06CH  | =DIS        |
| 0877 | CHECK TCS. HTR_ENADIS_13CH  | =DIS        |
| 0878 | CHECK TCS. HTR_ENADIS_22CH  | =DIS        |
| 0879 | CHECK TCS. HTR_ENADIS_29CH  | =DIS        |
| 0880 | . #                         |             |
| 0881 | . TCS. SET_PRM_ENA          |             |

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0882 . #
0883 . # < CHECK >
0884 . CHECK TCS. SET_PRM_ST =ENA
0885 . #
0886 . TCS. SET_PEAK_PWR 0x2328
0887 . TCS. CTRL_PKPWR_EXE
0888 . #
0889 . # < CHECK >
0890 . CHECK TCS. SET_POWER 90
0891 . #
0892 . TCS. CTRL_HTR_P1_ENA
0893 . WAIT_SEC 2
0894 . TCS. CTRL_HTR_P2_ENA
0895 . WAIT_SEC 2
0896 . TCS. CTRL_HTR_P3_ENA
0897 . WAIT_SEC 2
0898 . TCS. CTRL_HTR_P4_ENA
0899 . #
0900 . # < CHECK >
0901 . CHECK TCS. HTR01_P_VOL_ST =ON
0902 . CHECK TCS. HTR09_P_VOL_ST =ON
0903 . CHECK TCS. HTR17_P_VOL_ST =ON
0904 . CHECK TCS. HTR25_P_VOL_ST =ON
0905 . #
0906 . # < TABLE 選択 >
0907 . TCS. SET_NMNL_SEL_TBL_1
0908 . WAIT_SEC 2
0909 . TCS. SET_ATSF_SEL_TBL_1
0910 . WAIT_SEC 2
0911 . TCS. SET_BKUP_SEL_TBL_1
0912 . #
0913 . # < CHECK >
0914 . CHECK TCS. SET_NMNL_TBL =TBL1
0915 . CHECK TCS. SET_SAFE_TBL1 =ENA
0916 . CHECK TCS. SET_SAFE_TBL2 =DIS
0917 . CHECK TCS. SET_SAFE_TBL3 =DIS
0918 . CHECK TCS. SET_SAFE_TBL4 =DIS
0919 . CHECK TCS. SET_BKUP_TBL1 =ENA
0920 . CHECK TCS. SET_BKUP_TBL2 =DIS
0921 . CHECK TCS. SET_BKUP_TBL3 =DIS
0922 . CHECK TCS. SET_BKUP_TBL4 =DIS
0923 . #
0924 . # < ピーク電力制御 ON >
0925 . TCS. CTRL_PKPWR_ON
0926 . #
0927 . # < CHECK >
0928 . CHECK TCS. PPWR_CTR =ON
0929 . #
0930 . # < ノミナル制御モード移行 >
0931 . TCS. MODE_NMNL
0932 . #
0933 . # < CHECK >

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0934 CHECK TCS.MODE_ST =NMNL
0935 #
0936 . # QL Check-Item Spec Check
0937 . # -----+-----+-----+-----
0938 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0939 . #
0940 . # =====
0941 . # 1.3. SI 系初期 C0 時における AT/RQ/ST ENA/DIS 設定 (MDP 立ち上げ前)
0942 . # =====
0943 . #
0944 . # << 概要 >>
0945 . #
0946 . # ATMC 0x01 は、BAT INT 後に ENA、BAT EXT 前に DIS とする。
0947 . # ATMC 0x30-0x34, 0x36 は MDP 立ち上げ後に ENA、MDP 立ち上げ前に DIS とする。
0948 . # ATMC 0x2B は、衛星立ち上げ後に ENA、立下げ前に DIS とする。
0949 . # ATMC 0x09 は、ヒータ設定/制御開始後に ENA、ヒータ立下げ前に DIS とする。
0950 . #
0951 . # RQMC 0x08-0x0B, 0x0D, 0x0E は、各 SI 機器 OFF のリクエストであり、ENA とする
0952 . #
0953 . # << ATMC_ENA/DIS >>
0954 . #
0955 . DH. ATMC_EACH_DIS 0x00 # // 分離後 DMC CPU リセット検出
0956 WAIT_SEC 2
0957 DH. ATMC_EACH_ENA 0x01 # // BAT UVC in Cruise mode
0958 WAIT_SEC 2
0959 DH. ATMC_EACH_DIS 0x02 # //Separation Detection1
0960 WAIT_SEC 2
0961 DH. ATMC_EACH_DIS 0x03 # //Separation Detection2
0962 WAIT_SEC 2
0963 DH. ATMC_EACH_DIS 0x04 # // Backup Separation Detection1 (SA-V)
0964 WAIT_SEC 2
0965 DH. ATMC_EACH_DIS 0x05 # // Backup Separation Detection2 (SSAS_STATUS)
0966 WAIT_SEC 2
0967 DH. ATMC_EACH_DIS 0x06 # //Status check before separation #1
0968 WAIT_SEC 2
0969 DH. ATMC_EACH_DIS 0x07 # //Status check before separation #2
0970 WAIT_SEC 2
0971 DH. ATMC_EACH_DIS 0x08 # N/A
0972 WAIT_SEC 2
0973 DH. ATMC_EACH_ENA 0x09 # //分離前 CPU リセット時 TCS モード再度 NMNL 制御
0974 WAIT_SEC 2
0975 DH. ATMC_EACH_DIS 0x0A # N/A
0976 WAIT_SEC 2
0977 DH. ATMC_EACH_DIS 0x0B # N/A
0978 WAIT_SEC 2
0979 DH. ATMC_EACH_DIS 0x0C # //APR 故障検出
0980 WAIT_SEC 2
0981 DH. ATMC_EACH_DIS 0x0D # // DRU-A OFF 検出 #1
0982 WAIT_SEC 2
0983 DH. ATMC_EACH_DIS 0x0E # // DRU OFF 検出 #2
0984 WAIT_SEC 2
0985 DH. ATMC_EACH_DIS 0x0F # // MGA deploy monitor

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0986 WAIT_SEC 2
0987 DH. ATMC_EACH_DIS 0x10 # //SSAS サンプルスチェック (ACS 自律化 A)
0988 WAIT_SEC 2
0989 DH. ATMC_EACH_DIS 0x11 # //SSAS 太陽角チェック (ACS 自律化 B)
0990 WAIT_SEC 2
0991 DH. ATMC_EACH_DIS 0x12 # // CH1 FIFO エラーリセット
0992 WAIT_SEC 2
0993 DH. ATMC_EACH_DIS 0x13 # N/A
0994 WAIT_SEC 2
0995 DH. ATMC_EACH_DIS 0x14 # // ON/OFF Keying (APM Temperature)
0996 WAIT_SEC 2
0997 DH. ATMC_EACH_DIS 0x15 # // ON/OFF Keying (TWTA-EPC-A Temperature)
0998 WAIT_SEC 2
0999 DH. ATMC_EACH_DIS 0x16 # // ON/OFF Keying (TWTA-EPC-B Temperature)
1000 WAIT_SEC 2
1001 DH. ATMC_EACH_DIS 0x17 # // ON/OFF Keying (LO deck (DMC) Temperature)
1002 WAIT_SEC 2
1003 DH. ATMC_EACH_DIS 0x18 # // ON/OFF Keying (LO deck Temperature)
1004 WAIT_SEC 2
1005 DH. ATMC_EACH_DIS 0x19 # // ON/OFF Keying (SAP Temperature)
1006 WAIT_SEC 2
1007 DH. ATMC_EACH_DIS 0x1A # // ON/OFF Keying (BAT1 Temperature)
1008 WAIT_SEC 2
1009 DH. ATMC_EACH_DIS 0x1B # // ON/OFF Keying (RCS Tank Pressure)
1010 WAIT_SEC 2
1011 DH. ATMC_EACH_DIS 0x1C # N/A
1012 WAIT_SEC 2
1013 DH. ATMC_EACH_DIS 0x1D # //APR2 復帰#2
1014 WAIT_SEC 2
1015 DH. ATMC_EACH_DIS 0x1E # //ON/OFF Keying (ADMCE-A 電流)
1016 WAIT_SEC 2
1017 DH. ATMC_EACH_DIS 0x1F # //ON/OFF Keying (ADMCE-B 電流)
1018 WAIT_SEC 2
1019 DH. ATMC_EACH_DIS 0x20 # //ON/OFF Keying (BUS 電圧)
1020 WAIT_SEC 2
1021 DH. ATMC_EACH_DIS 0x21 # //ON/OFF Keying (BUS 電流)
1022 WAIT_SEC 2
1023 DH. ATMC_EACH_DIS 0x22 # //ON/OFF Keying (BAT 放電)
1024 WAIT_SEC 2
1025 DH. ATMC_EACH_DIS 0x23 # //ON/OFF Keying (SA 電圧)
1026 WAIT_SEC 2
1027 DH. ATMC_EACH_DIS 0x24 # //ON/OFF Keying (DMC-PSU-A 電流)
1028 WAIT_SEC 2
1029 DH. ATMC_EACH_DIS 0x25 # //ON/OFF Keying (DMC-PSU-B 電流)
1030 WAIT_SEC 2
1031 DH. ATMC_EACH_DIS 0x26 # //ON/OFF Keying (TWTA-EPC-A 電流)
1032 WAIT_SEC 2
1033 DH. ATMC_EACH_DIS 0x27 # //ON/OFF Keying (TWTA-EPC-B 電流)
1034 WAIT_SEC 2
1035 DH. ATMC_EACH_DIS 0x28 # //ON/OFF Keying (DRU-A 電流)
1036 WAIT_SEC 2
1037 DH. ATMC_EACH_DIS 0x29 # //ON/OFF Keying (DRU-B 電流)

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1038 WAIT_SEC 2
1039 DH. ATMC_EACH_DIS 0x2A # // BAT UVC
1040 WAIT_SEC 2
1041 DH. ATMC_EACH_ENA 0x2B # //PCD NIC リセットモニタ
1042 WAIT_SEC 2
1043 DH. ATMC_EACH_DIS 0x2C # //BAT 温度モニタ 1
1044 WAIT_SEC 2
1045 DH. ATMC_EACH_DIS 0x2D # //BAT 温度モニタ 2
1046 WAIT_SEC 2
1047 DH. ATMC_EACH_DIS 0x2E # // PCD User Request Error Clear
1048 WAIT_SEC 2
1049 DH. ATMC_EACH_DIS 0x2F # // BAT CV Timer Reset
1050 WAIT_SEC 2
1051 DH. ATMC_EACH_DIS 0x30 # //MDP-DPU1 プログラム起動異常検出
1052 WAIT_SEC 2
1053 DH. ATMC_EACH_DIS 0x31 # //MDP-DPU2 プログラム起動異常検出
1054 WAIT_SEC 2
1055 DH. ATMC_EACH_DIS 0x32 # //MDP-DPU1 重大エラー発生検出
1056 WAIT_SEC 2
1057 DH. ATMC_EACH_DIS 0x33 # //MDP-DPU2 重大エラー発生検出
1058 WAIT_SEC 2
1059 DH. ATMC_EACH_DIS 0x34 # //MDP-DPU1 DMC 通信切れ発生検出 1
1060 WAIT_SEC 2
1061 DH. ATMC_EACH_DIS 0x35 # //MDP-DPU1 DMC 通信切れ発生検出 2
1062 WAIT_SEC 2
1063 DH. ATMC_EACH_DIS 0x36 # //MDP-DPU2 DMC 通信切れ発生検出 1
1064 WAIT_SEC 2
1065 DH. ATMC_EACH_DIS 0x37 # //MDP-DPU2 DMC 通信切れ発生検出 2
1066 WAIT_SEC 2
1067 DH. ATMC_EACH_DIS 0x38 # //MDP-DPU1 冗長時 重大エラー発生検出
1068 WAIT_SEC 2
1069 DH. ATMC_EACH_DIS 0x39 # //MDP-DPU2 冗長時 重大エラー発生検出
1070 WAIT_SEC 2
1071 DH. ATMC_EACH_DIS 0x3A # //MDP-DPU1 冗長時 DMC 通信切れ発生検出 1
1072 WAIT_SEC 2
1073 DH. ATMC_EACH_DIS 0x3B # //MDP-DPU1 冗長時 DMC 通信切れ発生検出 2
1074 WAIT_SEC 2
1075 DH. ATMC_EACH_DIS 0x3C # //MDP-DPU2 冗長時 DMC 通信切れ発生検出 1
1076 WAIT_SEC 2
1077 DH. ATMC_EACH_DIS 0x3D # //MDP-DPU2 冗長時 DMC 通信切れ発生検出 2
1078 WAIT_SEC 2
1079 DH. ATMC_EACH_DIS 0x3E # // MDP1 User Request Error Clear
1080 WAIT_SEC 2
1081 DH. ATMC_EACH_DIS 0x3F # // MDP2 User Request Error Clear
1082 #
1083 # < CHECK >
1084 CHECK DH. ATMC_ENA_DIS00 =DIS
1085 CHECK DH. ATMC_ENA_DIS01 =ENA
1086 CHECK DH. ATMC_ENA_DIS02 =DIS
1087 CHECK DH. ATMC_ENA_DIS03 =DIS
1088 CHECK DH. ATMC_ENA_DIS04 =DIS
1089 CHECK DH. ATMC_ENA_DIS05 =DIS

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| 1090 | CHECK DH. ATMC_ENA_DIS06 =DIS |
| 1091 | CHECK DH. ATMC_ENA_DIS07 =DIS |
| 1092 | CHECK DH. ATMC_ENA_DIS08 =DIS |
| 1093 | CHECK DH. ATMC_ENA_DIS09 =ENA |
| 1094 | CHECK DH. ATMC_ENA_DIS0A =DIS |
| 1095 | CHECK DH. ATMC_ENA_DIS0B =DIS |
| 1096 | CHECK DH. ATMC_ENA_DIS0C =DIS |
| 1097 | CHECK DH. ATMC_ENA_DIS0D =DIS |
| 1098 | CHECK DH. ATMC_ENA_DIS0E =DIS |
| 1099 | CHECK DH. ATMC_ENA_DIS0F =DIS |
| 1100 | CHECK DH. ATMC_ENA_DIS10 =DIS |
| 1101 | CHECK DH. ATMC_ENA_DIS11 =DIS |
| 1102 | CHECK DH. ATMC_ENA_DIS12 =DIS |
| 1103 | CHECK DH. ATMC_ENA_DIS13 =DIS |
| 1104 | CHECK DH. ATMC_ENA_DIS14 =DIS |
| 1105 | CHECK DH. ATMC_ENA_DIS15 =DIS |
| 1106 | CHECK DH. ATMC_ENA_DIS16 =DIS |
| 1107 | CHECK DH. ATMC_ENA_DIS17 =DIS |
| 1108 | CHECK DH. ATMC_ENA_DIS18 =DIS |
| 1109 | CHECK DH. ATMC_ENA_DIS19 =DIS |
| 1110 | CHECK DH. ATMC_ENA_DIS1A =DIS |
| 1111 | CHECK DH. ATMC_ENA_DIS1B =DIS |
| 1112 | CHECK DH. ATMC_ENA_DIS1C =DIS |
| 1113 | CHECK DH. ATMC_ENA_DIS1D =DIS |
| 1114 | CHECK DH. ATMC_ENA_DIS1E =DIS |
| 1115 | CHECK DH. ATMC_ENA_DIS1F =DIS |
| 1116 | CHECK DH. ATMC_ENA_DIS20 =DIS |
| 1117 | CHECK DH. ATMC_ENA_DIS21 =DIS |
| 1118 | CHECK DH. ATMC_ENA_DIS22 =DIS |
| 1119 | CHECK DH. ATMC_ENA_DIS23 =DIS |
| 1120 | CHECK DH. ATMC_ENA_DIS24 =DIS |
| 1121 | CHECK DH. ATMC_ENA_DIS25 =DIS |
| 1122 | CHECK DH. ATMC_ENA_DIS26 =DIS |
| 1123 | CHECK DH. ATMC_ENA_DIS27 =DIS |
| 1124 | CHECK DH. ATMC_ENA_DIS28 =DIS |
| 1125 | CHECK DH. ATMC_ENA_DIS29 =DIS |
| 1126 | CHECK DH. ATMC_ENA_DIS2A =DIS |
| 1127 | CHECK DH. ATMC_ENA_DIS2B =ENA |
| 1128 | CHECK DH. ATMC_ENA_DIS2C =DIS |
| 1129 | CHECK DH. ATMC_ENA_DIS2D =DIS |
| 1130 | CHECK DH. ATMC_ENA_DIS2E =DIS |
| 1131 | CHECK DH. ATMC_ENA_DIS2F =DIS |
| 1132 | CHECK DH. ATMC_ENA_DIS30 =DIS |
| 1133 | CHECK DH. ATMC_ENA_DIS31 =DIS |
| 1134 | CHECK DH. ATMC_ENA_DIS32 =DIS |
| 1135 | CHECK DH. ATMC_ENA_DIS33 =DIS |
| 1136 | CHECK DH. ATMC_ENA_DIS34 =DIS |
| 1137 | CHECK DH. ATMC_ENA_DIS35 =DIS |
| 1138 | CHECK DH. ATMC_ENA_DIS36 =DIS |
| 1139 | CHECK DH. ATMC_ENA_DIS37 =DIS |
| 1140 | CHECK DH. ATMC_ENA_DIS38 =DIS |
| 1141 | CHECK DH. ATMC_ENA_DIS39 =DIS |

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1142 CHECK DH. ATMC_ENA_DIS3A =DIS
1143 CHECK DH. ATMC_ENA_DIS3B =DIS
1144 CHECK DH. ATMC_ENA_DIS3C =DIS
1145 CHECK DH. ATMC_ENA_DIS3D =DIS
1146 CHECK DH. ATMC_ENA_DIS3E =DIS
1147 CHECK DH. ATMC_ENA_DIS3F =DIS
1148 #
1149 #
1150 . # << RQMC_ENA/DIS >>
1151 . #
1152 . DH. RQMC_EACH_DIS 0x00 # N/A
1153 WAIT_SEC 2
1154 DH. RQMC_EACH_DIS 0x01 # // BAT CEL UVC
1155 WAIT_SEC 2
1156 DH. RQMC_EACH_DIS 0x02 # N/A
1157 WAIT_SEC 2
1158 DH. RQMC_EACH_DIS 0x03 # N/A
1159 WAIT_SEC 2
1160 DH. RQMC_EACH_DIS 0x04 # N/A
1161 WAIT_SEC 2
1162 DH. RQMC_EACH_DIS 0x05 # N/A
1163 WAIT_SEC 2
1164 DH. RQMC_EACH_DIS 0x06 # N/A
1165 WAIT_SEC 2
1166 DH. RQMC_EACH_DIS 0x07 # N/A
1167 WAIT_SEC 2
1168 DH. RQMC_EACH_DIS 0x08 # // MEA1-PSU-OFF
1169 WAIT_SEC 2
1170 DH. RQMC_EACH_DIS 0x09 # // MEA2-PSU-OFF
1171 WAIT_SEC 2
1172 DH. RQMC_EACH_DIS 0x0A # // MIA-PSU-OFF
1173 WAIT_SEC 2
1174 DH. RQMC_EACH_DIS 0x0B # // MSA-PSU-OFF
1175 WAIT_SEC 2
1176 DH. RQMC_EACH_DIS 0x0C # N/A
1177 WAIT_SEC 2
1178 DH. RQMC_EACH_DIS 0x0D # // HEP-I-PSU-OFF
1179 WAIT_SEC 2
1180 DH. RQMC_EACH_DIS 0x0E # // ENA-PSU-OFF
1181 WAIT_SEC 2
1182 DH. RQMC_EACH_DIS 0x0F # N/A
1183 WAIT_SEC 2
1184 DH. RQMC_EACH_DIS 0x10 # // スピンレート制御正常終了 (Req A)
1185 WAIT_SEC 2
1186 DH. RQMC_EACH_DIS 0x11 # // 太陽角監視異常 (Req C)
1187 WAIT_SEC 2
1188 DH. RQMC_EACH_DIS 0x12 # // 太陽角制御異常 (Req D)
1189 WAIT_SEC 2
1190 DH. RQMC_EACH_DIS 0x13 # // Sun Pulse チェック異常時 (Req E)
1191 WAIT_SEC 2
1192 DH. RQMC_EACH_DIS 0x14 # // 分離時 スピニアップ異常時 (Req 初期 F/Req F)
1193 WAIT_SEC 2

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1194 DH.RQMC_EACH_DIS 0x15 # // ADM 回転異常 (Req G)
1195 WAIT_SEC 2
1196 DH.RQMC_EACH_DIS 0x16 # // RGS 圧力センサ異常 (Req H)
1197 WAIT_SEC 2
1198 DH.RQMC_EACH_DIS 0x17 # // 定常時 CPU リセット (Req I)
1199 WAIT_SEC 2
1200 DH.RQMC_EACH_DIS 0x18 # N/A
1201 WAIT_SEC 2
1202 DH.RQMC_EACH_DIS 0x19 # N/A
1203 WAIT_SEC 2
1204 DH.RQMC_EACH_DIS 0x1A # N/A
1205 WAIT_SEC 2
1206 DH.RQMC_EACH_DIS 0x1B # N/A
1207 WAIT_SEC 2
1208 DH.RQMC_EACH_DIS 0x1C # N/A
1209 WAIT_SEC 2
1210 DH.RQMC_EACH_DIS 0x1D # N/A
1211 WAIT_SEC 2
1212 DH.RQMC_EACH_DIS 0x1E # N/A
1213 WAIT_SEC 2
1214 DH.RQMC_EACH_DIS 0x1F # N/A
1215 #
1216 # < CHECK >
1217 CHECK DH.RQMC_ENA_DIS00 =DIS
1218 CHECK DH.RQMC_ENA_DIS01 =DIS
1219 CHECK DH.RQMC_ENA_DIS02 =DIS
1220 CHECK DH.RQMC_ENA_DIS03 =DIS
1221 CHECK DH.RQMC_ENA_DIS04 =DIS
1222 CHECK DH.RQMC_ENA_DIS05 =DIS
1223 CHECK DH.RQMC_ENA_DIS06 =DIS
1224 CHECK DH.RQMC_ENA_DIS07 =DIS
1225 CHECK DH.RQMC_ENA_DIS08 =DIS
1226 CHECK DH.RQMC_ENA_DIS09 =DIS
1227 CHECK DH.RQMC_ENA_DIS0A =DIS
1228 CHECK DH.RQMC_ENA_DIS0B =DIS
1229 CHECK DH.RQMC_ENA_DIS0C =DIS
1230 CHECK DH.RQMC_ENA_DIS0D =DIS
1231 CHECK DH.RQMC_ENA_DIS0E =DIS
1232 CHECK DH.RQMC_ENA_DIS0F =DIS
1233 CHECK DH.RQMC_ENA_DIS10 =DIS
1234 CHECK DH.RQMC_ENA_DIS11 =DIS
1235 CHECK DH.RQMC_ENA_DIS12 =DIS
1236 CHECK DH.RQMC_ENA_DIS13 =DIS
1237 CHECK DH.RQMC_ENA_DIS14 =DIS
1238 CHECK DH.RQMC_ENA_DIS15 =DIS
1239 CHECK DH.RQMC_ENA_DIS16 =DIS
1240 CHECK DH.RQMC_ENA_DIS17 =DIS
1241 CHECK DH.RQMC_ENA_DIS18 =DIS
1242 CHECK DH.RQMC_ENA_DIS19 =DIS
1243 CHECK DH.RQMC_ENA_DIS1A =DIS
1244 CHECK DH.RQMC_ENA_DIS1B =DIS
1245 CHECK DH.RQMC_ENA_DIS1C =DIS

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1246 CHECK DH.RQMC_ENA_DIS1D =DIS
1247 CHECK DH.RQMC_ENA_DIS1E =DIS
1248 CHECK DH.RQMC_ENA_DIS1F =DIS
1249 #
1250 #
1251 . # << SYSTME_ENA/DIS >>
1252 . #
1253 . DH.SYSTMR_EACH_DIS 0x00 # //XSW2 クロス時 Command 受信間隔 Timer
1254 WAIT_SEC 2
1255 DH.SYSTMR_EACH_DIS 0x01 # //XSW2 ストレート時 Command 受信間隔 Timer
1256 WAIT_SEC 2
1257 DH.SYSTMR_EACH_DIS 0x02 # //MDP-DPU1 系 Mission 機器 OFF
1258 WAIT_SEC 2
1259 DH.SYSTMR_EACH_DIS 0x03 # //MDP-DPU2 系 Mission 機器 OFF
1260 WAIT_SEC 2
1261 DH.SYSTMR_EACH_DIS 0x04 # //分離時 Backup Timer
1262 WAIT_SEC 2
1263 DH.SYSTMR_EACH_DIS 0x05 # //Mission 機器 ALL OFF
1264 WAIT_SEC 2
1265 DH.SYSTMR_EACH_DIS 0x06 # //分離時 Sun Acquisition Start
1266 WAIT_SEC 2
1267 DH.SYSTMR_EACH_DIS 0x07 # //分離時 可視時間終了後通信系 OFF
1268 #
1269 . # < CHECK >
1270 CHECK DH.SYSTMR_ENA_DIS0 =DIS
1271 CHECK DH.SYSTMR_ENA_DIS1 =DIS
1272 CHECK DH.SYSTMR_ENA_DIS2 =DIS
1273 CHECK DH.SYSTMR_ENA_DIS3 =DIS
1274 CHECK DH.SYSTMR_ENA_DIS4 =DIS
1275 CHECK DH.SYSTMR_ENA_DIS5 =DIS
1276 CHECK DH.SYSTMR_ENA_DIS6 =DIS
1277 CHECK DH.SYSTMR_ENA_DIS7 =DIS
1278 . #
1279 . # # AT 機能を ENA にする
1280 . DH.ATMC_ENA
1281 . #
1282 . # < CHECK >
1283 CHECK DH.ATMC_ENA_DIS =ENA
1284 . #
1285 . # # RQ waiting に設定する。
1286 . DH.RQMC_ENA
1287 . #
1288 . # < CHECK >
1289 CHECK DH.RQMC_ENA_DIS =ENA
1290 . #
1291 . # # システムタイマー機能を有効にする。
1292 . DH.SYSTMR_ENA
1293 . #
1294 . # < CHECK >
1295 CHECK DH.SYSTMR_ENA_DIS =ENA
1296 . #
1297 . # QL Check-Item Spec Check

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1298 . # -----+-----+-----+-----
1299 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1300 . #
1301 . # =====
1302 . # 1.4. SI 機器初期チェックアウト時設定
1303 . # =====
1304 . #
1305 . # 【SI 系初期 CO 試験時の探査機コンフィギュレーション】
1306 . # [COM/DH]
1307 . # XTRP: ON & TX OFF, TWTA: OFF
1308 . #
1309 . # [DMCS/DRU]
1310 . # DMC: ON Dual Mode, 1553B 通信
1311 . # DRU: ON, Heater: NMNL
1312 . #
1313 . # [ACS]
1314 . # RCS: DRV OFF, APM: DRV OFF, ADMCE: OFF
1315 . # SSAS: OFF, SSC: OFF
1316 . # 疑似パルス出力
1317 . #
1318 . # [PCS]
1319 . # APR: OFF, PCD: ON
1320 . # BAT: INT
1321 . # BAT 充放電: 不要
1322 . #
1323 . # [SI]
1324 . # MDP: ON
1325 . # その他 SI 機器: CO 対象機器について ON
1326 . # 初期 CO 試験用 機器 ON/OFF コンフィギュレーション確認
1327 . #
1328 . # < CHECK >
1329 . CHECK PCD.XTRP_A_ON_OFF =ON
1330 . CHECK PCD.XTRP_B_ON_OFF =ON
1331 . CHECK PCD.EPC_A_ON_OFF =OFF
1332 . CHECK PCD.EPC_B_ON_OFF =OFF
1333 . CHECK DH.XTRP_A_TX_ON_OFF =OFF
1334 . CHECK DH.XTRP_B_TX_ON_OFF =OFF
1335 . CHECK PCD.EPC_A_ON_OFF =OFF
1336 . CHECK PCD.EPC_B_ON_OFF =OFF
1337 . CHECK PCD.DRU_PSU_A_ON_OFF =ON
1338 . CHECK PCD.DRU_PSU_B_ON_OFF =OFF
1339 . CHECK TCS.MODE_ST =NMNL
1340 . CHECK ACS_SYS.RCS_A_B_ST =OFF
1341 . CHECK ACS_SYS.STS_DRUAPM_ON =OFF
1342 . CHECK PCD.ADMCE_A_ON_OFF =OFF
1343 . CHECK PCD.ADMCE_B_ON_OFF =OFF
1344 . CHECK ACS_SYS.STS_SSASA_PWR =OFF
1345 . CHECK ACS_SYS.STS_SSASB_PWR =OFF
1346 . CHECK ACS_SYS.SSC_PWR =OFF
1347 . CHECK PCD.APR1_ON_OFF =OFF
1348 . CHECK PCD.APR2_ON_OFF =ON
1349 . CHECK PCD.PSU_MDP1_ON_OFF =OFF

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1350 CHECK PCD. PSU_MDP2_ON_OFF =OFF
1351 CHECK PCD. PSU_MORTOR_A_ON_OFF =OFF
1352 CHECK PCD. PSU_MORTOR_B_ON_OFF =OFF
1353 CHECK PCD. PSU_ENA_ON_OFF =OFF
1354 CHECK PCD. PSU_MEA1_ON_OFF =OFF
1355 CHECK PCD. PSU_MEA2_ON_OFF =OFF
1356 CHECK PCD. PSU_MSA_ON_OFF =OFF
1357 CHECK PCD. PSU_MIA_ON_OFF =OFF
1358 CHECK PCD. PSU_HEP_E_ON_OFF =OFF
1359 CHECK PCD. PSU_HEP_I_ON_OFF =OFF
1360 CHECK PCD. PSU_PME_ON_OFF =OFF
1361 CHECK PCD. PSU_MGF_I_ON_OFF =OFF
1362 CHECK PCD. MDM_E_ON_OFF =OFF
1363 CHECK PCD. MSASI_ON_OFF =OFF
1364 CHECK PCD. SURVIVAL_HTR_ON_OFF =ON
1365 #
1366 . # QL Check-Item Spec Check
1367 # -----+-----+-----+-----
1368 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1369 #
1370 . # *****
1371 . # 2. MDP 立ち上げ
1372 . # *****
1373 . #
1374 . DH.MC_EACH_EXE 0x002C # 1553B TLM mode 0 (SYS HK 1)
1375 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
1376 #: 0x00 DH. DMP_PERIOD 0x04
1377 #: 0x04 ACS. ACS_PKT_FMT_1
1378 #: 0x05 DH. OPE_MODE 0x0C 0x0A 0x00 0x00 0x00000000000000000000000000000000
1379 #: 0x06 DH. TLMFMT1553B_CHG 0x00
1380 WAIT_SEC 40
1381 #
1382 # < CHECK >
1383 CHECK DH. DMP_PERIOD =SEC_16
1384 CHECK DH. CATEGORY_TBL_NO 0x00
1385 CHECK DH. DWNLNK_RATE =BPS16384
1386 CHECK DH. TLM1553BFMTNO 0x00
1387 #
1388 # MDP-DPU1,2(A 系) の LINK Enable を確認
1389 #
1390 # < CHECK >
1391 CHECK DH. SPW_POTMD_L1 =ENA
1392 CHECK DH. SPW_POTMD_L2 =ENA
1393 #
1394 . # MDP-DPU1,2 を電源 ON する。(HW_STATE1 は 16 秒以内にチェックすること。)
1395 #
1396 # <<< MDP-DPU1/2 Power ON >>>
1397 #
1398 . DH. MDP_DPU12_POWER_ON # DH. MC_EACH_EXE 0x099
1399 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
1400 #: 0x00 PCD. ON_OFF_ENA
1401 #: 0x01 PCD. PSU_MDP1_ON

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1402 #:          0x02 PCD.PSU_MDP2_ON
1403 #:          0x03 PCD.ON_OFF_DIS
1404 #
1405 #
1406 # < CHECK >
1407 CHECK MD1F.DPU_HW_STATE1          =DPU_RESET
1408 CHECK MD2F.DPU_HW_STATE1          =DPU_RESET
1409 CHECK PCD.ON_OFF_ENA_DIS          =DIS
1410 CHECK PCD.PSU_MDP1_ON_OFF          =ON
1411 CHECK PCD.PSU_MDP2_ON_OFF          =ON
1412 CHECK DH.SPW_LNKST_L1              =RUN
1413 CHECK DH.SPW_LNKST_L2              =RUN
1414 #
1415 # *****
1416 # WAIT 16 sec (HW Initialization of DPU1 & DPU2)
1417 # *****
1418 WAIT_SEC 40
1419 #
1420 # <<< MDP-DPU Initial HK check (2): 16 sec after PWR-ON >>>
1421 #
1422 # < CHECK >
1423 CHECK MD1F.DPU_HW_STATE1          =DPU_RUN
1424 CHECK MD1F.DPU_HW_STATE2          =CPUOFF_DPUBUS
1425 CHECK MD1F.DMC_IF_RMAPERR          =na
1426 CHECK MD1F.DMC_IF_2BITERR          =na
1427 CHECK MD1F.DMC_IF_BTOERR           =na
1428 CHECK MD1F.BTO_ERR                 =na
1429 CHECK MD1F.WDT_ERR                 =na
1430 CHECK MD1F.DPU_SELECT              =DPU1
1431 CHECK MD2F.DPU_HW_STATE1          =DPU_RUN
1432 CHECK MD2F.DPU_HW_STATE2          =CPUOFF_DPUBUS
1433 CHECK MD2F.DMC_IF_RMAPERR          =na
1434 CHECK MD2F.DMC_IF_2BITERR          =na
1435 CHECK MD2F.DMC_IF_BTOERR           =na
1436 CHECK MD2F.BTO_ERR                 =na
1437 CHECK MD2F.WDT_ERR                 =na
1438 CHECK MD2F.DPU_SELECT              =DPU2
1439 #
1440 # <<< MDP-DPU CPU_ON / RUN >>>
1441 DH.MDP_DPU12_CPU_RUN # DH.MC_EACH_EXE 0x009A
1442 #:          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
1443 #:          0x00 DH.ATMC_EACH_ENA 0x34
1444 #:          0x01 DH.SPW RTEFDIR_SET 0x1E
1445 #:          0x05 DH.ATMC_EACH_ENA 0x36
1446 #:          0x06 MD1F.CPU_ON
1447 #:          0x07 MD2F.CPU_ON
1448 #:          0x08 MD1F.CPU_RUN
1449 #:          0x09 MD2F.CPU_RUN
1450 #:          0x0A DH.ATMC_EACH_ENA 0x30
1451 #:          0x0F DH.ATMC_EACH_ENA 0x31
1452 WAIT_SEC 180
1453 # ここで 180 秒待機 (MDP MiddleWare による初期化終了待ち)

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1454 #
1455 # < CHECK >
1456 CHECK DH. SPW_FDIR_ENA_MDP1      =ENA
1457 CHECK DH. SPW_FDIR_ENA_MDP2      =ENA
1458 CHECK DH. SPW_FDIR_ENA_PCD       =ENA
1459 CHECK DH. SPW_FDIR_ENA_RMTRTR    =ENA
1460 CHECK MD1F.DPU_HW_STATE2          =CPU_RUN
1461 CHECK MD2F.DPU_HW_STATE2          =CPU_RUN
1462 #
1463 #
1464 # NECP で-updatewait は使用できないのでコメントアウト
1465 #
1466 # < MDP1 SATELITE TIME CHECK (0x200/s) >
1467 #
1468 # CHECK MD1F.SAT_TIME -updatewait 180
1469 # LET LocalA0 = {MD1F.SAT_TIME} & 0xFFFFFFFF
1470 # CHECK MD1F.SAT_TIME -updatewait 4
1471 # LET LocalA1 = {MD1F.SAT_TIME} & 0xFFFFFFFF
1472 # LET LocalA1 = LocalA1 - LocalA0
1473 # SYSTEM test {LocalA1} = 512
1474 #
1475 # < MDP1 SUNPULSE TIME CHECK (0x800-0xB00/4s) >
1476 #
1477 # CHECK MD1F.SUNPULSE_TIME -updatewait 8
1478 # LET LocalA0 = {MD1F.SUNPULSE_TIME} & 0xFFFFFFFF
1479 # CHECK MD1F.SUNPULSE_TIME -updatewait 8
1480 # LET LocalA1 = {MD1F.SUNPULSE_TIME} & 0xFFFFFFFF
1481 # LET LocalA1 = LocalA1 - LocalA0
1482 # SYSTEM test {LocalA1} -ge 2048 -a {LocalA1} -le 2816
1483 #
1484 # < MDP2 SATELITE TIME CHECK (0x200/s) >
1485 #
1486 # CHECK MD2F.SAT_TIME -updatewait 8
1487 # LET LocalA0 = {MD2F.SAT_TIME} & 0xFFFFFFFF
1488 # CHECK MD2F.SAT_TIME -updatewait 4
1489 # LET LocalA1 = {MD2F.SAT_TIME} & 0xFFFFFFFF
1490 # LET LocalA1 = LocalA1 - LocalA0
1491 # SYSTEM test {LocalA1} = 512
1492 #
1493 # < MDP2 SUNPULSE TIME CHECK (0x800-0xB00/4s) >
1494 #
1495 # CHECK MD2F.SUNPULSE_TIME -updatewait 8
1496 # LET LocalA0 = {MD2F.SUNPULSE_TIME} & 0xFFFFFFFF
1497 # CHECK MD2F.SUNPULSE_TIME -updatewait 8
1498 # LET LocalA1 = {MD2F.SUNPULSE_TIME} & 0xFFFFFFFF
1499 # LET LocalA1 = LocalA1 - LocalA0
1500 # SYSTEM test {LocalA1} -ge 2048 -a {LocalA1} -le 2816
1501 #
1502 #
1503 # <<< MDP-DPU Initial HK check (3): after the Initialization >>>
1504 # CPU_RUN 時、確率 7/8 で SLOT_ERR 起きるが問題ない(起動時の SLOT 番号齟齬)
1505 #

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| 1506 | # < CHECK >                |          |
| 1507 | CHECK MD1F.DPU_HW_STATE1   | =DPU_RUN |
| 1508 | CHECK MD1F.DPU_HW_STATE2   | =CPU_RUN |
| 1509 | CHECK MD1F.DMC_IF_RMAPERR  | =na      |
| 1510 | CHECK MD1F.DMC_IF_2BITERR  | =na      |
| 1511 | CHECK MD1F.DMC_IF_BTOERR   | =na      |
| 1512 | CHECK MD1F.BTO_ERR         | =na      |
| 1513 | CHECK MD1F.WDT_ERR         | =na      |
| 1514 | CHECK MD1F.DPU_SELECT      | =DPU1    |
| 1515 | CHECK MD1F.DPU_SW_STATE    | 2        |
| 1516 | CHECK MD1F.SW_CMD_CODE     | 0        |
| 1517 | CHECK MD1F.SW_CMD_CNT      | 0        |
| 1518 | CHECK MD1F.SW_CMD_REJ_CNT  | 0        |
| 1519 | CHECK MD1F.SW_CMD_REJ_CODE | 0        |
| 1520 | CHECK MD1F.MS_CMD_PRIO     | =DMC     |
| 1521 | CHECK MD1F.RDN_MODE        | =OFF     |
| 1522 | CHECK MD1F.SUNPULSE_MODE   | =NML     |
| 1523 | CHECK MD1F.LOAD_DUMP_DIS   | =DIS     |
| 1524 | CHECK MD1F.MEMLOAD_BUSY    | =OFF     |
| 1525 | CHECK MD1F.MEMLOAD_NODE    | 0        |
| 1526 | CHECK MD1F.MEMDUMP_BUSY    | =OFF     |
| 1527 | CHECK MD1F.MEMDUMP_NODE    | 0        |
| 1528 | CHECK MD1F.TASK_STAT_AP01  | =OFF     |
| 1529 | CHECK MD1F.TASK_STAT_AP02  | =OFF     |
| 1530 | CHECK MD1F.TASK_STAT_AP03  | =OFF     |
| 1531 | CHECK MD1F.TASK_STAT_AP04  | =OFF     |
| 1532 | CHECK MD1F.TASK_STAT_AP05  | =OFF     |
| 1533 | CHECK MD1F.TASK_STAT_AP06  | =OFF     |
| 1534 | CHECK MD1F.TASK_STAT_AP07  | =OFF     |
| 1535 | CHECK MD1F.TASK_STAT_AP08  | =OFF     |
| 1536 | CHECK MD1F.TASK_STAT_AP09  | =OFF     |
| 1537 | CHECK MD1F.TASK_STAT_AP10  | =OFF     |
| 1538 | CHECK MD1F.TASK_STAT_AP11  | =OFF     |
| 1539 | CHECK MD1F.TASK_STAT_AP12  | =OFF     |
| 1540 | CHECK MD1F.TASK_STAT_AP13  | =OFF     |
| 1541 | CHECK MD1F.TASK_STAT_AP14  | =OFF     |
| 1542 | CHECK MD1F.TASK_STAT_AP15  | =OFF     |
| 1543 | CHECK MD1F.TASK_STAT_AP16  | =OFF     |
| 1544 | CHECK MD1F.UNEXPECT_CNT    | 0        |
| 1545 | CHECK MD1F.UNEXPECT_CODE   | 0        |
| 1546 | CHECK MD1F.DMC_IF_ERR      | =na      |
| 1547 | CHECK MD1F.TIME_ERR        | =na      |
| 1548 | CHECK MD1F.INST_CACHE_ERR  | =na      |
| 1549 | CHECK MD1F.DATA_CACHE_ERR  | =na      |
| 1550 | CHECK MD1F.SRAM_1BERR      | =na      |
| 1551 | CHECK MD1F.CMD_BUFF_1BERR  | =na      |
| 1552 | CHECK MD1F.CMD_BUFF_2BERR  | =na      |
| 1553 | CHECK MD1F.TLM_BUFF_1BERR  | =na      |
| 1554 | CHECK MD1F.TLM_BUFF_2BERR  | =na      |
| 1555 | CHECK MD1F.COMP_1BERR      | =na      |
| 1556 | CHECK MD1F.DS_1BERR        | =na      |
| 1557 | CHECK MD1F.DS_2BERR        | =na      |

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| 1558 | CHECK MD1F.RDN_LINK_STAT   | =ON      |
| 1559 | CHECK MD1F.RIF_LINK_ERR    | =na      |
| 1560 | CHECK MD1F.RIF_ERR         | =na      |
| 1561 | CHECK MD1F.MS0_LINK_STAT   | =OFF     |
| 1562 | CHECK MD1F.MS1_LINK_STAT   | =OFF     |
| 1563 | CHECK MD1F.MS2_LINK_STAT   | =OFF     |
| 1564 | CHECK MD1F.MS3_LINK_STAT   | =OFF     |
| 1565 | CHECK MD1F.MS4_LINK_STAT   | =OFF     |
| 1566 | CHECK MD1F.MS5_LINK_STAT   | =OFF     |
| 1567 | CHECK MD1F.MS6_LINK_STAT   | =OFF     |
| 1568 | CHECK MD1F.MS7_LINK_STAT   | =OFF     |
| 1569 | CHECK MD1F.MS0_DAT_CLCT    | =OFF     |
| 1570 | CHECK MD1F.MS1_DAT_CLCT    | =OFF     |
| 1571 | CHECK MD1F.MS2_DAT_CLCT    | =OFF     |
| 1572 | CHECK MD1F.MS3_DAT_CLCT    | =OFF     |
| 1573 | CHECK MD1F.MS4_DAT_CLCT    | =OFF     |
| 1574 | CHECK MD1F.MS5_DAT_CLCT    | =OFF     |
| 1575 | CHECK MD1F.MS6_DAT_CLCT    | =OFF     |
| 1576 | CHECK MD1F.MS7_DAT_CLCT    | =OFF     |
| 1577 | CHECK MD1F.MS0_BUFF_SEL    | =L       |
| 1578 | CHECK MD1F.MS1_BUFF_SEL    | =L       |
| 1579 | CHECK MD1F.MS2_BUFF_SEL    | =L       |
| 1580 | CHECK MD1F.MS3_BUFF_SEL    | =L       |
| 1581 | CHECK MD1F.MS4_BUFF_SEL    | =L       |
| 1582 | CHECK MD1F.MS5_BUFF_SEL    | =L       |
| 1583 | CHECK MD1F.MS6_BUFF_SEL    | =L       |
| 1584 | CHECK MD1F.MS7_BUFF_SEL    | =L       |
| 1585 | CHECK MD1F.MS0_SGL_CMD_ERR | =na      |
| 1586 | CHECK MD1F.MS1_SGL_CMD_ERR | =na      |
| 1587 | CHECK MD1F.MS2_SGL_CMD_ERR | =na      |
| 1588 | CHECK MD1F.MS3_SGL_CMD_ERR | =na      |
| 1589 | CHECK MD1F.MS4_SGL_CMD_ERR | =na      |
| 1590 | CHECK MD1F.MS5_SGL_CMD_ERR | =na      |
| 1591 | CHECK MD1F.MS6_SGL_CMD_ERR | =na      |
| 1592 | CHECK MD1F.MS7_SGL_CMD_ERR | =na      |
| 1593 | CHECK MD1F.MS0_MTI_CMD_ERR | =na      |
| 1594 | CHECK MD1F.MS1_MTI_CMD_ERR | =na      |
| 1595 | CHECK MD1F.MS2_MTI_CMD_ERR | =na      |
| 1596 | CHECK MD1F.MS3_MTI_CMD_ERR | =na      |
| 1597 | CHECK MD1F.MS4_MTI_CMD_ERR | =na      |
| 1598 | CHECK MD1F.MS5_MTI_CMD_ERR | =na      |
| 1599 | CHECK MD1F.MS6_MTI_CMD_ERR | =na      |
| 1600 | CHECK MD1F.MS7_MTI_CMD_ERR | =na      |
| 1601 | CHECK MD2F.DPU_HW_STATE1   | =DPU_RUN |
| 1602 | CHECK MD2F.DPU_HW_STATE2   | =CPU_RUN |
| 1603 | CHECK MD2F.DMC_IF_RMAPERR  | =na      |
| 1604 | CHECK MD2F.DMC_IF_2BITERR  | =na      |
| 1605 | CHECK MD2F.DMC_IF_BTOERR   | =na      |
| 1606 | CHECK MD2F.BTO_ERR         | =na      |
| 1607 | CHECK MD2F.WDT_ERR         | =na      |
| 1608 | CHECK MD2F.DPU_SELECT      | =DPU2    |
| 1609 | CHECK MD2F.DPU_SW_STATE    | 2        |

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| 1610 | CHECK MD2F. SW_CMD_CODE     | 0    |
| 1611 | CHECK MD2F. SW_CMD_CNT      | 0    |
| 1612 | CHECK MD2F. SW_CMD_REJ_CNT  | 0    |
| 1613 | CHECK MD2F. SW_CMD_REJ_CODE | 0    |
| 1614 | CHECK MD2F. MS_CMD_PRIO     | =DMC |
| 1615 | CHECK MD2F. RDN_MODE        | =OFF |
| 1616 | CHECK MD2F. SUNPULSE_MODE   | =NML |
| 1617 | CHECK MD2F. LOAD_DUMP_DIS   | =DIS |
| 1618 | CHECK MD2F. MEMLOAD_BUSY    | =OFF |
| 1619 | CHECK MD2F. MEMLOAD_NODE    | 0    |
| 1620 | CHECK MD2F. MEMDUMP_BUSY    | =OFF |
| 1621 | CHECK MD2F. MEMDUMP_NODE    | 0    |
| 1622 | CHECK MD2F. TASK_STAT_AP01  | =OFF |
| 1623 | CHECK MD2F. TASK_STAT_AP02  | =OFF |
| 1624 | CHECK MD2F. TASK_STAT_AP03  | =OFF |
| 1625 | CHECK MD2F. TASK_STAT_AP04  | =OFF |
| 1626 | CHECK MD2F. TASK_STAT_AP05  | =OFF |
| 1627 | CHECK MD2F. TASK_STAT_AP06  | =OFF |
| 1628 | CHECK MD2F. TASK_STAT_AP07  | =OFF |
| 1629 | CHECK MD2F. TASK_STAT_AP08  | =OFF |
| 1630 | CHECK MD2F. TASK_STAT_AP09  | =OFF |
| 1631 | CHECK MD2F. TASK_STAT_AP10  | =OFF |
| 1632 | CHECK MD2F. TASK_STAT_AP11  | =OFF |
| 1633 | CHECK MD2F. TASK_STAT_AP12  | =OFF |
| 1634 | CHECK MD2F. TASK_STAT_AP13  | =OFF |
| 1635 | CHECK MD2F. TASK_STAT_AP14  | =OFF |
| 1636 | CHECK MD2F. TASK_STAT_AP15  | =OFF |
| 1637 | CHECK MD2F. TASK_STAT_AP16  | =OFF |
| 1638 | CHECK MD2F. UNEXPECT_CNT    | 0    |
| 1639 | CHECK MD2F. UNEXPECT_CODE   | 0    |
| 1640 | CHECK MD2F. DMC_IF_ERR      | =na  |
| 1641 | CHECK MD2F. TIME_ERR        | =na  |
| 1642 | CHECK MD2F. INST_CACHE_ERR  | =na  |
| 1643 | CHECK MD2F. DATA_CACHE_ERR  | =na  |
| 1644 | CHECK MD2F. SRAM_1BERR      | =na  |
| 1645 | CHECK MD2F. CMD_BUFF_1BERR  | =na  |
| 1646 | CHECK MD2F. CMD_BUFF_2BERR  | =na  |
| 1647 | CHECK MD2F. TLM_BUFF_1BERR  | =na  |
| 1648 | CHECK MD2F. TLM_BUFF_2BERR  | =na  |
| 1649 | CHECK MD2F. COMP_1BERR      | =na  |
| 1650 | CHECK MD2F. DS_1BERR        | =na  |
| 1651 | CHECK MD2F. DS_2BERR        | =na  |
| 1652 | CHECK MD2F. RDN_LINK_STAT   | =ON  |
| 1653 | CHECK MD2F. RIF_LINK_ERR    | =na  |
| 1654 | CHECK MD2F. RIF_ERR         | =na  |
| 1655 | CHECK MD2F. MS0_LINK_STAT   | =OFF |
| 1656 | CHECK MD2F. MS1_LINK_STAT   | =OFF |
| 1657 | CHECK MD2F. MS2_LINK_STAT   | =OFF |
| 1658 | CHECK MD2F. MS3_LINK_STAT   | =OFF |
| 1659 | CHECK MD2F. MS4_LINK_STAT   | =OFF |
| 1660 | CHECK MD2F. MS5_LINK_STAT   | =OFF |
| 1661 | CHECK MD2F. MS6_LINK_STAT   | =OFF |

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1662 CHECK MD2F.MS7_LINK_STAT =OFF
1663 CHECK MD2F.MS1_DAT_CLCT =OFF
1664 CHECK MD2F.MS3_DAT_CLCT =OFF
1665 CHECK MD2F.MS5_DAT_CLCT =OFF
1666 CHECK MD2F.MS6_DAT_CLCT =OFF
1667 CHECK MD2F.MS7_DAT_CLCT =OFF
1668 CHECK MD2F.MS1_BUFF_SEL =L
1669 CHECK MD2F.MS3_BUFF_SEL =L
1670 CHECK MD2F.MS5_BUFF_SEL =L
1671 CHECK MD2F.MS6_BUFF_SEL =L
1672 CHECK MD2F.MS7_BUFF_SEL =L
1673 CHECK MD2F.MS0_SGL_CMD_ERR =na
1674 CHECK MD2F.MS1_SGL_CMD_ERR =na
1675 CHECK MD2F.MS2_SGL_CMD_ERR =na
1676 CHECK MD2F.MS3_SGL_CMD_ERR =na
1677 CHECK MD2F.MS4_SGL_CMD_ERR =na
1678 CHECK MD2F.MS5_SGL_CMD_ERR =na
1679 CHECK MD2F.MS6_SGL_CMD_ERR =na
1680 CHECK MD2F.MS7_SGL_CMD_ERR =na
1681 CHECK MD2F.MS0_MTI_CMD_ERR =na
1682 CHECK MD2F.MS1_MTI_CMD_ERR =na
1683 CHECK MD2F.MS2_MTI_CMD_ERR =na
1684 CHECK MD2F.MS3_MTI_CMD_ERR =na
1685 CHECK MD2F.MS4_MTI_CMD_ERR =na
1686 CHECK MD2F.MS5_MTI_CMD_ERR =na
1687 CHECK MD2F.MS6_MTI_CMD_ERR =na
1688 CHECK MD2F.MS7_MTI_CMD_ERR =na
1689 #
1690 #
1691 . # # Mission Data を 1553B 通信の半分の帯域 (約 4kbps) で出力するモードとする
1692 . DH.MC_EACH_EXE 0x0036 # 1553B TLM mode 10 (SYS HK 1/16, BUS User HK 1/8, DUMP 1/16,
1693 # # MDP User HK 1/8, Mission User HK 1/8, Mission Data 1/2)
1694 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
1695 #: 0x00 DH.DMP_PERIOD 0x04
1696 #: 0x01 DH.DR_PTLOGCYC_SET 0x00
1697 #: 0x02 ACS.ACS_PKT_FMT_1
1698 #: 0x05 DH.OPE_MODE 0x0C 0x3E 0x2C 0x04 0x000000000000000000000000
1699 #: 0x06 DH.TLMFMT1553B_CHG 0x0A
1700 #
1701 WAIT_SEC 40
1702 #
1703 CHECK DH.DMP_PERIOD =SEC_16
1704 CHECK DH.CATEGORY_TBL_NO 0x2C
1705 CHECK DH.DWNLNK_RATE =BPS16384
1706 CHECK DH.TLM1553BFMTNO 0x0A
1707 CHECK ACS.F_ACS_PKT_FMT =ACSHK1
1708 #
1709 #
1710 . # < MDP-DPU User-Task RUN : MDP-DPU1/2 APL START >
1711 . DH.MDP_DPU12_APL_START # DH.MC_EACH_EXE 0x009B
1712 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
1713 #: 0x00 DH.ATMC_EACH_ENA 0x32

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1714 #: 0x02 MD1F. APP01_START
1715 #: 0x03 MD1F. APP02_START
1716 #: 0x04 MD1F. APP03_START
1717 #: 0x05 MD1F. APP04_START
1718 #: 0x06 MD1F. APP05_START
1719 #: 0x07 DH. ATMC_EACH_ENA 0x33
1720 #: 0x08 MD2F. APP01_START
1721 #: 0x09 MD2F. APP02_START
1722 #: 0x0A MD2F. APP13_START
1723 #: 0x0B MD2F. APP14_START
1724 #: 0x0C MD2F. APP05_START
1725 #
1726 WAIT_SEC 40
1727 #
1728 #
1729 # < CHECK >
1730 CHECK DH. ATMC_ENA_DIS32 =ENA
1731 CHECK DH. ATMC_ENA_DIS33 =ENA
1732 #
1733 # QL      Check-Item      Spec      Check
1734 #-----+-----+-----+-----
1735 # MD1F    << DPU1 S/W HK >>
1736 #          SWCMD_CNT      (+5 increment)      N/A
1737 #          SWREJ_CNT      (no change)          N/A
1738 #          << APPLICATIONS >>
1739 #
1740 # < CHECK >
1741 CHECK MD1F. TASK_STAT_AP01 =ON
1742 CHECK MD1F. TASK_STAT_AP02 =ON
1743 CHECK MD1F. TASK_STAT_AP03 =ON
1744 CHECK MD1F. TASK_STAT_AP04 =ON
1745 CHECK MD1F. TASK_STAT_AP05 =ON
1746 #
1747 # QL      Check-Item      Spec      Check
1748 #-----+-----+-----+-----
1749 # MD2F    << DPU2 S/W HK >>
1750 #          SWCMD_CNT      (+5 increment)      N/A
1751 #          SWREJ_CNT      (no change)          N/A
1752 #          << APPLICATIONS >>
1753 #
1754 # < CHECK >
1755 CHECK MD2F. TASK_STAT_AP01 =ON
1756 CHECK MD2F. TASK_STAT_AP02 =ON
1757 CHECK MD2F. TASK_STAT_AP13 =ON
1758 CHECK MD2F. TASK_STAT_AP14 =ON
1759 CHECK MD2F. TASK_STAT_AP05 =ON
1760 #
1761 #
1762 # < MDP USER-TASK: Initial Sequence - MDP-DPU-INITSET>
1763 #
1764 #
1765 . DH. MDP_DPU12_INIT_SET      # DH. MC_EACH_EXE 0x080

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1766 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)  
 1767 #: 0x00 MD1U. HK\_SET 0xFF 0x08  
 1768 #: 0x01 MD1U. HK\_MODE\_CHK  
 1769 #: 0x02 MD1U. TLM\_CMP 0xFF 0x00 0x11  
 1770 #: 0x03 MD2U. HK\_SET 0xFF 0x08  
 1771 #: 0x04 MD2U. HK\_MODE\_CHK  
 1772 #: 0x05 MD2U. TLM\_CMP 0xFF 0x00 0x11  
 1773 #: 0x0E MD1U. WDT\_ENA  
 1774 #: 0x0F MD2U. WDT\_ENA

1775 WAIT\_SEC 5

1776 MD1U. HK\_SET 0xFF 0x04

1777 WAIT\_SEC 2

1778 MD2U. HK\_SET 0xFF 0x04

1779 WAIT\_SEC 40

1780 #

| #    | QL                           | Check-Item       | Spec             | Check |
|------|------------------------------|------------------|------------------|-------|
| 1781 | #                            |                  |                  |       |
| 1782 | #                            |                  |                  |       |
| 1783 | # MD1U                       | U_CmdDmc_GetCNT  | (increment: +5)  | N/A   |
| 1784 | #                            | U_CmdDmc_ExecCNT | (increment: +5)  | N/A   |
| 1785 | #                            | U_App03_Load     | (checked by MDP) |       |
| 1786 | #                            |                  |                  |       |
| 1787 | # MD2U                       | U_CmdDmc_GetCNT  | (increment: +5)  | N/A   |
| 1788 | #                            | U_CmdDmc_ExecCNT | (increment: +5)  | N/A   |
| 1789 | #                            | U_App13_Load     | (checked by MDP) |       |
| 1790 | #                            |                  |                  |       |
| 1791 | #                            | < CHECK >        |                  |       |
| 1792 | CHECK MD1U. U_WdtEna         |                  | =ENA             |       |
| 1793 | CHECK MD1U. U_HkMode         |                  | =CHK             |       |
| 1794 | CHECK MD1U. U_Hk_RepInt_DPU1 |                  | 4 4              |       |
| 1795 | CHECK MD1U. U_Hk_RepInt_ENA  |                  | 4 4              |       |
| 1796 | CHECK MD1U. U_Hk_RepInt_HEPE |                  | 4 4              |       |
| 1797 | CHECK MD1U. U_Hk_RepInt_HEPI |                  | 4 4              |       |
| 1798 | CHECK MD1U. U_Hk_RepInt_MEA1 |                  | 4 4              |       |
| 1799 | CHECK MD1U. U_Hk_RepInt_MEA2 |                  | 4 4              |       |
| 1800 | CHECK MD1U. U_Hk_RepInt_MGFO |                  | 4 4              |       |
| 1801 | CHECK MD1U. U_Hk_RepInt_MIA  |                  | 4 4              |       |
| 1802 | CHECK MD1U. U_Hk_RepInt_MSA  |                  | 4 4              |       |
| 1803 | CHECK MD1U. U_TlmML_CMP_DPU1 |                  | =CMP             |       |
| 1804 | CHECK MD1U. U_TlmML_CMP_ENA  |                  | =USR             |       |
| 1805 | CHECK MD1U. U_TlmML_CMP_HEPE |                  | =ARO             |       |
| 1806 | CHECK MD1U. U_TlmML_CMP_HEPI |                  | =ARO             |       |
| 1807 | CHECK MD1U. U_TlmML_CMP_MEA1 |                  | =USR             |       |
| 1808 | CHECK MD1U. U_TlmML_CMP_MEA2 |                  | =USR             |       |
| 1809 | CHECK MD1U. U_TlmML_CMP_MGFO |                  | =ARO             |       |
| 1810 | CHECK MD1U. U_TlmML_CMP_MIA  |                  | =USR             |       |
| 1811 | CHECK MD1U. U_TlmML_CMP_MSA  |                  | =USR             |       |
| 1812 | CHECK MD1U. U_TlmH_CMP_DPU1  |                  | =CMP             |       |
| 1813 | CHECK MD1U. U_TlmH_CMP_HEPE  |                  | =ARO             |       |
| 1814 | CHECK MD1U. U_TlmH_CMP_HEPI  |                  | =ARO             |       |
| 1815 | CHECK MD1U. U_TlmH_CMP_MEA1  |                  | =USR             |       |
| 1816 | CHECK MD1U. U_TlmH_CMP_MEA2  |                  | =USR             |       |
| 1817 | CHECK MD1U. U_TlmH_CMP_MGFO  |                  | =ARO             |       |

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1818 CHECK MD1U.U_TImH_CMP_MIA =USR
1819 CHECK MD1U.U_TImH_CMP_MSA =USR
1820 CHECK MD2U.U_WdtEna =ENA
1821 CHECK MD2U.U_HkMode =CHK
1822 CHECK MD2U.U_Hk_RepInt_DPU2 4 4
1823 CHECK MD2U.U_Hk_RepInt_EWO 4 4
1824 CHECK MD2U.U_Hk_RepInt_MEF 4 4
1825 CHECK MD2U.U_Hk_RepInt_MGFI 4 4
1826 CHECK MD2U.U_Hk_RepInt_MSAS 4 4
1827 CHECK MD2U.U_Hk_RepInt_MWE 4 4
1828 CHECK MD2U.U_TImML_CMP_DPU2 =CMP
1829 CHECK MD2U.U_TImML_CMP_AM2P =ARO
1830 CHECK MD2U.U_TImML_CMP_EFD =ARO
1831 CHECK MD2U.U_TImML_CMP_EWOB =ARO
1832 CHECK MD2U.U_TImML_CMP_EWOE =ARO
1833 CHECK MD2U.U_TImML_CMP_MDM =ARO
1834 CHECK MD2U.U_TImML_CMP_MGFI =ARO
1835 CHECK MD2U.U_TImML_CMP_MSAS =ARO
1836 CHECK MD2U.U_TImML_CMP_SOR =ARO
1837 CHECK MD2U.U_TImH_CMP_DPU2 =CMP
1838 CHECK MD2U.U_TImH_CMP_EFD =ARO
1839 CHECK MD2U.U_TImH_CMP_EWOB =USR
1840 CHECK MD2U.U_TImH_CMP_EWOE =USR
1841 CHECK MD2U.U_TImH_CMP_MGFI =ARO
1842 #
1843 # QL Check-Item Spec Check
1844 # -----+-----+-----+-----
1845 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1846 #
1847 #
1848 # *****
1849 # 3. MEA1 初期高圧 CO
1850 # *****
1851 # =====
1852 # 3.1. MEA1 LV check
1853 # =====
1854 # -----
1855 # 3.1.1. MEA1 SWITCH ON
1856 # <<M15-5 Power ON Duration:5 min Day:69>>
1857 # -----
1858 #
1859 # # MEA1 の電源を ON する。
1860 # Power on MEA1 by switching on.
1861 #
1862 . PCD.ON_OFF_ENA
1863 WAIT_SEC 40
1864 #
1865 # < CHECK >
1866 CHECK PCD.ON_OFF_ENA_DIS =ENA
1867 #
1868 . PCD.PSU_MEA1_ON
1869 WAIT_SEC 40

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```

1870 #
1871 #
1872 # < CHECK >
1873 CHECK PCD.PSU_MEA1_ON_OFF =ON
1874 #
1875 . PCD.ON_OFF_DIS
1876 WAIT_SEC 40
1877 #
1878 # < CHECK >
1879 CHECK PCD.ON_OFF_ENA_DIS =DIS
1880 CHECK MD1F.MS1_LINK_STAT =ON
1881 #
1882 # # SW Error clear
1883 . MD1F.DPU_SW_ERR_CLR
1884 WAIT_SEC 40
1885 #
1886 # < CHECK >
1887 CHECK MD1F.MS1_LINK_ERR =na
1888 CHECK MD1F.SLOT_ERR =na
1889 #
1890 #
1891 #
1892 WAIT_SEC 300
1893 #
1894 #
1895 # # MEA1 テレメトリ表示の確認(LINK 確立から 8 分以上経過後に実施)
1896 # Confirmation of the MEA1 telemetry indication
1897 # (More than 8 minutes after from LINK establishment)
1898 #
1899 # < CHECK >
1900 CHECK MEA1.HK_I_P5V <110 mA
1901 CHECK MEA1.HK_I_P12V <17 # mA
1902 CHECK MEA1.HK_MCP 0 # V
1903 CHECK MEA1.I_MCP 0 # uA
1904 CHECK MEA1.HK_TMP_FPG <30 # deg. (depend on the environment,
maximum 85° C)
1905 CHECK MEA1.HK_TMP_MCP <30 # deg. (depend on the environment,
maximum 80° C)
1906 CHECK MEA1.Status_reg 0x80 # (initial value)
1907 CHECK MEA1.I1_cmd_exe =0xFFFF
1908 CHECK MEA1.I2_cmd_exe =0xFFFF
1909 #
1910 # QL Check-Item Spec Check
1911 # -----+-----+-----+-----
1912 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1913 #
1914 # -----
1915 . # 3.1.2. MEM_DUMP
1916 # <<M15-6 Cal Check Duration:15 min Day:69>>
1917 #
1918 # CHECK ALL HV MEMORY TABLE in EEPROM to prevent errors, "corruption, delete...
1919 #

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1920 # -----
1921 #
1922 # # HV Table の確認    HV Table Verification
1923 # # MDP send a CMD MEA DUMP memory 0x2F 0x04 : HV Table 3eV -> 3KeV
1924 #   (transfert between EEPROM HV Table (n∞4) to SRAM Spacewire before
1925 #     the MDP RMAP dump packet)
1926 #
1927 . MEA1.Scan_OF
1928 WAIT_SEC 40
1929 #
1930 . MD1F.MEA1_CLCT_STOP
1931 WAIT_SEC 40
1932 #
1933 # < CHECK >
1934 CHECK MD1F.MS1_DAT_CLCT                =OFF
1935 #
1936 #
1937 #
1938 #
1939 # DUMP 1st HV table : Table 3eV-300eV
1940 #   1-Send Command to read EEPROM and transfer HV table in RAM1008
1941 #
1942 . MEA1.Dump_EEP          0x00
1943 WAIT_SEC 40
1944 #
1945 . DH.OPE_MODE 0x0C 0x3E 0x2C 0x04 0x0000000000000000000000000000
1946 DH.DMP_PERIOD 0x02
1947 WAIT_SEC 40
1948 CHECK DH.DMP_PERIOD                =SEC_4
1949 CHECK DH.CATEGORY_TBL_NO          0x2C
1950 CHECK DH.DWNLNK_RATE              =BPS16384
1951 #
1952 . #
1953 . DH.TLMFMT1553B_CHG 0x0A
1954 WAIT_SEC 40
1955 #
1956 # < CHECK >
1957 CHECK MEA1.l1_cmd_exe              =0x2F00
1958 #
1959 . DH.TLMFMT1553B_CHG 0x06
1960 WAIT_SEC 40
1961 #
1962 # < CHECK >
1963 CHECK DH.TLM1553BFMTNO            0x06
1964 #
1965 # # MDP Read DUMP memory 1st packet 512Bytes
1966 #   MDP から MEA1 ROM を 512byte DUMP
1967 #
1968 #   2-Dump RAM to MDP (Adresse : 0x600 and size=512B
1969 #
1970 . MD1F.MEM_DUMP 0x01 0x05 0x00000600 0x000200
1971 WAIT_SEC 300

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1972  #
1973  . # # MEA 側にてダンプ結果を確認
1974  #
1975  #   3-Check the 1st HV Table
1976  #
1977  #
1978  #
1979  #
1980  # DUMP 2nd HV table : Table 3eV-25KeV
1981  #   1-Send Commant to read EEPROM and transfert HV table in RAM
1982  #
1983  . MEA1.Dump_EEP           0x01
1984  WAIT_SEC 40
1985  #
1986  . DH.TLMFMT1553B_CHG 0x0A
1987  WAIT_SEC 40
1988  #
1989  # < CHECK >
1990  CHECK DH.TLM1553BFMTNO           0x0A
1991  CHECK MEA1.l1_cmd_exe           =0x2F01
1992  CHECK MEA1.l2_cmd_exe           =0x2F00
1993  #
1994  . DH.TLMFMT1553B_CHG 0x06
1995  WAIT_SEC 40
1996  #
1997  # < CHECK >
1998  CHECK DH.TLM1553BFMTNO           0x06
1999  #
2000  # # MDP Read DUMP memory 1st packet 512Bytes
2001  #   MDP から MEA1 ROM を 512byte DUMP
2002  #
2003  #   2-Dump RAM to MDP (Adresse : 0x600 and size=512B
2004  # => need 2 times (1st time is not a good packet, 2nd time is OK)
2005  #
2006  . MD1F.MEM_DUMP 0x01 0x05 0x00000600 0x000200
2007  . MD1F.MEM_DUMP 0x01 0x05 0x00000600 0x000200
2008  WAIT_SEC 300
2009  #
2010  . # # MEA 側にてダンプ結果を確認
2011  #
2012  #   3-Check the 2nd HV Table
2013  #
2014  #
2015  #
2016  #
2017  # DUMP 3rd HV table : Table 3eV-3KeV
2018  #   1-Send Commant to read EEPROM and transfert HV table in RAM
2019  #
2020  . MEA1.Dump_EEP           0x02
2021  WAIT_SEC 40
2022  #
2023  . DH.TLMFMT1553B_CHG 0x0A

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2024 WAIT_SEC 40
2025 #
2026 # < CHECK >
2027 CHECK DH. TLM1553BFMTNO 0x0A
2028 CHECK MEA1. I1_cmd_exe =0x2F02
2029 CHECK MEA1. I2_cmd_exe =0x2F01
2030 #
2031 . DH. TLMFMT1553B_CHG 0x06
2032 WAIT_SEC 40
2033 #
2034 # < CHECK >
2035 CHECK DH. TLM1553BFMTNO 0x06
2036 #
2037 # # MDP Read DUMP memory 1st packet 512Bytes
2038 # MDP から MEA1 ROM を 512byte DUMP
2039 #
2040 # 2-Dump RAM to MDP (Adresse : 0x600 and size=512B
2041 # => need 2 times (1st time is not a good packet, 2nd time is OK)
2042 #
2043 . MD1F.MEM_DUMP 0x01 0x05 0x00000600 0x000200
2044 . MD1F.MEM_DUMP 0x01 0x05 0x00000600 0x000200
2045 WAIT_SEC 300
2046 #
2047 . # # MEA 側にてダンプ結果を確認
2048 #
2049 # 3-Check the 3rd HV Table
2050 #
2051 #
2052 #
2053 #
2054 # DUMP 4th HV table : Table 3KeV-25KeV
2055 # 1-Send Commant to read EEPROM and transfert HV table in RAM
2056 #
2057 . MEA1.Dump_EEP 0x03
2058 WAIT_SEC 40
2059 #
2060 . DH. TLMFMT1553B_CHG 0x0A
2061 WAIT_SEC 40
2062 #
2063 # < CHECK >
2064 CHECK DH. TLM1553BFMTNO 0x0A
2065 CHECK MEA1. I1_cmd_exe =0x2F03
2066 CHECK MEA1. I2_cmd_exe =0x2F02
2067 #
2068 . DH. TLMFMT1553B_CHG 0x06
2069 WAIT_SEC 40
2070 #
2071 # < CHECK >
2072 CHECK DH. TLM1553BFMTNO 0x06
2073 #
2074 # # MDP Read DUMP memory 1st packet 512Bytes
2075 # MDP から MEA1 ROM を 512byte DUMP

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2076 #
2077 # 2-Dump RAM to MDP (Adresse : 0x600 and size=512B
2078 # => need 2 times (1st time is not a good packet, 2nd time is OK)
2079 #
2080 . MD1F.MEM_DUMP 0x01 0x05 0x00000600 0x000200
2081 . MD1F.MEM_DUMP 0x01 0x05 0x00000600 0x000200
2082 WAIT_SEC 300
2083 #
2084 . # # MEA 側にてダンプ結果を確認
2085 #
2086 # 3-Check the 4th HV Table
2087 #
2088 #
2089 #
2090 #
2091 . MD1F.MEA1_CLCT_START
2092 #
2093 WAIT_SEC 40
2094 #
2095 # < CHECK >
2096 CHECK MD1F.MS1_DAT_CLCT =ON
2097 #
2098 . # # Mission Data を 1553B 通信の半分の帯域 (約 4kbps) で出力するモードとする
2099 . DH.MC_EACH_EXE 0x0036 # 1553B TLM mode 10 (SYS HK 1/16, BUS User HK 1/8, DUMP 1/16,
2100 # # MDP User HK 1/8, Mission User HK 1/8, Mission Data 1/2)
2101 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
2102 #: 0x00 DH.DMP_PERIOD 0x04
2103 #: 0x01 DH.DR_PTLOGCYC_SET 0x00
2104 #: 0x02 ACS.ACS_PKT_FMT_1
2105 #: 0x05 DH.OPE_MODE 0x0C 0x3E 0x2C 0x04 0x00000000000000000000000000000000
2106 #: 0x06 DH.TLMFMT1553B_CHG 0x0A
2107 #
2108 WAIT_SEC 40
2109 #
2110 # < CHECK >
2111 CHECK DH.DMP_PERIOD =SEC_16
2112 CHECK DH.CATEGORY_TBL_NO 0x2C
2113 CHECK DH.DWNLNK_RATE =BPS16384
2114 CHECK DH.TLM1553BFMTNO 0x0A
2115 CHECK ACS.F_ACS_PKT_FMT =ACSHK1
2116 #
2117 #
2118 # QL Check-Item Spec Check
2119 # -----+-----+-----+-----+
2120 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2121 #
2122 # =====
2123 . # 3.2. SECURITY OFF
2124 # <<M15-7 DISABLE HV SECURITY Duration:5 min Day:69>>
2125 # =====
2126 # -----
2127 . # 3.2.1. DISABLE HV SECURITY (MCP + SPHERES), 10 min"

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2128 # -----
2129 #
2130 # Enable HV ALL (MCP + SPHERES)
2131 . MEA1.HV_EN
2132 WAIT_SEC 300
2133 #
2134 . # <CHECK> the HK
2135 CHECK MEA1.l1_cmd_exe = 0x1D00
2136 #
2137 # QL          Check-Item          Spec          Check
2138 # -----+-----+-----+-----
2139 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G N
2140 #
2141 # -----
2142 . # 3.2.2. DISABLE MCP SECURITY
2143 # -----
2144 #
2145 # Before Disable MCP security, send 0V to MCP (prevent the big step
2146 # after security OFF
2147 . MEA1.MCP_ON 0x00
2148 WAIT_SEC 300
2149 #
2150 . # <CHECK>
2151 #
2152 # This command start the internal no regulated MCP HV at 3500V and its
2153 # not possible to decrease this value (not remoteled) and the parameter
2154 # of this command fixe the MCP HV outut voltage (at 0V for this parameter),
2155 # => So MCP is secure because MPC HV output is at 0V although the internal HV is at 3500V
2156 #
2157 CHECK MEA1.l1_cmd_exe          =0x1C00
2158 CHECK MEA1.Status_reg          =0xC0 # (bit n° 7 = 1 and Bit n° 6 = 1)
2159 # CHECK MEA1.HK_MCP : ~0 after 1 or 2 min
2160 #
2161 #
2162 # QL          Check-Item          Spec          Check
2163 # -----+-----+-----+-----
2164 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G N
2165 #
2166 #
2167 # -----
2168 . # 3.2.3. Disable the MCP bit security (This function is checked after)
2169 # -----
2170 #
2171 . MEA1.SECRTY_OFF
2172 WAIT_SEC 300
2173 #
2174 . # <CHECK> the HK
2175 CHECK MEA1.l1_cmd_exe          =0x1F00
2176 CHECK MEA1.Status_reg          0x40 # (bit n° 7 = 0)
2177 #
2178 #
2179 # QL          Check-Item          Spec          Check

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2180 # -----+-----+-----+-----
2181 # CMD 出力装置      Message          STATUS CHECK 結果 OK      G  N
2182 #
2183 # =====
2184 . # 3.3. SWITCH ON MCP (2750V in vacuum)
2185 #   <<M15-8 SWITCH ON MCP   Duration:140 min Day:69>>
2186 # =====
2187 # -----
2188 . # 3.3.1. SWITCH ON MCP, 25 min to verify the MCP HV
2189 # -----
2190 #
2191 # Switch ON the MCP, depend on the environment :
2192 #
2193 # The step to change the value must be to 100V during 5 min so :
2194 #   - for 2750 V, number of steps is 2750/100 + 1 => 29 Steps
2195 # OR
2196 #   - for 400 V, number of steps is 400/100 + 1 => 5 Steps
2197 #
2198 # Notes : Maximum Value depend on the environnement
2199 # -> Vaccum Chamber or in Space : MCP Value is 2750 V (parameter 0c8)
2200 # -> ON air, MCP Value is 400 V (parameter 0x1d)
2201 # Notes : Duration, depend on the maximum value and environnement
2202 # -> 29 x 5 min = 145 min
2203 # -> 5 x 5 min = 25 min
2204 # Notes : Step size
2205 # -> Size of 100 V Step = 100 V - 0x07
2206 # -> Value 400 V - 0x1d
2207 # -> Value 2750 V - 0xc8
2208 # -> Minimum step 26 V - 0x01 and Internal step
2209 # -> Command_Value = (Value_MCP / 0.858) / 16
2210 # -> example : MCP=400V => 29 => 0x1d
2211 # Send Value to MCP and MCP ON
2212 #
2213 # WAIT 5 MINUTE BETWEEN EACH STEPS
2214 #
2215 . MEA1.MCP_ON 0x00
2216 WAIT_SEC 300
2217 #
2218 . # <CHECK> the HK
2219 CHECK MEA1.l1_cmd_exe          =0x1c00
2220 # CHECK MEA1.HK_MCP : < 0V
2221 CHECK MEA1.Status_reg          0x40 # (bit n° 6 = 1)
2222 #
2223 #
2224 . MEA1.MCP_ON 0x07
2225 WAIT_SEC 40
2226 #
2227 . # <CHECK> the HK
2228 CHECK MEA1.l1_cmd_exe          =0x1c07
2229 # CHECK MEA1.HK_MCP : ~ 100V
2230 #
2231 #

```

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2232 . MEA1.MCP_ON 0x0E
2233 WAIT_SEC 300
2234 #
2235 . # <CHECK> the HK
2236 CHECK MEA1.l1_cmd_exe =0x1c0E
2237 # CHECK MEA1.HK_MCP : ~ 200V
2238 #
2239 #
2240 . MEA1.MCP_ON 0x015
2241 WAIT_SEC 300
2242 #
2243 . # <CHECK> the HK
2244 CHECK MEA1.l1_cmd_exe =0x1c15
2245 # CHECK MEA1.HK_MCP : ~ 300V
2246 #
2247 #
2248 . MEA1.MCP_ON 0x01D
2249 WAIT_SEC 300
2250 #
2251 . # <CHECK> the HK
2252 CHECK MEA1.l1_cmd_exe =0x1c1d
2253 # CHECK MEA1.HK_MCP : ~ 400V
2254 #
2255 # QL          Check-Item          Spec          Check
2256 # -----+-----+-----+-----+
2257 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2258 #
2259 #
2260 # -----+-----+-----+-----+
2261 . # 3.3.2. ENABLE MCP SECURITY, 5 min
2262 # -----+-----+-----+-----+
2263 #
2264 # Enable the MCP bit security, Value_MCP divide by 100, to verify the security MCP
2265 . MEA1.SECRTY_ON
2266 WAIT_SEC 300
2267 #
2268 . # <CHECK> the HK
2269 # CHECK MEA1.HK_MCP : ~ 0V after some minutes, 400V/100
2270 CHECK MEA1.l1_cmd_exe =0x1800
2271 CHECK MEA1.Status_reg 0xC0 # (bit n° 7 = 1 and 6 = 1)
2272 #
2273 # QL          Check-Item          Spec          Check
2274 # -----+-----+-----+-----+
2275 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2276 #
2277 #
2278 # -----+-----+-----+-----+
2279 . # 3.3.3. DISABLE MCP SECURITY, 5 min
2280 # -----+-----+-----+-----+
2281 #
2282 # Disbale the MCP bit security, MCP retrieve the previous value at 400V
2283 . MEA1.SECRTY_OFF

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2284 WAIT_SEC 300
2285 #
2286 . # <CHECK> the HK
2287 CHECK MEA1.l1_cmd_exe =0x1F00
2288 CHECK MEA1.Status_reg 0x40 # (bit n° 7 = 0)
2289 # CHECK MEA1.HK_MCP : ~ 400V
2290 #
2291 # QL Check-Item Spec Check
2292 # -----+-----+-----+-----
2293 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2294 #
2295 # -----
2296 . # 3.3.4. SWITCH OFF MCP, 25 min
2297 # -----
2298 #
2299 # Send Value to MCP and MCP ON
2300 #
2301 . # WAIT 5 MINUTE BETWEEN EACH STEPS
2302 . MEA1.MCP_ON 0x015
2303 WAIT_SEC 300
2304 #
2305 . # <CHECK> the HK
2306 CHECK MEA1.l1_cmd_exe =0x1c15
2307 # CHECK MEA1.HK_MCP : ~ 300V
2308 #
2309 #
2310 . MEA1.MCP_ON 0x00E
2311 WAIT_SEC 300
2312 #
2313 . # <CHECK> the HK
2314 CHECK MEA1.l1_cmd_exe =0x1c0E
2315 # CHECK MEA1.HK_MCP : ~ 200V
2316 #
2317 #
2318 . MEA1.MCP_ON 0x007
2319 WAIT_SEC 300
2320 #
2321 . # <CHECK> the HK
2322 CHECK MEA1.l1_cmd_exe =0x1c07
2323 # CHECK MEA1.HK_MCP : ~ 100V
2324 #
2325 #
2326 . MEA1.MCP_ON 0x000
2327 WAIT_SEC 300
2328 #
2329 . # <CHECK> the HK
2330 CHECK MEA1.l1_cmd_exe =0x1c00
2331 # CHECK MEA1.HK_MCP : < 0V after some minutes
2332 #
2333 #
2334 # Stop the MCP HV
2335 . MEA1.MCP_OFF

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2336 WAIT_SEC 300
2337 #
2338 # <CHECK> the HK
2339 CHECK MEA1.l1_cmd_exe =0x1900
2340 CHECK MEA1.Status_reg 0x00 # (bit n° 6 = 0)
2341 #
2342 # QL          Check-Item          Spec          Check
2343 # -----+-----+-----+-----+
2344 # CMD 出力装置  Message          STATUS CHECK 結果 OK      G  N
2345 #
2346 #
2347 # =====
2348 # 3.4. SWITCH ON SCAN, 15 min
2349 # <<M15-9 SWITCH ON SCAN  Duration:120 min Day:69>>
2350 # =====
2351 #
2352 # Start the scan of the sphere and Analyzer HV to verify the noise on A111 and MCP
2353 # Start the scan and verify if the instrument is synchronized or not
2354 #
2355 #
2356 MEA1.Scan_ON
2357 WAIT_SEC 300
2358 #
2359 # <CHECK> the HK
2360 CHECK MEA1.l1_cmd_exe =0x2500
2361 #
2362 #
2363 # Select the sun pulse synchronization
2364 MEA1.Md_S_Auto
2365 WAIT_SEC 300
2366 #
2367 # <CHECK> the HK
2368 CHECK MEA1.l1_cmd_exe =0x3100
2369 #
2370 #
2371 # Select energies mode : mode 32 energies/32 sweeps
2372 # -> HV table n° 2, 3eV-3KeV (~300V maxi on the analyzer)
2373 #
2374 MEA1.mode_ROM_4 0x02
2375 WAIT_SEC 300
2376 #
2377 # <CHECK> the HK
2378 CHECK MEA1.l1_cmd_exe =0x0302
2379 # Check HKP TmSStp = HKP TmSStp_dft after some secondes
2380 #
2381 ## *****
2382 # WAIT until (5 min maximum) HKP TmSStp = TmSStp_dft to indicate the synchronization
2383 # between sun plus and MEA.
2384 # If not, do not execute the next steps!!
2385 # Probably, sun pulse signal have a problem (cf previous test at ISAS)!!
2386 # If already executed, you can pass at the next step.
2387 ## *****

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2388 #
2389 WAIT_SEC 300
2390 # Check the science Data to verify the value
2391 # - wait 10 min to verify the Science Data
2392 #
2393 # QL          Check-Item          Spec          Check
2394 # -----+-----+-----+-----
2395 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2396 #
2397 #
2398 # =====
2399 # 3.5. TEST MODE ON, 150 min
2400 # <<M15-10 TEST MODE ON  Duration:30 min Day:69>>
2401 # =====
2402 #
2403 # Swtich ON the test mode for the Amplifier A111
2404 # to verify the good working of this
2405 # MEA1.Md_Tst_ON
2406 WAIT_SEC 900
2407 #
2408 # <CHECK> the HK
2409 CHECK MEA1.l1_cmd_exe          =0x3900
2410 CHECK MEA1.Status_reg          0x01 # (bit n° 0 = 1)
2411 #
2412 # - wait 15 min to verify the Science Data
2413 # Check the science Data and compare with the previous value
2414 #
2415 # QL          Check-Item          Spec          Check
2416 # -----+-----+-----+-----
2417 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2418 #
2419 #
2420 # =====
2421 # 3.6. TEST MODE OFF, 10 min
2422 # <<M15-12 TEST MODE OFF  Duration:5 min Day:69>>
2423 # =====
2424 #
2425 # Stop the A111 test Mode
2426 # MEA1.Md_Tst_OF
2427 WAIT_SEC 600
2428 #
2429 # <CHECK> the HK
2430 CHECK MEA1.l1_cmd_exe          =0x3800
2431 CHECK MEA1.Status_reg          0x00 # (bit n° 0 = 0)
2432 #
2433 # - wait 10 min to verify the Science Data are return to zero
2434 # Check the science Data and compare with the previous value
2435 #
2436 #
2437 # QL          Check-Item          Spec          Check
2438 # -----+-----+-----+-----
2439 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N

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2440 #
2441 #
2442 # =====
2443 . # 3.7. ENABLE HV of ANALYZER (25 minutes)
2444 # <<M15-** Duration:** min Day:69>>
2445 # =====
2446 #
2447 # -> DO NOT EXECUTE THIS PART IF HV TABLE IS NOT n° 2
2448 # -> HV table n° 2 are selected, 3eV-3KeV (~300V maxi on the analyzer), verify this value
on HKP
2449 # -> Same thing than the MCP, When the analyzer is ON, a 2nd internal HV and no regulated
is ON
2450 # and her value is 3000V even if the output analyzer is maximum at 300V (depend on the
HV table)
2451 ## *****
2452 #
2453 # Deactive the current limiter switch OFF (just for switch ON the HV)
2454 . MEA1.ON_F12V
2455 WAIT_SEC 300
2456 #
2457 . # <CHECK> the HK
2458 CHECK MEA1.l1_cmd_exe =0x3F00
2459 #
2460 #
2461 #Switch ON the HV Spheres of analyzer
2462 . MEA1.Anlyz_ON
2463 WAIT_SEC 300
2464 #
2465 . # <CHECK> the HK
2466 CHECK MEA1.l1_cmd_exe =0x3F00
2467 CHECK MEA1.Status_reg 0x20 # (bit n° 5 = 1)
2468 #
2469 #
2470 # Re-active the current limiter switch OFF
2471 . MEA1.AUTO12V
2472 WAIT_SEC 300
2473 #
2474 . # <CHECK> the HK
2475 CHECK MEA1.l1_cmd_exe =0x3E00
2476 #
2477 #
2478 #
2479 WAIT_SEC 560
2480 ## *****
2481 . # Check the science Data to verify the value
2482 #
2483 # - wait 10 min to verify the Science Data
2484 #
2485 #
2486 ## *****
2487 #
2488 #

```

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|      | QL | Check-Item                                                          | Spec                                | Check |
|------|----|---------------------------------------------------------------------|-------------------------------------|-------|
| 2489 | #  |                                                                     |                                     |       |
| 2490 | #  |                                                                     |                                     |       |
| 2491 | #  | CMD 出力装置 Message                                                    | STATUS CHECK 結果 OK                  | G N   |
| 2492 | #  |                                                                     |                                     |       |
| 2493 | #  |                                                                     |                                     |       |
| 2494 | #  | =====                                                               |                                     |       |
| 2495 | #  | 3.8. SWITCH ON MCP to verify the MCP noise (25 minutes)             |                                     |       |
| 2496 | #  | <<M15-** Duration:** min Day:69>>                                   |                                     |       |
| 2497 | #  | =====                                                               |                                     |       |
| 2498 | #  |                                                                     |                                     |       |
| 2499 | #  | Switch ON the MCP, depend on the environment                        |                                     |       |
| 2500 | #  |                                                                     |                                     |       |
| 2501 | #  | The step to change the value must be to 100V so :                   |                                     |       |
| 2502 | #  | - for 2750 V, number of steps is 2750/100 + 1 => 29 Steps           |                                     |       |
| 2503 | #  | OR                                                                  |                                     |       |
| 2504 | #  | - for 400 V, number of steps is 400/100 + 1 => 5 Steps"             |                                     |       |
| 2505 | #  | 1010 10 a                                                           |                                     |       |
| 2506 | #  | Notes : Maximum Value depend on the environnement                   |                                     |       |
| 2507 | #  | -> Vaccum Chamber or in Space : MCP Value is 2750 V (parameter 0c8) |                                     |       |
| 2508 | #  | -> ON air, MCP Value is 400 V (parameter 0x1d)"                     |                                     |       |
| 2509 | #  | Notes : Duration, depend on the maximum value and environnement     |                                     |       |
| 2510 | #  | -> 29 x 5 min = 145 min                                             |                                     |       |
| 2511 | #  | -> 5 x 5 min = 25 min                                               |                                     |       |
| 2512 | #  | Notes : Step size                                                   |                                     |       |
| 2513 | #  | -> Size of 100 V Step = 100 V - 0x07                                |                                     |       |
| 2514 | #  | -> Value 400 V - 0x1d                                               |                                     |       |
| 2515 | #  | -> Value 2750 V - 0xc8                                              |                                     |       |
| 2516 | #  | -> Minimum step 26 V - 0x01 and Internal step                       |                                     |       |
| 2517 | #  | -> Command_Value = (Value_MCP / 0.858) / 16                         |                                     |       |
| 2518 | #  | -> example : MCP=400V => 29 => 0x1d                                 |                                     |       |
| 2519 | ## | *****                                                               |                                     |       |
| 2520 | #  | Send Value to MCP and MCP ON                                        |                                     |       |
| 2521 | #  |                                                                     |                                     |       |
| 2522 | #  | MEA1.MCP_ON 0x00                                                    |                                     |       |
| 2523 | #  | WAIT_SEC 300                                                        |                                     |       |
| 2524 | #  |                                                                     |                                     |       |
| 2525 | #  | <CHECK> the HK                                                      |                                     |       |
| 2526 | #  | CHECK MEA1.l1_cmd_exe                                               | =0x1c00                             |       |
| 2527 | #  | CHECK MEA1.HK_MCP : < 0V                                            |                                     |       |
| 2528 | #  | CHECK MEA1.Status_reg                                               | 0x60 # (bit n° 6 = 1 and bit 5 = 1) |       |
| 2529 | #  |                                                                     |                                     |       |
| 2530 | #  | Check the science Data to verify the noise                          |                                     |       |
| 2531 | #  |                                                                     |                                     |       |
| 2532 | #  | - wait 5 min to verify the Science Data                             |                                     |       |
| 2533 | #  |                                                                     |                                     |       |
| 2534 | #  |                                                                     |                                     |       |
| 2535 | #  | MEA1.MCP_ON 0x07                                                    |                                     |       |
| 2536 | #  | WAIT_SEC 300                                                        |                                     |       |
| 2537 | #  |                                                                     |                                     |       |
| 2538 | #  | <CHECK> the HK                                                      |                                     |       |
| 2539 | #  | CHECK MEA1.l1_cmd_exe                                               | =0x1c07                             |       |
| 2540 | #  | CHECK MEA1.HK_MCP : ~ 100V                                          |                                     |       |

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2541 #
2542 #   Check the science Data to verify the noise
2543 #
2544 #   - wait 5 min to verify the Science Data
2545 #
2546 #
2547 . MEA1.MCP_ON 0x0E
2548 WAIT_SEC 300
2549 #
2550 . # <CHECK> the HK
2551 CHECK MEA1.l1_cmd_exe                      =0x1c0E
2552 # CHECK MEA1.HK_MCP : ~ 200V
2553 #
2554 #   Check the science Data to verify the noise
2555 #
2556 #   - wait 5 min to verify the Science Data
2557 #
2558 #
2559 . MEA1.MCP_ON 0x015
2560 WAIT_SEC 300
2561 #
2562 . # <CHECK> the HK
2563 CHECK MEA1.l1_cmd_exe                      =0x1c15
2564 # CHECK MEA1.HK_MCP : ~ 300V
2565 #
2566 #   Check the science Data to verify the noise
2567 #
2568 #   - wait 5 min to verify the Science Data
2569 #
2570 #
2571 . MEA1.MCP_ON 0x01D
2572 WAIT_SEC 300
2573 #
2574 . # <CHECK> the HK
2575 CHECK MEA1.l1_cmd_exe                      =0x1c1d
2576 # CHECK MEA1.HK_MCP : ~ 400V
2577 #
2578 #   Check the science Data to verify the noise
2579 #
2580 #   - wait 5 min to verify the Science Data
2581 #
2582 #
2583 ## *****
2584 ## *****
2585 ## TEST IS IN THE AIR ? -> IN THE AIR, STEP AT THE GF PART"
2586 ## *****
2587 ## *****
2588 #
2589 # QL          Check-Item          Spec          Check
2590 # -----+-----+-----+-----+
2591 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2592 #

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2593  #
2594  # =====
2595  # 3.9. GF (20 minutes duration, 5 minutes per geometric factor)
2596  # <<M15-** Duration:** min Day:69>>
2597  # CHECK both HK and Science Data for each geometric factor
2598  # =====
2599  #
2600  # Change the geometrical factor to 0.8
2601  . MEA1.G_Factor 0x01
2602  WAIT_SEC 300
2603  #
2604  # <CHECK> the HK
2605  CHECK MEA1.l1_cmd_exe =0x0801
2606  #
2607  # Check the science Data to verify the noise
2608  #
2609  # - wait 5 min to verify the Science Data
2610  #
2611  #
2612  # Change the geometrical factor to 0.53
2613  . MEA1.G_Factor 0x02
2614  WAIT_SEC 300
2615  #
2616  # <CHECK> the HK
2617  CHECK MEA1.l1_cmd_exe =0x0802
2618  #
2619  # Check the science Data to verify the noise
2620  #
2621  # - wait 5 min to verify the Science Data
2622  #
2623  #
2624  # Change the geometrical factor to 0.35
2625  . MEA1.G_Factor 0x03
2626  WAIT_SEC 300
2627  #
2628  # <CHECK> the HK
2629  CHECK MEA1.l1_cmd_exe =0x0803
2630  #
2631  # Check the science Data to verify the noise
2632  #
2633  # - wait 5 min to verify the Science Data
2634  #
2635  #
2636  # Change the geometrical factor to 0.27
2637  . MEA1.G_Factor 0x04
2638  WAIT_SEC 300
2639  #
2640  # <CHECK> the HK
2641  CHECK MEA1.l1_cmd_exe =0x0804
2642  #
2643  # Check the science Data to verify the noise
2644  #

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2645 # - wait 5 min to verify the Science Data
2646 #
2647 #
2648 # QL          Check-Item          Spec          Check
2649 # -----+-----+-----+-----
2650 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2651 #
2652 #
2653 # =====
2654 # 3.10. SWITCH OFF MCP in VACUUM (10 MINUTES)
2655 #   <<M15-**   Duration:** min Day:69>>
2656 #           CHECK both HK and Science Data for each geometric factor
2657 # =====
2658 #
2659 ## *****
2660 # Send Value to MCP and MCP ON
2661 # - wait 5 min between each step (2x5 minutes = 10 minutes)
2662 ## *****
2663 # START HERE IN THE AIR
2664 # MEA1.MCP_ON 0x0E
2665 # WAIT_SEC 300
2666 #
2667 # <CHECK> the HK
2668 # CHECK MEA1.l1_cmd_exe          =0x1c0E
2669 # CHECK MEA1.HK_MCP : ~ 200V
2670 #
2671 #
2672 # MEA1.MCP_ON 0x00
2673 # WAIT_SEC 300
2674 #
2675 # <CHECK> the HK
2676 # CHECK MEA1.l1_cmd_exe          =0x1c00
2677 # CHECK MEA1.HK_MCP : < 0V
2678 #
2679 #
2680 # MEA1.MCP_OFF
2681 # WAIT_SEC 300
2682 #
2683 # <CHECK> the HK
2684 # CHECK MEA1.l1_cmd_exe          =0x1900
2685 # CHECK MEA1.Status_reg          0x22 # (bit n° 6 = 0 and bit 5 = 1)
2686 #
2687 # QL          Check-Item          Spec          Check
2688 # -----+-----+-----+-----
2689 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2690 #
2691 #
2692 # =====
2693 # 3.11. SWITCH OFF the SCAN (15 minutes)
2694 #   <<M15-**   Duration:** min Day:69>>
2695 #           CHECK both HK and Science Data for each geometric factor
2696 # =====

```

```

2697 #
2698 # SWITCH OFF THE ANALYZER HV, after this NO HV inside MEA"
2699 #
2700 #
2701 . MEA1.Anlyz_OF
2702 WAIT_SEC 300
2703 #
2704 . # <CHECK> the HK
2705 CHECK MEA1.l1_cmd_exe =0x2000
2706 CHECK MEA1.Status_reg 0x02 # (bit n° 6 = 0 and bit 5 = 0)
2707 #
2708 #
2709 # SWITCH OFF THE SCAN
2710 . MEA1.Scan_OF
2711 WAIT_SEC 300
2712 #
2713 . # <CHECK> the HK
2714 CHECK MEA1.l1_cmd_exe 0x2400
2715 #
2716 #
2717 # SWITCH ON THE MCP SECURITY
2718 . MEA1.SECRTY_ON
2719 WAIT_SEC 300
2720 #
2721 . # <CHECK> the HK
2722 CHECK MEA1.l1_cmd_exe =0x1800
2723 CHECK MEA1.Status_reg 0x82 # (bit n° 8 = 1)
2724 #
2725 # QL Check-Item Spec Check
2726 # -----+-----+-----+-----
2727 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2728 #
2729 # *****
2730 . # 4. MEA1 SHUTDOWN
2731 # *****
2732 #
2733 . PCD.ON_OFF_ENA
2734 WAIT_SEC 40
2735 #
2736 # < CHECK >
2737 CHECK PCD.ON_OFF_ENA_DIS =ENA
2738 #
2739 . MD1F.MEA1_CLCT_STOP
2740 WAIT_SEC 40
2741 #
2742 # < CHECK >
2743 CHECK MD1F.MS1_DAT_CLCT =OFF
2744 #
2745 . PCD.PSU_MEA1_OFF
2746 WAIT_SEC 40
2747 #
2748 . # < CHECK >

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2749 CHECK PCD.PSU_MEA1_ON_OFF =OFF
2750 CHECK MD1F.MS1_LINK_STAT =OFF
2751 #
2752 . PCD.ON_OFF_DIS
2753 WAIT_SEC 40
2754 . #
2755 . # < CHECK >
2756 CHECK PCD.ON_OFF_ENA_DIS =DIS
2757 #
2758 #
2759 # QL Check-Item Spec Check
2760 # -----+-----+-----+-----
2761 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2762 #
2763 #
2764 # *****
2765 . # 5. 立ち下げ手順
2766 # *****
2767 # =====
2768 . # 5.1. MDP 立ち下げ
2769 # =====
2770 #
2771 . # <<< MDP-DPU RST/OFF >>>
2772 . DH.MDP_DPU12_CPU_OFF # DH.MC_EACH_EXE 0x00B1
2773 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
2774 #: 0x00 DH.ATMC_EACH_DIS 0x32
2775 #: 0x03 DH.ATMC_EACH_DIS 0x33
2776 #: 0x06 DH.ATMC_EACH_DIS 0x30
2777 #: 0x09 DH.ATMC_EACH_DIS 0x31
2778 #: 0x0A MD1F.CPU_RESET_ENA
2779 #: 0x0B MD1F.CPU_RESET
2780 #: 0x0C MD1F.CPU_OFF
2781 #: 0x0D MD2F.CPU_RESET_ENA
2782 #: 0x0E MD2F.CPU_RESET
2783 #: 0x0F MD2F.CPU_OFF
2784 #
2785 . # < CHECK >
2786 CHECK DH.ATMC_ENA_DIS30 =DIS
2787 CHECK DH.ATMC_ENA_DIS31 =DIS
2788 CHECK DH.ATMC_ENA_DIS32 =DIS
2789 CHECK DH.ATMC_ENA_DIS33 =DIS
2790 #
2791 . # QL Check-Item Spec Check
2792 # -----+-----+-----+-----
2793 # MD1F_OBS_HK << DPU1 H/W-HK >>
2794 # HW_STATE2 CPU_RST_ENA (short) N/A
2795 # HWCMD_CODE 0x0801 (short) N/A
2796 # HW_STATE2 STANDBY (short) N/A
2797 # HWCMD_CODE 0x0702 (short) N/A
2798 #
2799 . # < CHECK >
2800 CHECK MD1F.DPU_HW_STATE2 =CPUOFF_DPUBUS

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2801 CHECK MD1F.DPU_CMD_CODE 0x0202
2802 #
2803 # QL Check-Item Spec Check
2804 # -----+-----+-----+-----
2805 # MD2F_OBS_HK << DPU2 H/W-HK >>
2806 # HW_STATE2 CPU_RST_ENA (short) N/A
2807 # HWCMD_CODE 0x0801 (short) N/A
2808 # HW_STATE2 STANDBY (short) N/A
2809 # HWCMD_CODE 0x0702 (short) N/A
2810 #
2811 # < CHECK >
2812 CHECK MD2F.DPU_HW_STATE2 =CPUOFF_DPUBUS
2813 CHECK MD2F.DPU_CMD_CODE 0x0202
2814 #
2815 . DH.MC_EACH_EXE 0x002C # 1553B TLM mode 0 (SYS HK 1)
2816 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
2817 #: 0x00 DH.DMP_PERIOD 0x04
2818 #: 0x04 ACS.ACS_PKT_FMT_1
2819 #: 0x05 DH.OPE_MODE 0x0C 0x0A 0x00 0x00 0x00000000000000000000000000000000
2820 #: 0x06 DH.TLMFMT1553B_CHG 0x00
2821 #
2822 # <CHECK>
2823 CHECK DH.DMP_PERIOD =SEC_16
2824 CHECK DH.CATEGORY_TBL_NO 0x00
2825 CHECK DH.DWNLNK_RATE =BPS16384
2826 CHECK DH.TLM1553BFMTNO 0x00
2827 #
2828 # <<< MDP-DPU1/2 Power OFF >>>
2829 . DH.MDP_DPU12_OFF # DH.MC_EACH_EXE 0x00B0
2830 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
2831 #: 0x00 DH.ATMC_EACH_DIS 0x34
2832 #: 0x01 DH.SPW_RTEFDIR_SET 0x18
2833 #: 0x03 DH.ATMC_EACH_DIS 0x36
2834 #: 0x06 DH.ATMC_EACH_DIS 0x32
2835 #: 0x09 DH.ATMC_EACH_DIS 0x33
2836 #: 0x0A PCD.ON_OFF_ENA
2837 #: 0x0B PCD.PSU_MDP1_OFF
2838 #: 0x0C DH.ATMC_EACH_DIS 0x30
2839 #: 0x0D PCD.PSU_MDP2_OFF
2840 #: 0x0E PCD.ON_OFF_DIS
2841 #: 0x0F DH.ATMC_EACH_DIS 0x31
2842 #
2843 # < CHECK >
2844 # CHECK DH.ATMC_ENA_DIS30 =DIS
2845 # CHECK DH.ATMC_ENA_DIS31 =DIS
2846 # CHECK DH.ATMC_ENA_DIS32 =DIS
2847 # CHECK DH.ATMC_ENA_DIS33 =DIS
2848 # CHECK DH.ATMC_ENA_DIS34 =DIS
2849 # CHECK DH.ATMC_ENA_DIS36 =DIS
2850 CHECK DH.SPW_FDIR_ENA_MDP1 =DIS
2851 CHECK DH.SPW_FDIR_ENA_MDP2 =DIS
2852 CHECK DH.SPW_FDIR_ENA_PCD =ENA

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2853 CHECK DH. SPW_FDIR_ENA_RMTRTR =ENA
2854 CHECK PCD. PSU_MDP1_ON_OFF =OFF
2855 CHECK PCD. PSU_MDP2_ON_OFF =OFF
2856 CHECK PCD. ON_OFF_ENA_DIS =DIS
2857 #
2858 . # QL Check-Item Spec Check
2859 # -----+-----+-----+-----
2860 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2861 #
2862 # =====
2863 . # 5.2. TCS 立ち下げ(HTR CTRL OFF/DRU_OFF)
2864 # =====
2865 . #
2866 . DH.MC_EACH_EXE 0x0032 # 1553B TLM mode 6 (SYS HK 1/2, BUS User HK 1/4, DUMP 1/4)
2867 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
2868 #: 0x00 DH. DMP_PERIOD 0x02
2869 #: 0x02 ACS. ACS_PKT_FMT_1
2870 #: 0x05 DH. OPE_MODE 0x0C 0x26 0x28 0x03 0x00000000000000000000000000000000
2871 #: 0x06 DH. TLMFMT1553B_CHG 0x06
2872 #
2873 WAIT_SEC 40
2874 CHECK DH. DMP_PERIOD =SEC_4
2875 CHECK DH. CATEGORY_TBL_NO 0x28
2876 CHECK DH. DWNLNK_RATE =BPS16384
2877 CHECK DH. TLM1553BFMTNO 0x06
2878 CHECK ACS. F_ACS_PKT_FMT =ACSHK1
2879 #
2880 . # < CHECK >
2881 CHECK DH. ATMC_ENA_DIS30 =DIS
2882 CHECK DH. ATMC_ENA_DIS31 =DIS
2883 CHECK DH. ATMC_ENA_DIS32 =DIS
2884 CHECK DH. ATMC_ENA_DIS33 =DIS
2885 CHECK DH. ATMC_ENA_DIS34 =DIS
2886 CHECK DH. ATMC_ENA_DIS35 =DIS
2887 CHECK DH. ATMC_ENA_DIS36 =DIS
2888 CHECK DH. ATMC_ENA_DIS37 =DIS
2889 . #
2890 . DH. ATMC_EACH_DIS 0x09 # //分離前 CPU リセット時 TCS モード再度 NMNL 制御
2891 #
2892 . # < CHECK >
2893 CHECK DH. ATMC_ENA_DIS09 =DIS
2894 . #
2895 . TCS. MODE_STBY
2896 #
2897 . # < CHECK >
2898 CHECK TCS. MODE_ST =STBY
2899 . #
2900 . TCS. CTRL_HTR_P1_DIS
2901 WAIT_SEC 2
2902 TCS. CTRL_HTR_P2_DIS
2903 WAIT_SEC 2
2904 TCS. CTRL_HTR_P3_DIS

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2905 WAIT_SEC 2
2906 TCS. CTRL_HTR_P4_DIS
2907 #
2908 . # < CHECK >
2909 CHECK TCS. HTR01_P_VOL_ST =OFF
2910 CHECK TCS. HTR09_P_VOL_ST =OFF
2911 CHECK TCS. HTR17_P_VOL_ST =OFF
2912 CHECK TCS. HTR25_P_VOL_ST =OFF
2913 . #
2914 . ACS. DRU_SEL_NA
2915 #
2916 . # < CHECK >
2917 CHECK ACS. DRU_SEL_PT =NA
2918 CHECK ACS. SYS. DRU_SEL_PT =NA
2919 CHECK ACS. BIT_DRU_SEL_PT =NA
2920 . #
2921 . PCD. ON_OFF_ENA
2922 #
2923 . # < CHECK >
2924 CHECK PCD. ON_OFF_ENA_DIS =ENA
2925 . #
2926 . PCD. DRU_PSU_A_OFF
2927 #
2928 . # < CHECK >
2929 CHECK PCD. DRU_PSU_A_ON_OFF =OFF
2930 . #
2931 . PCD. ON_OFF_DIS
2932 #
2933 . # < CHECK >
2934 CHECK PCD. ON_OFF_ENA_DIS =DIS
2935 #
2936 . # QL          Check-Item          Spec          Check
2937 # -----+-----+-----+-----+
2938 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2939 #
2940 # =====
2941 . # 5.3. MC_DIS
2942 # =====
2943 . #
2944 . # << PCS 系個別 ATMC DIS >>
2945 . #
2946 . DH. ATMC_EACH_DIS 0x01 # // BAT UVC in Cruise mode
2947 . DH. ATMC_EACH_DIS 0x2B # //PCD NIC リセットモニタ
2948 #
2949 . # < CHECK >
2950 CHECK DH. ATMC_ENA_DIS01 =DIS
2951 CHECK DH. ATMC_ENA_DIS2B =DIS
2952 #
2953 . #
2954 . DH. ATMC_DIS
2955 #
2956 . # < CHECK >

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2957 CHECK DH. ATMC_ENA_DIS =DIS
2958 . #
2959 . DH. RQMC_DIS
2960 . #
2961 . # < CHECK >
2962 CHECK DH. RQMC_ENA_DIS =DIS
2963 . #
2964 . DH. SYSTM_R_DIS
2965 . #
2966 . # < CHECK >
2967 CHECK DH. SYSTM_R_ENA_DIS =DIS
2968 . #
2969 . DH. ALL_MC_STOP
2970 . #
2971 . # < CHECK >
2972 CHECK DH. MC_ENA_DIS =DIS
2973 . #
2974 . # QL Check-Item Spec Check
2975 . # -----+-----+-----+-----
2976 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2977 . #
2978 . # =====
2979 . # 5. 4. 衛星立ち下げ
2980 . # =====
2981 . #
2982 . # QL Check-Item Spec Check
2983 . # -----+-----+-----+-----
2984 . # PCS1 PCD. CHG_LR1_ON_OFF OFF G N
2985 . # PCD. CHG_LR2_ON_OFF OFF G N
2986 . # DMC-PSU-A, -B, XTRP-A, B, SV-Heater 以外が OFF であること G N
2987 . # TCIU DH. XTRP_A_TX_ON_OFF OFF G N
2988 . # DH. XTRP_B_TX_ON_OFF OFF G N
2989 . # 局管制 Uplink OFF G N
2990 . #
2991 . #
2992 . # 【確認】 Check
2993 . # BUS-V > BAT-V であることを確認する。 G N
2994 . #
2995 . PCD. INT_EXT_ENA
2996 . PCD. BAT_EXT
2997 . #
2998 . # < CHECK >
2999 CHECK PCD. INT_EXT_ENA_DIS =DIS
3000 CHECK PCD. BAT_INT_EXT =EXT
3001 . #
3002 . PCD. CHG_BYPS_DIS
3003 . #
3004 . # < CHECK >
3005 CHECK PCD. CHG_BYPS_ENA_DIS 255
3006 CHECK PCD. SA_V -
3007 CHECK PCD. SA_I -
3008 CHECK PCD. APR1_I -

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3009 CHECK PCD. APR2_I -
3010 CHECK PCD. BUS_V -
3011 CHECK PCD. LOAD_I -
3012 CHECK PCD. CHG_I -
3013 CHECK PCD. DCHG_I -
3014 CHECK PCD. BAT_V -
3015 CHECK PCD. CEL1_V -
3016 CHECK PCD. CEL2_V -
3017 CHECK PCD. CEL3_V -
3018 CHECK PCD. CEL4_V -
3019 CHECK PCD. CEL5_V -
3020 CHECK PCD. CEL6_V -
3021 CHECK PCD. CEL7_V -
3022 CHECK PCD. CEL8_V -
3023 CHECK PCD. CEL9_V -
3024 CHECK PCD. CEL10_V -
3025 CHECK PCD. CEL11_V -
3026 CHECK PCD. CEL12_V -
3027 CHECK PCD. BAT_CV -
3028 CHECK PCD. BAT_CV_ON_OFF -
3029 . #
3030 . PCD. CEL_V_MONI_OFF # 【BAT 管理手順書 (TPP-7S8238-03K) へ記録】
3031 . #
3032 . # < CHECK >
3033 CHECK PCD. CEL_V_MONI_ON_OFF =OFF
3034 CHECK PCD. BAT_CV -
3035 CHECK PCD. BAT_CV_ON_OFF -
3036 . #
3037 . # 【MIS コマンド操作】
3038 . # MIS 電源出力 A 系 OFF
3039 . # "SYSTEM Control → PFE → Modules[2] → PRI → ON → YES"
3040 . #
3041 . # main2-7852 に続く
3042 . #
3043 . # *****
3044 . # END OF PROCEDURE
3045 . # *****

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[illegible]

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0050 . # (9) Enabled Selected Categories ボタンをクリック
0051 . # (ログ取得開始)
0052 . # (10) System Control 画面で“CCI”をクリック
0053 . # (11) Interface Management:
0054 . #     Open Interface
0055 . #     TM Distribution: Enable
0056 . #     TC Handling: Enable
0057 . #     をクリック
0058 . # (12) I/F 変換装置の Operation Log 画面で以下のメッセージを確認する
0059 . #     Socket opened CCS/MIS-EGSE 3000
0060 . #     Socket opened CCS/MIS-EGSE 3001
0061 . # (13) System Control 画面で“MFE”をクリック
0062 . # (14) Bus profile ボタンをクリック
0063 . # (15) Polling Sequence Table Selection and Verification において
0064 . #     ファイル BCMIS_MM0 PST defs\BCMIS_MM0.PST を選択し、Verify ボタンを
0065 . #     クリック → load ボタンをクリック
0066 . # (16) Polling Sequence Table Control において、start ボタンをクリック
0067 . #     (1553B バスへの Sync メッセージ出力開始)
0068 . # (17) System Control 画面で“PFE”をクリック
0069 . # (18) Power Control Bus A の“OFF”をクリック
0070 . #     “OFF”→“ON”になる
0071 . # (19) External Bus Power Supply の、DC VOLTS 表示 = 28.00 を確認する
0072 . # (20) PFE Platform の、Under Voltage の LED=消灯を確認する
0073 . # (21) System Control → DFE → Aquisition Control → Start をクリック
0074 . #
0075 . # 【MIS 操作】 System Control→MFE→Platform→Bus:A 選択→Apply
0076 . #
0077 . # 【操作】
0078 . # 下記手順に従い、MIS にて 1553B バスモニタを開始する。
0079 . # (1)start 1553 BM client アイコンをダブルクリック
0080 . #     Bus Monitor が起動する
0081 . # (2)Main Menu: Mode → On-Line Mode クリック
0082 . # (3)Main Menu: Mode → Start New Acquisition クリック
0083 . #
0084 . # 【局管制 設定】
0085 . # 初期設定          : INIT_MM0_RF. → INIT_MM0_RD.
0086 . #
0087 . # 【衛星管制 設定】
0088 . # 接続 CMD          : ccs-mis-if1
0089 . # 接続 TLM          : ccs-mis-if1
0090 . # プリチェック      : OFF
0091 . # センドベリファイ  : 強制 Type-B
0092 . # サクセスベリファイ: OFF
0093 . # RF/LINE 切換え     : RF
0094 . #
0095 . # 実運用(ESOC からの運用 or MP0 MTL による運用)を想定し、
0096 . # プリチェック OFF、センドベリファイ 強制 Type-B、サクセスベリファイ OFF とする。
0097 . #
0098 . # 【地上装置設定】
0099 . # EXT-PS CV: 90.0V
0100 . #     CC: 6.0A
0101 . # OVP     54.0V

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0102 # UVP      32.0V
0103 #
0104 . # 【共通 QL 設定】
0105 # MPO_RL_X 選択
0106 #
0107 # =====
0108 . # 1.2. 衛星立ち上げ
0109 # =====
0110 # -----
0111 . # 1.2.1. CNV-A MAIN MODE 選択
0112 # -----
0113 #
0114 . # MIS にて “HPC_Command1(MIS 出力 ch37)”を出力し、CNV-A の MODE を“MAIN”へ
0115 # 切り替えること。
0116 #
0117 . # 【MIS コマンド操作】
0118 # “SYSTEM Control → DFE → IO Activation Control → IO Number → 37
0119 # → Activate”
0120 #
0121 . # 【MIS ステータス確認操作】
0122 # “View → TM/TC Data Monitor → FE Data Filter
0123 # → DFE TM Packet をチェック → Data Category List 右クリック
0124 # → Track Latest Entry → Datafield Decode → Acquisition Data
0125 # → Relay Status 参照”
0126 #
0127 . # MIS          Check-Item          Spec          Check
0128 # -----+-----+-----+-----
0129 # Relay Status  CNV-A MAIN (RLY6)      SHORT:32767(dec) 以下      G  N
0130 #                CNV-A HTR  (RLY7)      OPEN :32768(dec) 以上      G  N
0131 #
0132 # -----
0133 . # 1.2.2. CNV-B MAIN MODE 選択
0134 # -----
0135 #
0136 . # MIS にて “HPC_Command3(MIS 出力 ch39)”を出力し、CNV-B の MODE を“MAIN”へ
0137 # 切り替えること。
0138 #
0139 . # 【MIS コマンド操作】
0140 # “SYSTEM Control → DFE → IO Activation Control → IO Number → 39
0141 # → Activate”
0142 #
0143 . # 【MIS ステータス確認操作】
0144 # “View → TM/TC Data Monitor → FE Data Filter
0145 # → DFE TM Packet をチェック → Data Category List 右クリック
0146 # → Track Latest Entry → Datafield Decode → Acquisition Data
0147 # → Relay Status 参照”
0148 #
0149 . # MIS          Check-Item          Spec          Check
0150 # -----+-----+-----+-----
0151 # Relay Status  CNV-B MAIN (RLY13)     SHORT :32767(dec) 以下      G  N
0152 #                CNV-B HTR  (RLY14)     OPEN  :32768(dec) 以上      G  N
0153 #

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0154  # -----
0155  # 1.2.3. 衛星立ち上げ(CNV)
0156  # -----
0157  #
0158  # 【MIS コマンド操作】
0159  # MIS 電源出力 A 系 ON(SV-HTR)
0160  # "SYSTEM Control → PFE → Modules[2] → PRI → ON → YES"
0161  #
0162  # -----
0163  # 1.2.4. CMD/TLM LINK 確認(1553B)
0164  # -----
0165  #
0166  # < CHECK >
0167  CHECK DH. CATEGORY_TBL_NO          0x00
0168  CHECK DH. DWNLNK_RATE              =BPS32768
0169  CHECK DH. ATMC_FMT_ERR              =OK
0170  CHECK DH. ATMC_SUM_STAT             =OK
0171  CHECK DH. MC_FMT_ERR                =OK
0172  CHECK DH. MC_SUM_STAT               =OK
0173  CHECK DH. RQMC_FMT_ERR              =OK
0174  CHECK DH. RQMC_SUM_STAT             =OK
0175  CHECK DH. SYSTM_R_FMT_ERR           =OK
0176  CHECK DH. SYSTM_R_SUM_STAT          =OK
0177  CHECK DH. TL_FMT_ERR                =OK
0178  CHECK DH. TL_SUM_STAT               =OK
0179  CHECK DH. CATGRY_SUM_STAT           =OK
0180  CHECK DH. TM1TBL_SUM_STAT           =OK
0181  CHECK DH. TM2TBL_SUM_STAT           =OK
0182  CHECK DH. ONOFKY_SUMERR             =OK
0183  CHECK DH. CPU_CMD_RESET             =NON
0184  CHECK DH. CPUA_ECC_2BE              =NON
0185  CHECK DH. CPUA_SW_RST               =NON
0186  CHECK DH. CPUA_WDT_OVF              =NON
0187  CHECK DH. CPUB_ECC_2BE              =NON
0188  CHECK DH. CPUB_SW_RST               =NON
0189  CHECK DH. CPUB_WDT_OVF              =NON
0190  CHECK DH. CPU_AB                    =CPU_A
0191  CHECK DH. HK_ACFIS_VALID            =ENA
0192  CHECK DH. HK_DHFS_VALID             =ENA
0193  CHECK DH. HK_SPW_VALID              =ENA
0194  CHECK DH. HK_TCFIS_VALID            =ENA
0195  CHECK DH. NML_IPL                   =NML
0196  CHECK DH. TC1U_ECC_2BE              =NON
0197  CHECK DH. DR_SUM_STAT               =OK
0198  CHECK PCD. APR1_ON_OFF              =OFF
0199  CHECK PCD. APR2_ON_OFF              =ON
0200  #
0201  # < CHECK >
0202  CHECK DH. ENA_TEMP                  -
0203  CHECK DH. TANK_TEMP                 -
0204  CHECK DH. ADM_TEMP                  -
0205  CHECK DH. APM_TEMP                  -

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|      |                           |       |
|------|---------------------------|-------|
| 0206 | CHECK DH. UP_PNL2_TEMP    | -     |
| 0207 | CHECK DH. UP_PNL3_TEMP    | -     |
| 0208 | CHECK DH. UP_PNL6_TEMP    | -     |
| 0209 | CHECK DH. UP_PNL7_TEMP    | -     |
| 0210 | CHECK DH. UP_PNL8_TEMP    | -     |
| 0211 | CHECK DH. LO_PNL1_TEMP    | -     |
| 0212 | CHECK DH. LO_PNL2_TEMP    | -     |
| 0213 | CHECK DH. LO_PNL5_TEMP    | -     |
| 0214 | CHECK DH. LO_PNL6_TEMP    | -     |
| 0215 | CHECK DH. LO_PNL8_TEMP    | -     |
| 0216 | CHECK DH. SSAS_TEMP       | -     |
| 0217 | CHECK DH. THR_T1T2_TEMP   | -     |
| 0218 | CHECK DH. SSC_TEMP1       | -     |
| 0219 | CHECK DH. ACM1_TEMP       | -     |
| 0220 | CHECK DH. THR_T3T4_TEMP   | -     |
| 0221 | CHECK DH. ACM2_TEMP       | -     |
| 0222 | CHECK DH. THR_A1_TEMP     | -     |
| 0223 | CHECK DH. THR_A2_TEMP     | -     |
| 0224 | CHECK DH. VLV_M_TEMP      | -     |
| 0225 | CHECK DH. EPC_A_TEMP      | -     |
| 0226 | CHECK DH. EPC_B_TEMP      | -     |
| 0227 | CHECK DH. TWT_A_TEMP      | -     |
| 0228 | CHECK DH. TWT_B_TEMP      | -     |
| 0229 | CHECK DH. XTRP_A_TEMP     | -     |
| 0230 | CHECK DH. XTRP_B_TEMP     | -     |
| 0231 | CHECK DH. BAT_TEMP1       | -     |
| 0232 | CHECK DH. BAT_TEMP2       | -     |
| 0233 | CHECK DH. SSC_TEMP2       | -     |
| 0234 | CHECK DH. SIDE1_TEMP      | -     |
| 0235 | CHECK DH. SIDE2_TEMP      | -     |
| 0236 | CHECK DH. SIDE3_TEMP      | -     |
| 0237 | CHECK DH. SIDE4_TEMP      | -     |
| 0238 | CHECK DH. UP_PNL1_TEMP    | -     |
| 0239 | CHECK DH. UP_PNL5_TEMP    | -     |
| 0240 | CHECK DH. MAST_MGF_TEMP   | -     |
| 0241 | CHECK DH. LO_PNL3_TEMP    | -     |
| 0242 | CHECK DH. LO_PNL7_TEMP    | -     |
| 0243 | CHECK DH. SIDE5_TEMP      | -     |
| 0244 | CHECK DH. SIDE6_TEMP      | -     |
| 0245 | CHECK DH. SIDE7_TEMP      | -     |
| 0246 | CHECK DH. SIDE8_TEMP      | -     |
| 0247 | . #                       |       |
| 0248 | DH. MC_START              |       |
| 0249 | . #                       |       |
| 0250 | . # < CHECK >             |       |
| 0251 | CHECK DH. MC_ENA_DIS      | =ENA  |
| 0252 | CHECK PCD. ON_OFF_ENA_DIS | =DIS  |
| 0253 | CHECK PCD. BUS_V          | 48 52 |
| 0254 | . #                       |       |
| 0255 | . # 【衛星管制設定】              |       |
| 0256 | . # プリチェック : OFF          |       |
| 0257 | . # センドベリファイ : 強制 TYPE-B  |       |

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0258 . # サクセスベリファイ: OFF
0259 . # COP N(S)値設定
0260 . #
0261 . # QL          Check-Item          Spec          Check
0262 . # -----+-----+-----+-----
0263 . # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
0264 . #
0265 . # -----+-----+-----+-----
0266 . # 1.2.5. BAT INT
0267 . # -----+-----+-----+-----
0268 . #
0269 . PCD. BAT_OT_DIS
0270 . WAIT_SEC 2
0271 . PCD. BAT_OT_ON_SET 0xBD  # 28degC
0272 . WAIT_SEC 2
0273 . DH. BAT_SM_EXE
0274 . WAIT_SEC 2
0275 . PCD. BAT_OT_OFF_SET 0xC5  # 26degC
0276 . WAIT_SEC 2
0277 . DH. BAT_SM_EXE
0278 . WAIT_SEC 2
0279 . PCD. CEL_V_MONI_ON          # 【BAT 管理手順書 (TPP-7S8238-03K) へ記録】
0280 . #
0281 . # < CHECK >
0282 . CHECK PCD. CEL1_V          >3. 705
0283 . CHECK PCD. CEL2_V          >3. 705
0284 . CHECK PCD. CEL3_V          >3. 705
0285 . CHECK PCD. CEL4_V          >3. 705
0286 . CHECK PCD. CEL5_V          >3. 705
0287 . CHECK PCD. CEL6_V          >3. 705
0288 . CHECK PCD. CEL7_V          >3. 705
0289 . CHECK PCD. CEL8_V          >3. 705
0290 . CHECK PCD. CEL9_V          >3. 705
0291 . CHECK PCD. CEL10_V         >3. 705
0292 . CHECK PCD. CEL11_V         >3. 705
0293 . CHECK PCD. CEL12_V         >3. 705
0294 . CHECK PCD. BAT_INT_EXT     =EXT
0295 . CHECK PCD. BUS_SW_ON_OFF    =OFF
0296 . CHECK PCD. BAT_OT_ENA_DIS   =DIS
0297 . #
0298 . # < CHECK >
0299 . CHECK PCD. OT_ON_SET        27.5 28.5
0300 . CHECK PCD. OT_OFF_SET       25.5 26.5
0301 . #
0302 . PCD. BAT_OT_ENA
0303 . #
0304 . # < CHECK >
0305 . CHECK PCD. BAT_OT_ENA_DIS    =ENA
0306 . CHECK PCD. BAT_OT_ON_OFF     =OFF
0307 . #
0308 . # 【確認】                      Check
0309 . # BUS-V > BAT-V であることを確認する。          G  N

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0310  #
0311  . PCD. INT_EXT_ENA
0312  #
0313  . # < CHECK >
0314  CHECK PCD. INT_EXT_ENA_DIS          =ENA
0315  . #
0316  . PCD. BAT_INT
0317  #
0318  . # < CHECK >
0319  CHECK PCD. INT_EXT_ENA_DIS          =DIS
0320  CHECK PCD. BAT_INT_EXT              =INT
0321  CHECK PCD. BUS_SW_ENA_DIS          =DIS
0322  CHECK PCD. BUS_SW_ON_OFF          =OFF
0323  CHECK PCD. CHG_I                  -0.1 0.1
0324  CHECK PCD. DCHG_I                 -0.2 0.2
0325  #
0326  . #
0327  . PCD. BAT_K1_SET 0xA7          # K1 167
0328  WAIT_SEC 2
0329  DH. BAT_SM_EXE
0330  WAIT_SEC 2
0331  PCD. BAT_K2_SET 0xA5          # K2 165
0332  WAIT_SEC 2
0333  DH. BAT_SM_EXE
0334  WAIT_SEC 2
0335  PCD. BAT_CD_SET 0x80          # 10.56Ah
0336  WAIT_SEC 2
0337  DH. BAT_SM_EXE
0338  . #
0339  . # < CHECK >
0340  CHECK PCD. BAT_K1              167
0341  CHECK PCD. BAT_K2              165
0342  CHECK PCD. BAT_CD              10.55 10.57
0343  CHECK PCD. BAT_CV              -
0344  #
0345  . # QL          Check-Item          Spec          Check
0346  # -----+-----+-----+-----+
0347  # CMD 出力装置  Message          STATUS CHECK 結果 OK          G N
0348  #
0349  # -----+-----+-----+-----+
0350  . # 1.2.6. インデックスパルス出力
0351  # -----+-----+-----+-----+
0352  . #
0353  . DH. MC_EACH_EXE 0x0032 # 1553B TLM mode 6 (SYS HK 1/2, BUS User HK 1/4, DUMP 1/4,
0354  #:          << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0355  #:          0x00 0x00 DH. DMP_PERIOD 0x02
0356  #:          0x02 0x02 ACS. ACS_PKT_FMT_1
0357  #:          0x05 0x05 DH. OPE_MODE 0x0C 0x26 0x28 0x03 0x000000000000000000000000
0358  #:          0x06 0x06 DH. TLMFMT1553B_CHG 0x06
0359  #
0360  WAIT_SEC 40
0361  #

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0362 CHECK DH. DMP_PERIOD =SEC_4
0363 CHECK DH. CATEGORY_TBL_NO 0x28
0364 CHECK DH. DWNLNK_RATE =BPS16384
0365 CHECK DH. TLM1553BFMTNO 0x06
0366 CHECK ACS. F_ACS_PKT_FMT =ACSHK1
0367 #
0368 . # < CHECK >
0369 CHECK ACS. F_ACS_PKT_FMT =ACSHK1
0370 CHECK ACS. STS_IP_SRC =NULL
0371 CHECK ACS. STS_IP_OUT =DIS
0372 CHECK ACS. STS_IP_GNRT =DIS
0373 CHECK ACS. I_IPCHK =RST
0374 CHECK ACS. I_IP_GNRT =SELF
0375 CHECK ACS_SYS. I_IPCHK =RST
0376 CHECK ACS_SYS. I_IP_GNRT =SELF
0377 CHECK ACS. BIT_I_IPCHK =RST
0378 #
0379 . # SSAS を擬似モードにする
0380 . ACS. SSAS_SEL_IMT
0381 #
0382 # 疑似サンパルスを発生
0383 . # (SIN:2.5V, COS:5.0V, START:0x80, COARSE:0x10, Period:4.138sec)
0384 . ACS. DUM_SP_ON 2047 4095 128 16 8475
0385 #
0386 . # < CHECK >
0387 CHECK ACS. SSAS_ST_PT =IMT
0388 CHECK ACS. SSAS_ST =IMT
0389 CHECK ACS. F_SPN_ERR_SEL =OFF
0390 CHECK ACS. P_SPIN_SSAS_SEL 4.137 4.139
0391 CHECK ACS. THT_CRS_SEL 0x10
0392 CHECK ACS. SUN_ANG_COS 4.999 5.001
0393 CHECK ACS. SUN_ANG_SIN 2.499 2.501
0394 CHECK ACS. SUN_ANG_SEL -2 2
0395 CHECK ACS_SYS. SSAS_ST =IMT
0396 CHECK ACS_SYS. P_SPIN_SSAS_SEL 4.137 4.139
0397 CHECK ACS_SYS. THT_CRS_SEL 0x10
0398 CHECK ACS_SYS. SUN_ANG_SEL -2 2
0399 CHECK ACS. BIT_SSAS_ST =IMT
0400 CHECK ACS. BIT_P_SPIN_SSAS_SEL 4.013 4.263
0401 CHECK ACS. BIT_SUN_ANG_SEL -0.125 0.125
0402 #
0403 . # ACS HK パケットフォーマットを ACS_HK4 に設定
0404 . ACS. ACS_PKT_FMT_4
0405 #
0406 . # < CHECK >
0407 CHECK ACS. SSAS_IMT_SP_STS =ON
0408 CHECK ACS. E_SSAS_SPIN 0.0999 0.1001
0409 CHECK ACS. N_SPINRATE_AVE 5 5
0410 CHECK ACS_SYS. F_ECL =OFF
0411 CHECK ACS_SYS. I_ATT =STBY
0412 CHECK ACS_SYS. P_B_AVE 4.137 4.139
0413 #

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0414 . # IP フィードバックゲインを設定する。
0415 . ACS. IP_K_DT 0x3C9BA5E3 0x3C9BA5E3
0416 #
0417 . # < CHECK >
0418 CHECK ACS. K_DT0 0.018999 0.019001
0419 CHECK ACS. K_DT1 0.018999 0.019001
0420 #
0421 . # IP 源泉を IP にする
0422 . ACS. IP_SRC_IP
0423 # IP 生成機能を ON にする
0424 WAIT_SEC 2
0425 ACS. IP_GNRT_ON
0426 # MDP への IP 出力を開始する
0427 WAIT_SEC 2
0428 ACS. IP_OUT_ENA
0429 # ACS HK パケットフォーマットを ACS_HK1 に設定
0430 WAIT_SEC 2
0431 ACS. ACS_PKT_FMT_1
0432 #
0433 . # < CHECK >
0434 CHECK ACS. STS_IP_SRC =IP
0435 CHECK ACS. STS_IP_OUT =ENA
0436 CHECK ACS. STS_IP_GNRT =ENA
0437 CHECK ACS. I_IP_GNRT =SSAS
0438 CHECK ACS_SYS. I_IP_GNRT =SSAS
0439 #
0440 . # << DR STATUS CHECK >>
0441 #
0442 . # < CHECK >
0443 CHECK DH. DR_STATE =STANDBY
0444 CHECK DH. DR_CHK_ON_OFF =OFF
0445 #
0446 . DH. DR_ERRCLR
0447 . # QL Check-Item Spec Check
0448 # -----+-----+-----+-----+
0449 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0450 #
0451 # -----+-----+-----+-----+
0452 . # 1.2.7. TCFS 設定
0453 # -----+-----+-----+-----+
0454 #
0455 . PCD. ON_OFF_ENA
0456 #
0457 . # < CHECK >
0458 CHECK PCD. ON_OFF_ENA_DIS =ENA
0459 #
0460 . PCD. DRU_PSU_A_ON
0461 #
0462 . # < CHECK >
0463 CHECK PCD. DRU_PSU_A_ON_OFF =ON
0464 #
0465 . PCD. ON_OFF_DIS

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0466 . #
0467 . # < CHECK >
0468 CHECK PCD.ON_OFF_ENA_DIS =DIS
0469 . #
0470 . ACS.DRU_SEL_A
0471 . #
0472 . # < CHECK >
0473 CHECK ACS.DRU_SEL_PT =A
0474 CHECK ACS_SYS.DRU_SEL_PT =A
0475 CHECK ACS.BIT_DRU_SEL_PT =A
0476 . #
0477 . ACS.ACS_IOPERR_CLR
0478 . #
0479 . # < CHECK >
0480 CHECK ACS.DRU_A_ONOFF_ST =ON
0481 CHECK ACS.DRU_B_ONOFF_ST =OFF
0482 CHECK ACS.DRU_ACIMIF_A_ST =ON
0483 CHECK ACS.DRU_ACIMIF_B_ST =OFF
0484 CHECK ACS.SRAM1_MEMST =MEMO
0485 CHECK ACS.SRAM2_MEMST =MEMO
0486 CHECK ACS.C_DRU_CMD_ECC2 0
0487 CHECK ACS.C_DRU_TLM_ECC2 0
0488 CHECK ACS.C_LBUSU_ECC1 0
0489 CHECK ACS.C_LBUSL_ECC1 0
0490 CHECK ACS.C_DRU_CMD_Prt 0
0491 CHECK ACS.C_DRU_TLM_Prt 0
0492 CHECK ACS.DATA_ERR_STATUS =OK
0493 CHECK ACS.STS_DRUAPM_ON =OFF
0494 CHECK ACS.I_ADMCE_A_PWR =OFF
0495 CHECK ACS.I_ADMCE_B_PWR =OFF
0496 CHECK ACS.ADM_SEL_PORT1 =A
0497 CHECK ACS.SRAM_ST =SRAM1
0498 CHECK ACS_SYS.DRU_A_ONOFF_ST =ON
0499 CHECK ACS_SYS.DRU_B_ONOFF_ST =OFF
0500 CHECK ACS_SYS.DRU_ACIMIF_A_ST =ON
0501 CHECK ACS_SYS.DRU_ACIMIF_B_ST =OFF
0502 CHECK ACS_SYS.SRAM1_MEMST =MEMO
0503 CHECK ACS_SYS.SRAM2_MEMST =MEMO
0504 CHECK ACS_SYS.C_DRU_CMD_ECC2 0
0505 CHECK ACS_SYS.C_DRU_TLM_ECC2 0
0506 CHECK ACS_SYS.C_LBUSU_ECC1 0
0507 CHECK ACS_SYS.C_LBUSL_ECC1 0
0508 CHECK ACS_SYS.C_DRU_CMD_Prt 0
0509 CHECK ACS_SYS.C_DRU_TLM_Prt 0
0510 CHECK ACS_SYS.DATA_ERR_STATUS =OK
0511 CHECK ACS_SYS.STS_DRUAPM_ON =OFF
0512 CHECK ACS_SYS.I_ADMCE_A_PWR =OFF
0513 CHECK ACS_SYS.I_ADMCE_B_PWR =OFF
0514 CHECK ACS_SYS.ADM_SEL_PORT1 =A
0515 CHECK ACS_SYS.SRAM_ST =SRAM1
0516 CHECK ACS.BIT_DRU_A_ONOFF_ST =ON
0517 CHECK ACS.BIT_DRU_B_ONOFF_ST =OFF

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|      |                                          |                          |
|------|------------------------------------------|--------------------------|
| 0518 | CHECK ACS. BIT_I_ADMCE_A_PWR             | =OFF                     |
| 0519 | CHECK ACS. BIT_I_ADMCE_B_PWR             | =OFF                     |
| 0520 | . #                                      |                          |
| 0521 | . ACS. ACS_PKT_FMT_4                     |                          |
| 0522 | . #                                      |                          |
| 0523 | . # < CHECK >                            |                          |
| 0524 | CHECK ACS. F_ACS_PKT_FMT                 | =ACSHK4                  |
| 0525 | CHECK ACS. I_ECCERR_S_D                  | 0                        |
| 0526 | CHECK ACS. I_ECCERR_S_L                  | 0                        |
| 0527 | CHECK ACS. I_DRU_ACIMIF1                 | 0                        |
| 0528 | CHECK ACS. I_DRU_ACIMIF2                 | 0                        |
| 0529 | CHECK ACS. I_DRU_ACIMIF3                 | 0                        |
| 0530 | CHECK ACS. I_DRU_ACIMIF4                 | 0                        |
| 0531 | CHECK ACS. DRU_CMD_ECC2                  | 0                        |
| 0532 | CHECK ACS. DRU_TLM_ECC2                  | 0                        |
| 0533 | CHECK ACS. DRU_CMD_Parity1               | 0                        |
| 0534 | CHECK ACS. DRU_CMD_Parity2               | 0                        |
| 0535 | CHECK ACS. DRU_TLM_Parity1               | 0                        |
| 0536 | CHECK ACS. DRU_TLM_Parity2               | 0                        |
| 0537 | CHECK ACS. I_CMD_FLAG1                   | 0x02ec0c00               |
| 0538 | CHECK ACS. I_CMD_FLAG2                   | 0                        |
| 0539 | . #                                      |                          |
| 0540 | . ACS. ACS_PKT_FMT_1                     |                          |
| 0541 | . #                                      |                          |
| 0542 | . # < CHECK >                            |                          |
| 0543 | CHECK ACS. F_ACS_PKT_FMT                 | =ACSHK1                  |
| 0544 | . #                                      |                          |
| 0545 | . TCS. SET_TBL_ENA                       |                          |
| 0546 | . #                                      |                          |
| 0547 | . # < CHECK >                            |                          |
| 0548 | CHECK TCS. SET_TBL_ST                    | =ENA                     |
| 0549 | . #                                      |                          |
| 0550 | . # < BKUP DTY TBL1 >                    |                          |
| 0551 | . TCS. SET_BKUP_DTY_TBL 0x00 0x00 0x0020 | # BKUP DTY TBL1 CH01: 32 |
| 0552 | WAIT_SEC 2                               |                          |
| 0553 | . TCS. SET_BKUP_DTY_TBL 0x00 0x01 0x0020 | # BKUP DTY TBL1 CH02: 32 |
| 0554 | WAIT_SEC 2                               |                          |
| 0555 | . TCS. SET_BKUP_DTY_TBL 0x00 0x02 0x0020 | # BKUP DTY TBL1 CH03: 32 |
| 0556 | WAIT_SEC 2                               |                          |
| 0557 | . TCS. SET_BKUP_DTY_TBL 0x00 0x03 0x0020 | # BKUP DTY TBL1 CH04: 32 |
| 0558 | WAIT_SEC 2                               |                          |
| 0559 | . TCS. SET_BKUP_DTY_TBL 0x00 0x04 0x0020 | # BKUP DTY TBL1 CH05: 32 |
| 0560 | WAIT_SEC 2                               |                          |
| 0561 | . TCS. SET_BKUP_DTY_TBL 0x00 0x05 0x0020 | # BKUP DTY TBL1 CH06: 32 |
| 0562 | WAIT_SEC 2                               |                          |
| 0563 | . TCS. SET_BKUP_DTY_TBL 0x00 0x06 0x0020 | # BKUP DTY TBL1 CH07: 32 |
| 0564 | WAIT_SEC 2                               |                          |
| 0565 | . TCS. SET_BKUP_DTY_TBL 0x00 0x07 0x0020 | # BKUP DTY TBL1 CH08: 32 |
| 0566 | WAIT_SEC 2                               |                          |
| 0567 | . TCS. SET_BKUP_DTY_TBL 0x00 0x08 0x0020 | # BKUP DTY TBL1 CH09: 32 |
| 0568 | WAIT_SEC 2                               |                          |
| 0569 | . TCS. SET_BKUP_DTY_TBL 0x00 0x09 0x0020 | # BKUP DTY TBL1 CH10: 32 |

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| 0570 | WAIT_SEC 2                            |                          |
| 0571 | TCS.SET_BKUP_DTY_TBL 0x00 0x0A 0x0020 | # BKUP DTY TBL1 CH11: 32 |
| 0572 | WAIT_SEC 2                            |                          |
| 0573 | TCS.SET_BKUP_DTY_TBL 0x00 0x0B 0x0020 | # BKUP DTY TBL1 CH12: 32 |
| 0574 | WAIT_SEC 2                            |                          |
| 0575 | TCS.SET_BKUP_DTY_TBL 0x00 0x0C 0x0020 | # BKUP DTY TBL1 CH13: 32 |
| 0576 | WAIT_SEC 2                            |                          |
| 0577 | TCS.SET_BKUP_DTY_TBL 0x00 0x0D 0x0000 | # BKUP DTY TBL1 CH14: 0  |
| 0578 | WAIT_SEC 2                            |                          |
| 0579 | TCS.SET_BKUP_DTY_TBL 0x00 0x0E 0x0000 | # BKUP DTY TBL1 CH15: 0  |
| 0580 | WAIT_SEC 2                            |                          |
| 0581 | TCS.SET_BKUP_DTY_TBL 0x00 0x0F 0x0000 | # BKUP DTY TBL1 CH16: 0  |
| 0582 | WAIT_SEC 2                            |                          |
| 0583 | TCS.SET_BKUP_DTY_TBL 0x00 0x10 0x0020 | # BKUP DTY TBL1 CH17: 32 |
| 0584 | WAIT_SEC 2                            |                          |
| 0585 | TCS.SET_BKUP_DTY_TBL 0x00 0x11 0x0020 | # BKUP DTY TBL1 CH18: 32 |
| 0586 | WAIT_SEC 2                            |                          |
| 0587 | TCS.SET_BKUP_DTY_TBL 0x00 0x12 0x0020 | # BKUP DTY TBL1 CH19: 32 |
| 0588 | WAIT_SEC 2                            |                          |
| 0589 | TCS.SET_BKUP_DTY_TBL 0x00 0x13 0x0020 | # BKUP DTY TBL1 CH20: 32 |
| 0590 | WAIT_SEC 2                            |                          |
| 0591 | TCS.SET_BKUP_DTY_TBL 0x00 0x14 0x0020 | # BKUP DTY TBL1 CH21: 32 |
| 0592 | WAIT_SEC 2                            |                          |
| 0593 | TCS.SET_BKUP_DTY_TBL 0x00 0x15 0x0020 | # BKUP DTY TBL1 CH22: 32 |
| 0594 | WAIT_SEC 2                            |                          |
| 0595 | TCS.SET_BKUP_DTY_TBL 0x00 0x16 0x0020 | # BKUP DTY TBL1 CH23: 32 |
| 0596 | WAIT_SEC 2                            |                          |
| 0597 | TCS.SET_BKUP_DTY_TBL 0x00 0x17 0x0020 | # BKUP DTY TBL1 CH24: 32 |
| 0598 | WAIT_SEC 2                            |                          |
| 0599 | TCS.SET_BKUP_DTY_TBL 0x00 0x18 0x0020 | # BKUP DTY TBL1 CH25: 32 |
| 0600 | WAIT_SEC 2                            |                          |
| 0601 | TCS.SET_BKUP_DTY_TBL 0x00 0x19 0x0020 | # BKUP DTY TBL1 CH26: 32 |
| 0602 | WAIT_SEC 2                            |                          |
| 0603 | TCS.SET_BKUP_DTY_TBL 0x00 0x1A 0x0020 | # BKUP DTY TBL1 CH27: 32 |
| 0604 | WAIT_SEC 2                            |                          |
| 0605 | TCS.SET_BKUP_DTY_TBL 0x00 0x1B 0x0020 | # BKUP DTY TBL1 CH28: 32 |
| 0606 | WAIT_SEC 2                            |                          |
| 0607 | TCS.SET_BKUP_DTY_TBL 0x00 0x1C 0x0020 | # BKUP DTY TBL1 CH29: 32 |
| 0608 | WAIT_SEC 2                            |                          |
| 0609 | TCS.SET_BKUP_DTY_TBL 0x00 0x1D 0x0000 | # BKUP DTY TBL1 CH30: 0  |
| 0610 | WAIT_SEC 2                            |                          |
| 0611 | TCS.SET_BKUP_DTY_TBL 0x00 0x1E 0x0000 | # BKUP DTY TBL1 CH31: 0  |
| 0612 | WAIT_SEC 2                            |                          |
| 0613 | TCS.SET_BKUP_DTY_TBL 0x00 0x1F 0x0000 | # BKUP DTY TBL1 CH32: 0  |
| 0614 | #                                     |                          |
| 0615 | # < NMNL DTY TBL1 >                   |                          |
| 0616 | TCS.SET_NMNL_DTY_TBL 0x00 0x00 0x0040 | # NMNL DTY TBL1 CH01: 64 |
| 0617 | WAIT_SEC 2                            |                          |
| 0618 | TCS.SET_NMNL_DTY_TBL 0x00 0x01 0x0040 | # NMNL DTY TBL1 CH02: 64 |
| 0619 | WAIT_SEC 2                            |                          |
| 0620 | TCS.SET_NMNL_DTY_TBL 0x00 0x02 0x0040 | # NMNL DTY TBL1 CH03: 64 |
| 0621 | WAIT_SEC 2                            |                          |

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| 0622 | TCS.SET_NMNL_DTY_TBL 0x00 0x03 0x0040 | # NMNL DTY TBL1 CH04: 64 |
| 0623 | WAIT_SEC 2                            |                          |
| 0624 | TCS.SET_NMNL_DTY_TBL 0x00 0x04 0x0040 | # NMNL DTY TBL1 CH05: 64 |
| 0625 | WAIT_SEC 2                            |                          |
| 0626 | TCS.SET_NMNL_DTY_TBL 0x00 0x05 0x0040 | # NMNL DTY TBL1 CH06: 64 |
| 0627 | WAIT_SEC 2                            |                          |
| 0628 | TCS.SET_NMNL_DTY_TBL 0x00 0x06 0x0040 | # NMNL DTY TBL1 CH07: 64 |
| 0629 | WAIT_SEC 2                            |                          |
| 0630 | TCS.SET_NMNL_DTY_TBL 0x00 0x07 0x0040 | # NMNL DTY TBL1 CH08: 64 |
| 0631 | WAIT_SEC 2                            |                          |
| 0632 | TCS.SET_NMNL_DTY_TBL 0x00 0x08 0x0040 | # NMNL DTY TBL1 CH09: 64 |
| 0633 | WAIT_SEC 2                            |                          |
| 0634 | TCS.SET_NMNL_DTY_TBL 0x00 0x09 0x0040 | # NMNL DTY TBL1 CH10: 64 |
| 0635 | WAIT_SEC 2                            |                          |
| 0636 | TCS.SET_NMNL_DTY_TBL 0x00 0x0A 0x0040 | # NMNL DTY TBL1 CH11: 64 |
| 0637 | WAIT_SEC 2                            |                          |
| 0638 | TCS.SET_NMNL_DTY_TBL 0x00 0x0B 0x0040 | # NMNL DTY TBL1 CH12: 64 |
| 0639 | WAIT_SEC 2                            |                          |
| 0640 | TCS.SET_NMNL_DTY_TBL 0x00 0x0C 0x0040 | # NMNL DTY TBL1 CH13: 64 |
| 0641 | WAIT_SEC 2                            |                          |
| 0642 | TCS.SET_NMNL_DTY_TBL 0x00 0x0D 0x0000 | # NMNL DTY TBL1 CH14: 0  |
| 0643 | WAIT_SEC 2                            |                          |
| 0644 | TCS.SET_NMNL_DTY_TBL 0x00 0x0E 0x0000 | # NMNL DTY TBL1 CH15: 0  |
| 0645 | WAIT_SEC 2                            |                          |
| 0646 | TCS.SET_NMNL_DTY_TBL 0x00 0x0F 0x0000 | # NMNL DTY TBL1 CH16: 0  |
| 0647 | WAIT_SEC 2                            |                          |
| 0648 | TCS.SET_NMNL_DTY_TBL 0x00 0x10 0x0040 | # NMNL DTY TBL1 CH17: 64 |
| 0649 | WAIT_SEC 2                            |                          |
| 0650 | TCS.SET_NMNL_DTY_TBL 0x00 0x11 0x0040 | # NMNL DTY TBL1 CH18: 64 |
| 0651 | WAIT_SEC 2                            |                          |
| 0652 | TCS.SET_NMNL_DTY_TBL 0x00 0x12 0x0040 | # NMNL DTY TBL1 CH19: 64 |
| 0653 | WAIT_SEC 2                            |                          |
| 0654 | TCS.SET_NMNL_DTY_TBL 0x00 0x13 0x0040 | # NMNL DTY TBL1 CH20: 64 |
| 0655 | WAIT_SEC 2                            |                          |
| 0656 | TCS.SET_NMNL_DTY_TBL 0x00 0x14 0x0040 | # NMNL DTY TBL1 CH21: 64 |
| 0657 | WAIT_SEC 2                            |                          |
| 0658 | TCS.SET_NMNL_DTY_TBL 0x00 0x15 0x0040 | # NMNL DTY TBL1 CH22: 64 |
| 0659 | WAIT_SEC 2                            |                          |
| 0660 | TCS.SET_NMNL_DTY_TBL 0x00 0x16 0x0040 | # NMNL DTY TBL1 CH23: 64 |
| 0661 | WAIT_SEC 2                            |                          |
| 0662 | TCS.SET_NMNL_DTY_TBL 0x00 0x17 0x0040 | # NMNL DTY TBL1 CH24: 64 |
| 0663 | WAIT_SEC 2                            |                          |
| 0664 | TCS.SET_NMNL_DTY_TBL 0x00 0x18 0x0040 | # NMNL DTY TBL1 CH25: 64 |
| 0665 | WAIT_SEC 2                            |                          |
| 0666 | TCS.SET_NMNL_DTY_TBL 0x00 0x19 0x0040 | # NMNL DTY TBL1 CH26: 64 |
| 0667 | WAIT_SEC 2                            |                          |
| 0668 | TCS.SET_NMNL_DTY_TBL 0x00 0x1A 0x0040 | # NMNL DTY TBL1 CH27: 64 |
| 0669 | WAIT_SEC 2                            |                          |
| 0670 | TCS.SET_NMNL_DTY_TBL 0x00 0x1B 0x0040 | # NMNL DTY TBL1 CH28: 64 |
| 0671 | WAIT_SEC 2                            |                          |
| 0672 | TCS.SET_NMNL_DTY_TBL 0x00 0x1C 0x0040 | # NMNL DTY TBL1 CH29: 64 |
| 0673 | WAIT_SEC 2                            |                          |

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0674 TCS.SET_NMNL_DTY_TBL 0x00 0x1D 0x0000 # NMNL DTY TBL1 CH30: 0
0675 WAIT_SEC 2
0676 TCS.SET_NMNL_DTY_TBL 0x00 0x1E 0x0000 # NMNL DTY TBL1 CH31: 0
0677 WAIT_SEC 2
0678 TCS.SET_NMNL_DTY_TBL 0x00 0x1F 0x0000 # NMNL DTY TBL1 CH32: 0
0679 #
0680 # < NMNL TBL1 ヒータ ON/OFF スレッシュホールド設定 >
0681 TCS.SET_NMNL_TMP_TBL 0x00 0x00 0x9E 0xA2 # NMNL TBL1 CH01 ON: 27.94 OFF: 30
0682 WAIT_SEC 2
0683 TCS.SET_NMNL_TMP_TBL 0x00 0x01 0x9C 0xA0 # NMNL TBL1 CH02 ON: 27.94 OFF: 30
0684 WAIT_SEC 2
0685 TCS.SET_NMNL_TMP_TBL 0x00 0x02 0x9B 0x9F # NMNL TBL1 CH03 ON: 27.94 OFF: 30
0686 WAIT_SEC 2
0687 TCS.SET_NMNL_TMP_TBL 0x00 0x03 0x98 0x9C # NMNL TBL1 CH04 ON: 27.94 OFF: 30
0688 WAIT_SEC 2
0689 TCS.SET_NMNL_TMP_TBL 0x00 0x04 0x9C 0xA0 # NMNL TBL1 CH05 ON: 27.94 OFF: 30
0690 WAIT_SEC 2
0691 TCS.SET_NMNL_TMP_TBL 0x00 0x05 0x9B 0x9F # NMNL TBL1 CH06 ON: 27.94 OFF: 30
0692 WAIT_SEC 2
0693 TCS.SET_NMNL_TMP_TBL 0x00 0x06 0x9C 0xA0 # NMNL TBL1 CH07 ON: 27.94 OFF: 30
0694 WAIT_SEC 2
0695 TCS.SET_NMNL_TMP_TBL 0x00 0x07 0x83 0x84 # NMNL TBL1 CH08 ON: 28.46 OFF: 29.49
0696 WAIT_SEC 2
0697 TCS.SET_NMNL_TMP_TBL 0x00 0x08 0x9D 0xA1 # NMNL TBL1 CH09 ON: 27.94 OFF: 30
0698 WAIT_SEC 2
0699 TCS.SET_NMNL_TMP_TBL 0x00 0x09 0x9C 0xA0 # NMNL TBL1 CH10 ON: 27.94 OFF: 30
0700 WAIT_SEC 2
0701 TCS.SET_NMNL_TMP_TBL 0x00 0x0A 0x9A 0x9E # NMNL TBL1 CH11 ON: 27.75 OFF: 29.8
0702 WAIT_SEC 2
0703 TCS.SET_NMNL_TMP_TBL 0x00 0x0B 0x9A 0x9E # NMNL TBL1 CH12 ON: 27.94 OFF: 30
0704 WAIT_SEC 2
0705 TCS.SET_NMNL_TMP_TBL 0x00 0x0C 0x9B 0x9F # NMNL TBL1 CH13 ON: 27.94 OFF: 30
0706 WAIT_SEC 2
0707 TCS.SET_NMNL_TMP_TBL 0x00 0x0D 0x00 0x00 # NMNL TBL1 CH14 ON: -50.41 OFF: -50.41
0708 WAIT_SEC 2
0709 TCS.SET_NMNL_TMP_TBL 0x00 0x0E 0x00 0x00 # NMNL TBL1 CH15 ON: -50.41 OFF: -50.41
0710 WAIT_SEC 2
0711 TCS.SET_NMNL_TMP_TBL 0x00 0x0F 0x00 0x00 # NMNL TBL1 CH16 ON: -50.41 OFF: -50.41
0712 WAIT_SEC 2
0713 TCS.SET_NMNL_TMP_TBL 0x00 0x10 0x9E 0xA2 # NMNL TBL1 CH17 ON: 27.94 OFF: 30
0714 WAIT_SEC 2
0715 TCS.SET_NMNL_TMP_TBL 0x00 0x11 0x9D 0xA1 # NMNL TBL1 CH18 ON: 27.94 OFF: 30
0716 WAIT_SEC 2
0717 TCS.SET_NMNL_TMP_TBL 0x00 0x12 0x9C 0xA0 # NMNL TBL1 CH19 ON: 27.94 OFF: 30
0718 WAIT_SEC 2
0719 TCS.SET_NMNL_TMP_TBL 0x00 0x13 0x9C 0xA0 # NMNL TBL1 CH20 ON: 27.94 OFF: 30
0720 WAIT_SEC 2
0721 TCS.SET_NMNL_TMP_TBL 0x00 0x14 0x9D 0xA1 # NMNL TBL1 CH21 ON: 27.94 OFF: 30
0722 WAIT_SEC 2
0723 TCS.SET_NMNL_TMP_TBL 0x00 0x15 0x9B 0x9F # NMNL TBL1 CH22 ON: 27.94 OFF: 30
0724 WAIT_SEC 2
0725 TCS.SET_NMNL_TMP_TBL 0x00 0x16 0x9A 0x9E # NMNL TBL1 CH23 ON: 27.94 OFF: 30

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|      |                                                                                  |                        |
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| 0726 | WAIT_SEC 2                                                                       |                        |
| 0727 | TCS.SET_NMNL_TMP_TBL 0x00 0x17 0x9F 0xA3 # NMNL TBL1 CH24 ON: 27.94 OFF: 30      |                        |
| 0728 | WAIT_SEC 2                                                                       |                        |
| 0729 | TCS.SET_NMNL_TMP_TBL 0x00 0x18 0x9A 0x9E # NMNL TBL1 CH25 ON: 27.94 OFF: 30      |                        |
| 0730 | WAIT_SEC 2                                                                       |                        |
| 0731 | TCS.SET_NMNL_TMP_TBL 0x00 0x19 0x9C 0xA0 # NMNL TBL1 CH26 ON: 27.94 OFF: 30      |                        |
| 0732 | WAIT_SEC 2                                                                       |                        |
| 0733 | TCS.SET_NMNL_TMP_TBL 0x00 0x1A 0x9B 0x9F # NMNL TBL1 CH27 ON: 27.94 OFF: 30      |                        |
| 0734 | WAIT_SEC 2                                                                       |                        |
| 0735 | TCS.SET_NMNL_TMP_TBL 0x00 0x1B 0x99 0x9D # NMNL TBL1 CH28 ON: 27.95 OFF: 30      |                        |
| 0736 | WAIT_SEC 2                                                                       |                        |
| 0737 | TCS.SET_NMNL_TMP_TBL 0x00 0x1C 0x9B 0x9F # NMNL TBL1 CH29 ON: 27.94 OFF: 30      |                        |
| 0738 | WAIT_SEC 2                                                                       |                        |
| 0739 | TCS.SET_NMNL_TMP_TBL 0x00 0x1D 0x00 0x00 # NMNL TBL1 CH30 ON: -50.41 OFF: -50.41 |                        |
| 0740 | WAIT_SEC 2                                                                       |                        |
| 0741 | TCS.SET_NMNL_TMP_TBL 0x00 0x1E 0x00 0x00 # NMNL TBL1 CH31 ON: -50.41 OFF: -50.41 |                        |
| 0742 | WAIT_SEC 2                                                                       |                        |
| 0743 | TCS.SET_NMNL_TMP_TBL 0x00 0x1F 0x00 0x00 # NMNL TBL1 CH32 ON: -50.41 OFF: -50.41 |                        |
| 0744 | #                                                                                |                        |
| 0745 | . # <ヒータ ENA/DIS テーブル設定>                                                         |                        |
| 0746 | . TCS.SET_HTR_ENA 0x05 0x00                                                      | # ヒータ CH06: BAT1-1 DIS |
| 0747 | WAIT_SEC 2                                                                       |                        |
| 0748 | . TCS.SET_HTR_ENA 0x0C 0x00                                                      | # ヒータ CH13: BAT2-1 DIS |
| 0749 | WAIT_SEC 2                                                                       |                        |
| 0750 | . TCS.SET_HTR_ENA 0x15 0x00                                                      | # ヒータ CH22: BAT1-2 DIS |
| 0751 | WAIT_SEC 2                                                                       |                        |
| 0752 | . TCS.SET_HTR_ENA 0x1C 0x00                                                      | # ヒータ CH29: BAT2-2 DIS |
| 0753 | #                                                                                |                        |
| 0754 | . TCS.ONESHOT_TLM_0x11                                                           |                        |
| 0755 | . TCS.ONESHOT_TLM_0x19                                                           |                        |
| 0756 | . TCS.ONESHOT_TLM_0x1F                                                           |                        |
| 0757 | . TCS.ONESHOT_TLM_0x25                                                           |                        |
| 0758 | . #                                                                              |                        |
| 0759 | . # < CHECK >                                                                    |                        |
| 0760 | CHECK TCS.BK_DTY_TBL1_01CH                                                       | 32                     |
| 0761 | CHECK TCS.BK_DTY_TBL1_02CH                                                       | 32                     |
| 0762 | CHECK TCS.BK_DTY_TBL1_03CH                                                       | 32                     |
| 0763 | CHECK TCS.BK_DTY_TBL1_04CH                                                       | 32                     |
| 0764 | CHECK TCS.BK_DTY_TBL1_05CH                                                       | 32                     |
| 0765 | CHECK TCS.BK_DTY_TBL1_06CH                                                       | 32                     |
| 0766 | CHECK TCS.BK_DTY_TBL1_07CH                                                       | 32                     |
| 0767 | CHECK TCS.BK_DTY_TBL1_08CH                                                       | 32                     |
| 0768 | CHECK TCS.BK_DTY_TBL1_09CH                                                       | 32                     |
| 0769 | CHECK TCS.BK_DTY_TBL1_10CH                                                       | 32                     |
| 0770 | CHECK TCS.BK_DTY_TBL1_11CH                                                       | 32                     |
| 0771 | CHECK TCS.BK_DTY_TBL1_12CH                                                       | 32                     |
| 0772 | CHECK TCS.BK_DTY_TBL1_13CH                                                       | 32                     |
| 0773 | CHECK TCS.BK_DTY_TBL1_14CH                                                       | 0                      |
| 0774 | CHECK TCS.BK_DTY_TBL1_15CH                                                       | 0                      |
| 0775 | CHECK TCS.BK_DTY_TBL1_16CH                                                       | 0                      |
| 0776 | CHECK TCS.BK_DTY_TBL1_17CH                                                       | 32                     |
| 0777 | CHECK TCS.BK_DTY_TBL1_18CH                                                       | 32                     |

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| 0778 | CHECK TCS. BK_DTY_TBL1_19CH | 32   |      |
| 0779 | CHECK TCS. BK_DTY_TBL1_20CH | 32   |      |
| 0780 | CHECK TCS. BK_DTY_TBL1_21CH | 32   |      |
| 0781 | CHECK TCS. BK_DTY_TBL1_22CH | 32   |      |
| 0782 | CHECK TCS. BK_DTY_TBL1_23CH | 32   |      |
| 0783 | CHECK TCS. BK_DTY_TBL1_24CH | 32   |      |
| 0784 | CHECK TCS. BK_DTY_TBL1_25CH | 32   |      |
| 0785 | CHECK TCS. BK_DTY_TBL1_26CH | 32   |      |
| 0786 | CHECK TCS. BK_DTY_TBL1_27CH | 32   |      |
| 0787 | CHECK TCS. BK_DTY_TBL1_28CH | 32   |      |
| 0788 | CHECK TCS. BK_DTY_TBL1_29CH | 32   |      |
| 0789 | CHECK TCS. BK_DTY_TBL1_30CH | 0    |      |
| 0790 | CHECK TCS. BK_DTY_TBL1_31CH | 0    |      |
| 0791 | CHECK TCS. BK_DTY_TBL1_32CH | 0    |      |
| 0792 | CHECK TCS. NM_DTY_TBL1_01CH | 64   |      |
| 0793 | CHECK TCS. NM_DTY_TBL1_02CH | 64   |      |
| 0794 | CHECK TCS. NM_DTY_TBL1_03CH | 64   |      |
| 0795 | CHECK TCS. NM_DTY_TBL1_04CH | 64   |      |
| 0796 | CHECK TCS. NM_DTY_TBL1_05CH | 64   |      |
| 0797 | CHECK TCS. NM_DTY_TBL1_06CH | 64   |      |
| 0798 | CHECK TCS. NM_DTY_TBL1_07CH | 64   |      |
| 0799 | CHECK TCS. NM_DTY_TBL1_08CH | 64   |      |
| 0800 | CHECK TCS. NM_DTY_TBL1_09CH | 64   |      |
| 0801 | CHECK TCS. NM_DTY_TBL1_10CH | 64   |      |
| 0802 | CHECK TCS. NM_DTY_TBL1_11CH | 64   |      |
| 0803 | CHECK TCS. NM_DTY_TBL1_12CH | 64   |      |
| 0804 | CHECK TCS. NM_DTY_TBL1_13CH | 64   |      |
| 0805 | CHECK TCS. NM_DTY_TBL1_14CH | 0    |      |
| 0806 | CHECK TCS. NM_DTY_TBL1_15CH | 0    |      |
| 0807 | CHECK TCS. NM_DTY_TBL1_16CH | 0    |      |
| 0808 | CHECK TCS. NM_DTY_TBL1_17CH | 64   |      |
| 0809 | CHECK TCS. NM_DTY_TBL1_18CH | 64   |      |
| 0810 | CHECK TCS. NM_DTY_TBL1_19CH | 64   |      |
| 0811 | CHECK TCS. NM_DTY_TBL1_20CH | 64   |      |
| 0812 | CHECK TCS. NM_DTY_TBL1_21CH | 64   |      |
| 0813 | CHECK TCS. NM_DTY_TBL1_22CH | 64   |      |
| 0814 | CHECK TCS. NM_DTY_TBL1_23CH | 64   |      |
| 0815 | CHECK TCS. NM_DTY_TBL1_24CH | 64   |      |
| 0816 | CHECK TCS. NM_DTY_TBL1_25CH | 64   |      |
| 0817 | CHECK TCS. NM_DTY_TBL1_26CH | 64   |      |
| 0818 | CHECK TCS. NM_DTY_TBL1_27CH | 64   |      |
| 0819 | CHECK TCS. NM_DTY_TBL1_28CH | 64   |      |
| 0820 | CHECK TCS. NM_DTY_TBL1_29CH | 64   |      |
| 0821 | CHECK TCS. NM_DTY_TBL1_30CH | 0    |      |
| 0822 | CHECK TCS. NM_DTY_TBL1_31CH | 0    |      |
| 0823 | CHECK TCS. NM_DTY_TBL1_32CH | 0    |      |
| 0824 | CHECK TCS. NM_OFF_TBL1_01CH | 29.5 | 30.5 |
| 0825 | CHECK TCS. NM_OFF_TBL1_02CH | 29.5 | 30.5 |
| 0826 | CHECK TCS. NM_OFF_TBL1_03CH | 29.5 | 30.5 |
| 0827 | CHECK TCS. NM_OFF_TBL1_04CH | 29.5 | 30.5 |
| 0828 | CHECK TCS. NM_OFF_TBL1_05CH | 29.5 | 30.5 |
| 0829 | CHECK TCS. NM_OFF_TBL1_06CH | 29.5 | 30.5 |

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| 0830 | CHECK TCS. NM_OFF_TBL1_07CH | 29. 5 30. 5   |
| 0831 | CHECK TCS. NM_OFF_TBL1_08CH | 28. 99 29. 99 |
| 0832 | CHECK TCS. NM_OFF_TBL1_09CH | 29. 5 30. 5   |
| 0833 | CHECK TCS. NM_OFF_TBL1_10CH | 29. 5 30. 5   |
| 0834 | CHECK TCS. NM_OFF_TBL1_11CH | 29. 3 30. 3   |
| 0835 | CHECK TCS. NM_OFF_TBL1_12CH | 29. 5 30. 5   |
| 0836 | CHECK TCS. NM_OFF_TBL1_13CH | 29. 5 30. 5   |
| 0837 | CHECK TCS. NM_OFF_TBL1_17CH | 29. 5 30. 5   |
| 0838 | CHECK TCS. NM_OFF_TBL1_18CH | 29. 5 30. 5   |
| 0839 | CHECK TCS. NM_OFF_TBL1_19CH | 29. 5 30. 5   |
| 0840 | CHECK TCS. NM_OFF_TBL1_20CH | 29. 5 30. 5   |
| 0841 | CHECK TCS. NM_OFF_TBL1_21CH | 29. 5 30. 5   |
| 0842 | CHECK TCS. NM_OFF_TBL1_22CH | 29. 5 30. 5   |
| 0843 | CHECK TCS. NM_OFF_TBL1_23CH | 29. 5 30. 5   |
| 0844 | CHECK TCS. NM_OFF_TBL1_24CH | 29. 5 30. 5   |
| 0845 | CHECK TCS. NM_OFF_TBL1_25CH | 29. 5 30. 5   |
| 0846 | CHECK TCS. NM_OFF_TBL1_26CH | 29. 5 30. 5   |
| 0847 | CHECK TCS. NM_OFF_TBL1_27CH | 29. 5 30. 5   |
| 0848 | CHECK TCS. NM_OFF_TBL1_28CH | 29. 5 30. 5   |
| 0849 | CHECK TCS. NM_OFF_TBL1_29CH | 29. 5 30. 5   |
| 0850 | CHECK TCS. NM_ON_TBL1_01CH  | 27. 44 28. 44 |
| 0851 | CHECK TCS. NM_ON_TBL1_02CH  | 27. 44 28. 44 |
| 0852 | CHECK TCS. NM_ON_TBL1_03CH  | 27. 44 28. 44 |
| 0853 | CHECK TCS. NM_ON_TBL1_04CH  | 27. 44 28. 44 |
| 0854 | CHECK TCS. NM_ON_TBL1_05CH  | 27. 44 28. 44 |
| 0855 | CHECK TCS. NM_ON_TBL1_06CH  | 27. 44 28. 44 |
| 0856 | CHECK TCS. NM_ON_TBL1_07CH  | 27. 44 28. 44 |
| 0857 | CHECK TCS. NM_ON_TBL1_08CH  | 27. 96 28. 96 |
| 0858 | CHECK TCS. NM_ON_TBL1_09CH  | 27. 44 28. 44 |
| 0859 | CHECK TCS. NM_ON_TBL1_10CH  | 27. 44 28. 44 |
| 0860 | CHECK TCS. NM_ON_TBL1_11CH  | 27. 25 28. 25 |
| 0861 | CHECK TCS. NM_ON_TBL1_12CH  | 27. 44 28. 44 |
| 0862 | CHECK TCS. NM_ON_TBL1_13CH  | 27. 44 28. 44 |
| 0863 | CHECK TCS. NM_ON_TBL1_17CH  | 27. 44 28. 44 |
| 0864 | CHECK TCS. NM_ON_TBL1_18CH  | 27. 44 28. 44 |
| 0865 | CHECK TCS. NM_ON_TBL1_19CH  | 27. 44 28. 44 |
| 0866 | CHECK TCS. NM_ON_TBL1_20CH  | 27. 44 28. 44 |
| 0867 | CHECK TCS. NM_ON_TBL1_21CH  | 27. 44 28. 44 |
| 0868 | CHECK TCS. NM_ON_TBL1_22CH  | 27. 44 28. 44 |
| 0869 | CHECK TCS. NM_ON_TBL1_23CH  | 27. 44 28. 44 |
| 0870 | CHECK TCS. NM_ON_TBL1_24CH  | 27. 44 28. 44 |
| 0871 | CHECK TCS. NM_ON_TBL1_25CH  | 27. 44 28. 44 |
| 0872 | CHECK TCS. NM_ON_TBL1_26CH  | 27. 44 28. 44 |
| 0873 | CHECK TCS. NM_ON_TBL1_27CH  | 27. 44 28. 44 |
| 0874 | CHECK TCS. NM_ON_TBL1_28CH  | 27. 45 28. 45 |
| 0875 | CHECK TCS. NM_ON_TBL1_29CH  | 27. 44 28. 44 |
| 0876 | CHECK TCS. HTR_ENADIS_06CH  | =DIS          |
| 0877 | CHECK TCS. HTR_ENADIS_13CH  | =DIS          |
| 0878 | CHECK TCS. HTR_ENADIS_22CH  | =DIS          |
| 0879 | CHECK TCS. HTR_ENADIS_29CH  | =DIS          |
| 0880 | . #                         |               |
| 0881 | . TCS. SET_PRM_ENA          |               |

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0882 . #
0883 . # < CHECK >
0884 . CHECK TCS. SET_PRM_ST =ENA
0885 . #
0886 . TCS. SET_PEAK_PWR 0x2328
0887 . TCS. CTRL_PKPWR_EXE
0888 . #
0889 . # < CHECK >
0890 . CHECK TCS. SET_POWER 90
0891 . #
0892 . TCS. CTRL_HTR_P1_ENA
0893 . WAIT_SEC 2
0894 . TCS. CTRL_HTR_P2_ENA
0895 . WAIT_SEC 2
0896 . TCS. CTRL_HTR_P3_ENA
0897 . WAIT_SEC 2
0898 . TCS. CTRL_HTR_P4_ENA
0899 . #
0900 . # < CHECK >
0901 . CHECK TCS. HTR01_P_VOL_ST =ON
0902 . CHECK TCS. HTR09_P_VOL_ST =ON
0903 . CHECK TCS. HTR17_P_VOL_ST =ON
0904 . CHECK TCS. HTR25_P_VOL_ST =ON
0905 . #
0906 . # < TABLE 選択 >
0907 . TCS. SET_NMNL_SEL_TBL_1
0908 . WAIT_SEC 2
0909 . TCS. SET_ATSF_SEL_TBL_1
0910 . WAIT_SEC 2
0911 . TCS. SET_BKUP_SEL_TBL_1
0912 . #
0913 . # < CHECK >
0914 . CHECK TCS. SET_NMNL_TBL =TBL1
0915 . CHECK TCS. SET_SAFE_TBL1 =ENA
0916 . CHECK TCS. SET_SAFE_TBL2 =DIS
0917 . CHECK TCS. SET_SAFE_TBL3 =DIS
0918 . CHECK TCS. SET_SAFE_TBL4 =DIS
0919 . CHECK TCS. SET_BKUP_TBL1 =ENA
0920 . CHECK TCS. SET_BKUP_TBL2 =DIS
0921 . CHECK TCS. SET_BKUP_TBL3 =DIS
0922 . CHECK TCS. SET_BKUP_TBL4 =DIS
0923 . #
0924 . # < ピーク電力制御 ON >
0925 . TCS. CTRL_PKPWR_ON
0926 . #
0927 . # < CHECK >
0928 . CHECK TCS. PPWR_CTR =ON
0929 . #
0930 . # < ノミナル制御モード移行 >
0931 . TCS. MODE_NMNL
0932 . #
0933 . # < CHECK >

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0934 CHECK TCS.MODE_ST =NMNL
0935 #
0936 . # QL Check-Item Spec Check
0937 . # -----+-----+-----+-----
0938 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0939 . #
0940 . # =====
0941 . # 1.3. SI 系初期 C0 時における AT/RQ/ST ENA/DIS 設定 (MDP 立ち上げ前)
0942 . # =====
0943 . #
0944 . # << 概要 >>
0945 . #
0946 . # ATMC 0x01 は、BAT INT 後に ENA、BAT EXT 前に DIS とする。
0947 . # ATMC 0x30-0x34, 0x36 は MDP 立ち上げ後に ENA、MDP 立ち上げ前に DIS とする。
0948 . # ATMC 0x2B は、衛星立ち上げ後に ENA、立下げ前に DIS とする。
0949 . # ATMC 0x09 は、ヒータ設定/制御開始後に ENA、ヒータ立下げ前に DIS とする。
0950 . #
0951 . # RQMC 0x08-0x0B, 0x0D, 0x0E は、各 SI 機器 OFF のリクエストであり、ENA とする
0952 . #
0953 . # << ATMC_ENA/DIS >>
0954 . #
0955 . DH. ATMC_EACH_DIS 0x00 # // 分離後 DMC CPU リセット検出
0956 WAIT_SEC 2
0957 DH. ATMC_EACH_ENA 0x01 # // BAT UVC in Cruise mode
0958 WAIT_SEC 2
0959 DH. ATMC_EACH_DIS 0x02 # //Separation Detection1
0960 WAIT_SEC 2
0961 DH. ATMC_EACH_DIS 0x03 # //Separation Detection2
0962 WAIT_SEC 2
0963 DH. ATMC_EACH_DIS 0x04 # // Backup Separation Detection1 (SA-V)
0964 WAIT_SEC 2
0965 DH. ATMC_EACH_DIS 0x05 # // Backup Separation Detection2 (SSAS_STATUS)
0966 WAIT_SEC 2
0967 DH. ATMC_EACH_DIS 0x06 # //Status check before separation #1
0968 WAIT_SEC 2
0969 DH. ATMC_EACH_DIS 0x07 # //Status check before separation #2
0970 WAIT_SEC 2
0971 DH. ATMC_EACH_DIS 0x08 # N/A
0972 WAIT_SEC 2
0973 DH. ATMC_EACH_ENA 0x09 # //分離前 CPU リセット時 TCS モード再度 NMNL 制御
0974 WAIT_SEC 2
0975 DH. ATMC_EACH_DIS 0x0A # N/A
0976 WAIT_SEC 2
0977 DH. ATMC_EACH_DIS 0x0B # N/A
0978 WAIT_SEC 2
0979 DH. ATMC_EACH_DIS 0x0C # //APR 故障検出
0980 WAIT_SEC 2
0981 DH. ATMC_EACH_DIS 0x0D # // DRU-A OFF 検出 #1
0982 WAIT_SEC 2
0983 DH. ATMC_EACH_DIS 0x0E # // DRU OFF 検出 #2
0984 WAIT_SEC 2
0985 DH. ATMC_EACH_DIS 0x0F # // MGA deploy monitor

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0986 WAIT_SEC 2
0987 DH. ATMC_EACH_DIS 0x10 # //SSAS サンプルスチェック (ACS 自律化 A)
0988 WAIT_SEC 2
0989 DH. ATMC_EACH_DIS 0x11 # //SSAS 太陽角チェック (ACS 自律化 B)
0990 WAIT_SEC 2
0991 DH. ATMC_EACH_DIS 0x12 # // CH1 FIFO エラーリセット
0992 WAIT_SEC 2
0993 DH. ATMC_EACH_DIS 0x13 # N/A
0994 WAIT_SEC 2
0995 DH. ATMC_EACH_DIS 0x14 # // ON/OFF Keying (APM Temperature)
0996 WAIT_SEC 2
0997 DH. ATMC_EACH_DIS 0x15 # // ON/OFF Keying (TWTA-EPC-A Temperature)
0998 WAIT_SEC 2
0999 DH. ATMC_EACH_DIS 0x16 # // ON/OFF Keying (TWTA-EPC-B Temperature)
1000 WAIT_SEC 2
1001 DH. ATMC_EACH_DIS 0x17 # // ON/OFF Keying (LO deck (DMC) Temperature)
1002 WAIT_SEC 2
1003 DH. ATMC_EACH_DIS 0x18 # // ON/OFF Keying (LO deck Temperature)
1004 WAIT_SEC 2
1005 DH. ATMC_EACH_DIS 0x19 # // ON/OFF Keying (SAP Temperature)
1006 WAIT_SEC 2
1007 DH. ATMC_EACH_DIS 0x1A # // ON/OFF Keying (BAT1 Temperature)
1008 WAIT_SEC 2
1009 DH. ATMC_EACH_DIS 0x1B # // ON/OFF Keying (RCS Tank Pressure)
1010 WAIT_SEC 2
1011 DH. ATMC_EACH_DIS 0x1C # N/A
1012 WAIT_SEC 2
1013 DH. ATMC_EACH_DIS 0x1D # //APR2 復帰#2
1014 WAIT_SEC 2
1015 DH. ATMC_EACH_DIS 0x1E # //ON/OFF Keying (ADMCE-A 電流)
1016 WAIT_SEC 2
1017 DH. ATMC_EACH_DIS 0x1F # //ON/OFF Keying (ADMCE-B 電流)
1018 WAIT_SEC 2
1019 DH. ATMC_EACH_DIS 0x20 # //ON/OFF Keying (BUS 電圧)
1020 WAIT_SEC 2
1021 DH. ATMC_EACH_DIS 0x21 # //ON/OFF Keying (BUS 電流)
1022 WAIT_SEC 2
1023 DH. ATMC_EACH_DIS 0x22 # //ON/OFF Keying (BAT 放電)
1024 WAIT_SEC 2
1025 DH. ATMC_EACH_DIS 0x23 # //ON/OFF Keying (SA 電圧)
1026 WAIT_SEC 2
1027 DH. ATMC_EACH_DIS 0x24 # //ON/OFF Keying (DMC-PSU-A 電流)
1028 WAIT_SEC 2
1029 DH. ATMC_EACH_DIS 0x25 # //ON/OFF Keying (DMC-PSU-B 電流)
1030 WAIT_SEC 2
1031 DH. ATMC_EACH_DIS 0x26 # //ON/OFF Keying (TWTA-EPC-A 電流)
1032 WAIT_SEC 2
1033 DH. ATMC_EACH_DIS 0x27 # //ON/OFF Keying (TWTA-EPC-B 電流)
1034 WAIT_SEC 2
1035 DH. ATMC_EACH_DIS 0x28 # //ON/OFF Keying (DRU-A 電流)
1036 WAIT_SEC 2
1037 DH. ATMC_EACH_DIS 0x29 # //ON/OFF Keying (DRU-B 電流)

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1038 WAIT_SEC 2
1039 DH. ATMC_EACH_DIS 0x2A # // BAT UVC
1040 WAIT_SEC 2
1041 DH. ATMC_EACH_ENA 0x2B # //PCD NIC リセットモニタ
1042 WAIT_SEC 2
1043 DH. ATMC_EACH_DIS 0x2C # //BAT 温度モニタ 1
1044 WAIT_SEC 2
1045 DH. ATMC_EACH_DIS 0x2D # //BAT 温度モニタ 2
1046 WAIT_SEC 2
1047 DH. ATMC_EACH_DIS 0x2E # // PCD User Request Error Clear
1048 WAIT_SEC 2
1049 DH. ATMC_EACH_DIS 0x2F # // BAT CV Timer Reset
1050 WAIT_SEC 2
1051 DH. ATMC_EACH_DIS 0x30 # //MDP-DPU1 プログラム起動異常検出
1052 WAIT_SEC 2
1053 DH. ATMC_EACH_DIS 0x31 # //MDP-DPU2 プログラム起動異常検出
1054 WAIT_SEC 2
1055 DH. ATMC_EACH_DIS 0x32 # //MDP-DPU1 重大エラー発生検出
1056 WAIT_SEC 2
1057 DH. ATMC_EACH_DIS 0x33 # //MDP-DPU2 重大エラー発生検出
1058 WAIT_SEC 2
1059 DH. ATMC_EACH_DIS 0x34 # //MDP-DPU1 DMC 通信切れ発生検出 1
1060 WAIT_SEC 2
1061 DH. ATMC_EACH_DIS 0x35 # //MDP-DPU1 DMC 通信切れ発生検出 2
1062 WAIT_SEC 2
1063 DH. ATMC_EACH_DIS 0x36 # //MDP-DPU2 DMC 通信切れ発生検出 1
1064 WAIT_SEC 2
1065 DH. ATMC_EACH_DIS 0x37 # //MDP-DPU2 DMC 通信切れ発生検出 2
1066 WAIT_SEC 2
1067 DH. ATMC_EACH_DIS 0x38 # //MDP-DPU1 冗長時 重大エラー発生検出
1068 WAIT_SEC 2
1069 DH. ATMC_EACH_DIS 0x39 # //MDP-DPU2 冗長時 重大エラー発生検出
1070 WAIT_SEC 2
1071 DH. ATMC_EACH_DIS 0x3A # //MDP-DPU1 冗長時 DMC 通信切れ発生検出 1
1072 WAIT_SEC 2
1073 DH. ATMC_EACH_DIS 0x3B # //MDP-DPU1 冗長時 DMC 通信切れ発生検出 2
1074 WAIT_SEC 2
1075 DH. ATMC_EACH_DIS 0x3C # //MDP-DPU2 冗長時 DMC 通信切れ発生検出 1
1076 WAIT_SEC 2
1077 DH. ATMC_EACH_DIS 0x3D # //MDP-DPU2 冗長時 DMC 通信切れ発生検出 2
1078 WAIT_SEC 2
1079 DH. ATMC_EACH_DIS 0x3E # // MDP1 User Request Error Clear
1080 WAIT_SEC 2
1081 DH. ATMC_EACH_DIS 0x3F # // MDP2 User Request Error Clear
1082 #
1083 # < CHECK >
1084 CHECK DH. ATMC_ENA_DIS00 =DIS
1085 CHECK DH. ATMC_ENA_DIS01 =ENA
1086 CHECK DH. ATMC_ENA_DIS02 =DIS
1087 CHECK DH. ATMC_ENA_DIS03 =DIS
1088 CHECK DH. ATMC_ENA_DIS04 =DIS
1089 CHECK DH. ATMC_ENA_DIS05 =DIS

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| 1090 | CHECK DH. ATMC_ENA_DIS06 =DIS |
| 1091 | CHECK DH. ATMC_ENA_DIS07 =DIS |
| 1092 | CHECK DH. ATMC_ENA_DIS08 =DIS |
| 1093 | CHECK DH. ATMC_ENA_DIS09 =ENA |
| 1094 | CHECK DH. ATMC_ENA_DIS0A =DIS |
| 1095 | CHECK DH. ATMC_ENA_DIS0B =DIS |
| 1096 | CHECK DH. ATMC_ENA_DIS0C =DIS |
| 1097 | CHECK DH. ATMC_ENA_DIS0D =DIS |
| 1098 | CHECK DH. ATMC_ENA_DIS0E =DIS |
| 1099 | CHECK DH. ATMC_ENA_DIS0F =DIS |
| 1100 | CHECK DH. ATMC_ENA_DIS10 =DIS |
| 1101 | CHECK DH. ATMC_ENA_DIS11 =DIS |
| 1102 | CHECK DH. ATMC_ENA_DIS12 =DIS |
| 1103 | CHECK DH. ATMC_ENA_DIS13 =DIS |
| 1104 | CHECK DH. ATMC_ENA_DIS14 =DIS |
| 1105 | CHECK DH. ATMC_ENA_DIS15 =DIS |
| 1106 | CHECK DH. ATMC_ENA_DIS16 =DIS |
| 1107 | CHECK DH. ATMC_ENA_DIS17 =DIS |
| 1108 | CHECK DH. ATMC_ENA_DIS18 =DIS |
| 1109 | CHECK DH. ATMC_ENA_DIS19 =DIS |
| 1110 | CHECK DH. ATMC_ENA_DIS1A =DIS |
| 1111 | CHECK DH. ATMC_ENA_DIS1B =DIS |
| 1112 | CHECK DH. ATMC_ENA_DIS1C =DIS |
| 1113 | CHECK DH. ATMC_ENA_DIS1D =DIS |
| 1114 | CHECK DH. ATMC_ENA_DIS1E =DIS |
| 1115 | CHECK DH. ATMC_ENA_DIS1F =DIS |
| 1116 | CHECK DH. ATMC_ENA_DIS20 =DIS |
| 1117 | CHECK DH. ATMC_ENA_DIS21 =DIS |
| 1118 | CHECK DH. ATMC_ENA_DIS22 =DIS |
| 1119 | CHECK DH. ATMC_ENA_DIS23 =DIS |
| 1120 | CHECK DH. ATMC_ENA_DIS24 =DIS |
| 1121 | CHECK DH. ATMC_ENA_DIS25 =DIS |
| 1122 | CHECK DH. ATMC_ENA_DIS26 =DIS |
| 1123 | CHECK DH. ATMC_ENA_DIS27 =DIS |
| 1124 | CHECK DH. ATMC_ENA_DIS28 =DIS |
| 1125 | CHECK DH. ATMC_ENA_DIS29 =DIS |
| 1126 | CHECK DH. ATMC_ENA_DIS2A =DIS |
| 1127 | CHECK DH. ATMC_ENA_DIS2B =ENA |
| 1128 | CHECK DH. ATMC_ENA_DIS2C =DIS |
| 1129 | CHECK DH. ATMC_ENA_DIS2D =DIS |
| 1130 | CHECK DH. ATMC_ENA_DIS2E =DIS |
| 1131 | CHECK DH. ATMC_ENA_DIS2F =DIS |
| 1132 | CHECK DH. ATMC_ENA_DIS30 =DIS |
| 1133 | CHECK DH. ATMC_ENA_DIS31 =DIS |
| 1134 | CHECK DH. ATMC_ENA_DIS32 =DIS |
| 1135 | CHECK DH. ATMC_ENA_DIS33 =DIS |
| 1136 | CHECK DH. ATMC_ENA_DIS34 =DIS |
| 1137 | CHECK DH. ATMC_ENA_DIS35 =DIS |
| 1138 | CHECK DH. ATMC_ENA_DIS36 =DIS |
| 1139 | CHECK DH. ATMC_ENA_DIS37 =DIS |
| 1140 | CHECK DH. ATMC_ENA_DIS38 =DIS |
| 1141 | CHECK DH. ATMC_ENA_DIS39 =DIS |

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1142 CHECK DH. ATMC_ENA_DIS3A =DIS
1143 CHECK DH. ATMC_ENA_DIS3B =DIS
1144 CHECK DH. ATMC_ENA_DIS3C =DIS
1145 CHECK DH. ATMC_ENA_DIS3D =DIS
1146 CHECK DH. ATMC_ENA_DIS3E =DIS
1147 CHECK DH. ATMC_ENA_DIS3F =DIS
1148 #
1149 #
1150 . # << RQMC_ENA/DIS >>
1151 . #
1152 . DH. RQMC_EACH_DIS 0x00 # N/A
1153 WAIT_SEC 2
1154 DH. RQMC_EACH_DIS 0x01 # // BAT CEL UVC
1155 WAIT_SEC 2
1156 DH. RQMC_EACH_DIS 0x02 # N/A
1157 WAIT_SEC 2
1158 DH. RQMC_EACH_DIS 0x03 # N/A
1159 WAIT_SEC 2
1160 DH. RQMC_EACH_DIS 0x04 # N/A
1161 WAIT_SEC 2
1162 DH. RQMC_EACH_DIS 0x05 # N/A
1163 WAIT_SEC 2
1164 DH. RQMC_EACH_DIS 0x06 # N/A
1165 WAIT_SEC 2
1166 DH. RQMC_EACH_DIS 0x07 # N/A
1167 WAIT_SEC 2
1168 DH. RQMC_EACH_DIS 0x08 # // MEA1-PSU-OFF
1169 WAIT_SEC 2
1170 DH. RQMC_EACH_DIS 0x09 # // MEA2-PSU-OFF
1171 WAIT_SEC 2
1172 DH. RQMC_EACH_DIS 0x0A # // MIA-PSU-OFF
1173 WAIT_SEC 2
1174 DH. RQMC_EACH_DIS 0x0B # // MSA-PSU-OFF
1175 WAIT_SEC 2
1176 DH. RQMC_EACH_DIS 0x0C # N/A
1177 WAIT_SEC 2
1178 DH. RQMC_EACH_DIS 0x0D # // HEP-I-PSU-OFF
1179 WAIT_SEC 2
1180 DH. RQMC_EACH_DIS 0x0E # // ENA-PSU-OFF
1181 WAIT_SEC 2
1182 DH. RQMC_EACH_DIS 0x0F # N/A
1183 WAIT_SEC 2
1184 DH. RQMC_EACH_DIS 0x10 # // スピンレート制御正常終了 (Req A)
1185 WAIT_SEC 2
1186 DH. RQMC_EACH_DIS 0x11 # // 太陽角監視異常 (Req C)
1187 WAIT_SEC 2
1188 DH. RQMC_EACH_DIS 0x12 # // 太陽角制御異常 (Req D)
1189 WAIT_SEC 2
1190 DH. RQMC_EACH_DIS 0x13 # // Sun Pulse チェック異常時 (Req E)
1191 WAIT_SEC 2
1192 DH. RQMC_EACH_DIS 0x14 # // 分離時 スピニアップ異常時 (Req 初期 F/Req F)
1193 WAIT_SEC 2

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1194 DH.RQMC_EACH_DIS 0x15 # // ADM 回転異常 (Req G)
1195 WAIT_SEC 2
1196 DH.RQMC_EACH_DIS 0x16 # // RGS 圧力センサ異常 (Req H)
1197 WAIT_SEC 2
1198 DH.RQMC_EACH_DIS 0x17 # // 定常時 CPU リセット (Req I)
1199 WAIT_SEC 2
1200 DH.RQMC_EACH_DIS 0x18 # N/A
1201 WAIT_SEC 2
1202 DH.RQMC_EACH_DIS 0x19 # N/A
1203 WAIT_SEC 2
1204 DH.RQMC_EACH_DIS 0x1A # N/A
1205 WAIT_SEC 2
1206 DH.RQMC_EACH_DIS 0x1B # N/A
1207 WAIT_SEC 2
1208 DH.RQMC_EACH_DIS 0x1C # N/A
1209 WAIT_SEC 2
1210 DH.RQMC_EACH_DIS 0x1D # N/A
1211 WAIT_SEC 2
1212 DH.RQMC_EACH_DIS 0x1E # N/A
1213 WAIT_SEC 2
1214 DH.RQMC_EACH_DIS 0x1F # N/A
1215 #
1216 # < CHECK >
1217 CHECK DH.RQMC_ENA_DIS00 =DIS
1218 CHECK DH.RQMC_ENA_DIS01 =DIS
1219 CHECK DH.RQMC_ENA_DIS02 =DIS
1220 CHECK DH.RQMC_ENA_DIS03 =DIS
1221 CHECK DH.RQMC_ENA_DIS04 =DIS
1222 CHECK DH.RQMC_ENA_DIS05 =DIS
1223 CHECK DH.RQMC_ENA_DIS06 =DIS
1224 CHECK DH.RQMC_ENA_DIS07 =DIS
1225 CHECK DH.RQMC_ENA_DIS08 =DIS
1226 CHECK DH.RQMC_ENA_DIS09 =DIS
1227 CHECK DH.RQMC_ENA_DIS0A =DIS
1228 CHECK DH.RQMC_ENA_DIS0B =DIS
1229 CHECK DH.RQMC_ENA_DIS0C =DIS
1230 CHECK DH.RQMC_ENA_DIS0D =DIS
1231 CHECK DH.RQMC_ENA_DIS0E =DIS
1232 CHECK DH.RQMC_ENA_DIS0F =DIS
1233 CHECK DH.RQMC_ENA_DIS10 =DIS
1234 CHECK DH.RQMC_ENA_DIS11 =DIS
1235 CHECK DH.RQMC_ENA_DIS12 =DIS
1236 CHECK DH.RQMC_ENA_DIS13 =DIS
1237 CHECK DH.RQMC_ENA_DIS14 =DIS
1238 CHECK DH.RQMC_ENA_DIS15 =DIS
1239 CHECK DH.RQMC_ENA_DIS16 =DIS
1240 CHECK DH.RQMC_ENA_DIS17 =DIS
1241 CHECK DH.RQMC_ENA_DIS18 =DIS
1242 CHECK DH.RQMC_ENA_DIS19 =DIS
1243 CHECK DH.RQMC_ENA_DIS1A =DIS
1244 CHECK DH.RQMC_ENA_DIS1B =DIS
1245 CHECK DH.RQMC_ENA_DIS1C =DIS

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1246 CHECK DH.RQMC_ENA_DIS1D =DIS
1247 CHECK DH.RQMC_ENA_DIS1E =DIS
1248 CHECK DH.RQMC_ENA_DIS1F =DIS
1249 #
1250 #
1251 . # << SYSTME_ENA/DIS >>
1252 . #
1253 . DH.SYSTMR_EACH_DIS 0x00 # //XSW2 クロス時 Command 受信間隔 Timer
1254 WAIT_SEC 2
1255 DH.SYSTMR_EACH_DIS 0x01 # //XSW2 ストレート時 Command 受信間隔 Timer
1256 WAIT_SEC 2
1257 DH.SYSTMR_EACH_DIS 0x02 # //MDP-DPU1 系 Mission 機器 OFF
1258 WAIT_SEC 2
1259 DH.SYSTMR_EACH_DIS 0x03 # //MDP-DPU2 系 Mission 機器 OFF
1260 WAIT_SEC 2
1261 DH.SYSTMR_EACH_DIS 0x04 # //分離時 Backup Timer
1262 WAIT_SEC 2
1263 DH.SYSTMR_EACH_DIS 0x05 # //Mission 機器 ALL OFF
1264 WAIT_SEC 2
1265 DH.SYSTMR_EACH_DIS 0x06 # //分離時 Sun Acquisition Start
1266 WAIT_SEC 2
1267 DH.SYSTMR_EACH_DIS 0x07 # //分離時 可視時間終了後通信系 OFF
1268 #
1269 . # < CHECK >
1270 CHECK DH.SYSTMR_ENA_DIS0 =DIS
1271 CHECK DH.SYSTMR_ENA_DIS1 =DIS
1272 CHECK DH.SYSTMR_ENA_DIS2 =DIS
1273 CHECK DH.SYSTMR_ENA_DIS3 =DIS
1274 CHECK DH.SYSTMR_ENA_DIS4 =DIS
1275 CHECK DH.SYSTMR_ENA_DIS5 =DIS
1276 CHECK DH.SYSTMR_ENA_DIS6 =DIS
1277 CHECK DH.SYSTMR_ENA_DIS7 =DIS
1278 . #
1279 . # # AT 機能を ENA にする
1280 . DH.ATMC_ENA
1281 . #
1282 . # < CHECK >
1283 CHECK DH.ATMC_ENA_DIS =ENA
1284 . #
1285 . # # RQ waiting に設定する。
1286 . DH.RQMC_ENA
1287 . #
1288 . # < CHECK >
1289 CHECK DH.RQMC_ENA_DIS =ENA
1290 . #
1291 . # # システムタイマー機能を有効にする。
1292 . DH.SYSTMR_ENA
1293 . #
1294 . # < CHECK >
1295 CHECK DH.SYSTMR_ENA_DIS =ENA
1296 . #
1297 . # QL Check-Item Spec Check

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1298 . # -----+-----+-----+-----
1299 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1300 . #
1301 . # =====
1302 . # 1.4. SI 機器初期チェックアウト時設定
1303 . # =====
1304 . #
1305 . # 【SI 系初期 CO 試験時の探査機コンフィギュレーション】
1306 . # [COM/DH]
1307 . # XTRP: ON & TX OFF, TWTA: OFF
1308 . #
1309 . # [DMCS/DRU]
1310 . # DMC: ON Dual Mode, 1553B 通信
1311 . # DRU: ON, Heater: NMNL
1312 . #
1313 . # [ACS]
1314 . # RCS: DRV OFF, APM: DRV OFF, ADMCE: OFF
1315 . # SSAS: OFF, SSC: OFF
1316 . # 疑似パルス出力
1317 . #
1318 . # [PCS]
1319 . # APR: OFF, PCD: ON
1320 . # BAT: INT
1321 . # BAT 充放電: 不要
1322 . #
1323 . # [SI]
1324 . # MDP: ON
1325 . # その他 SI 機器: CO 対象機器について ON
1326 . # 初期 CO 試験用 機器 ON/OFF コンフィギュレーション確認
1327 . #
1328 . # < CHECK >
1329 . CHECK PCD.XTRP_A_ON_OFF =ON
1330 . CHECK PCD.XTRP_B_ON_OFF =ON
1331 . CHECK PCD.EPC_A_ON_OFF =OFF
1332 . CHECK PCD.EPC_B_ON_OFF =OFF
1333 . CHECK DH.XTRP_A_TX_ON_OFF =OFF
1334 . CHECK DH.XTRP_B_TX_ON_OFF =OFF
1335 . CHECK PCD.EPC_A_ON_OFF =OFF
1336 . CHECK PCD.EPC_B_ON_OFF =OFF
1337 . CHECK PCD.DRU_PSU_A_ON_OFF =ON
1338 . CHECK PCD.DRU_PSU_B_ON_OFF =OFF
1339 . CHECK TCS.MODE_ST =NMNL
1340 . CHECK ACS_SYS.RCS_A_B_ST =OFF
1341 . CHECK ACS_SYS.STS_DRUAPM_ON =OFF
1342 . CHECK PCD.ADMCE_A_ON_OFF =OFF
1343 . CHECK PCD.ADMCE_B_ON_OFF =OFF
1344 . CHECK ACS_SYS.STS_SSASA_PWR =OFF
1345 . CHECK ACS_SYS.STS_SSASB_PWR =OFF
1346 . CHECK ACS_SYS.SSC_PWR =OFF
1347 . CHECK PCD.APR1_ON_OFF =OFF
1348 . CHECK PCD.APR2_ON_OFF =ON
1349 . CHECK PCD.PSU_MDP1_ON_OFF =OFF

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1350 CHECK PCD. PSU_MDP2_ON_OFF =OFF
1351 CHECK PCD. PSU_MORTOR_A_ON_OFF =OFF
1352 CHECK PCD. PSU_MORTOR_B_ON_OFF =OFF
1353 CHECK PCD. PSU_ENA_ON_OFF =OFF
1354 CHECK PCD. PSU_MEA1_ON_OFF =OFF
1355 CHECK PCD. PSU_MEA2_ON_OFF =OFF
1356 CHECK PCD. PSU_MSA_ON_OFF =OFF
1357 CHECK PCD. PSU_MIA_ON_OFF =OFF
1358 CHECK PCD. PSU_HEP_E_ON_OFF =OFF
1359 CHECK PCD. PSU_HEP_I_ON_OFF =OFF
1360 CHECK PCD. PSU_PME_ON_OFF =OFF
1361 CHECK PCD. PSU_MGF_I_ON_OFF =OFF
1362 CHECK PCD. MDM_E_ON_OFF =OFF
1363 CHECK PCD. MSASI_ON_OFF =OFF
1364 CHECK PCD. SURVIVAL_HTR_ON_OFF =ON
1365 #
1366 . # QL Check-Item Spec Check
1367 # -----+-----+-----+-----
1368 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1369 #
1370 . # *****
1371 . # 2. MDP 立ち上げ
1372 . # *****
1373 . #
1374 . DH.MC_EACH_EXE 0x002C # 1553B TLM mode 0 (SYS HK 1)
1375 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
1376 #: 0x00 DH. DMP_PERIOD 0x04
1377 #: 0x04 ACS. ACS_PKT_FMT_1
1378 #: 0x05 DH. OPE_MODE 0x0C 0x0A 0x00 0x00 0x00000000000000000000000000000000
1379 #: 0x06 DH. TLMFMT1553B_CHG 0x00
1380 WAIT_SEC 40
1381 #
1382 # < CHECK >
1383 CHECK DH. DMP_PERIOD =SEC_16
1384 CHECK DH. CATEGORY_TBL_NO 0x00
1385 CHECK DH. DWNLNK_RATE =BPS16384
1386 CHECK DH. TLM1553BFMTNO 0x00
1387 #
1388 # MDP-DPU1,2(A 系) の LINK Enable を確認
1389 #
1390 # < CHECK >
1391 CHECK DH. SPW_POTMD_L1 =ENA
1392 CHECK DH. SPW_POTMD_L2 =ENA
1393 #
1394 . # MDP-DPU1,2 を電源 ON する。(HW_STATE1 は 16 秒以内にチェックすること。)
1395 #
1396 # <<< MDP-DPU1/2 Power ON >>>
1397 #
1398 . DH. MDP_DPU12_POWER_ON # DH. MC_EACH_EXE 0x099
1399 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
1400 #: 0x00 PCD. ON_OFF_ENA
1401 #: 0x01 PCD. PSU_MDP1_ON

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1402 #:          0x02 PCD.PSU_MDP2_ON
1403 #:          0x03 PCD.ON_OFF_DIS
1404 #
1405 #
1406 # < CHECK >
1407 CHECK MD1F.DPU_HW_STATE1          =DPU_RESET
1408 CHECK MD2F.DPU_HW_STATE1          =DPU_RESET
1409 CHECK PCD.ON_OFF_ENA_DIS          =DIS
1410 CHECK PCD.PSU_MDP1_ON_OFF          =ON
1411 CHECK PCD.PSU_MDP2_ON_OFF          =ON
1412 CHECK DH.SPW_LNKST_L1              =RUN
1413 CHECK DH.SPW_LNKST_L2              =RUN
1414 #
1415 # *****
1416 # WAIT 16 sec (HW Initialization of DPU1 & DPU2)
1417 # *****
1418 WAIT_SEC 40
1419 #
1420 # <<< MDP-DPU Initial HK check (2): 16 sec after PWR-ON >>>
1421 #
1422 # < CHECK >
1423 CHECK MD1F.DPU_HW_STATE1          =DPU_RUN
1424 CHECK MD1F.DPU_HW_STATE2          =CPUOFF_DPUBUS
1425 CHECK MD1F.DMC_IF_RMAPERR          =na
1426 CHECK MD1F.DMC_IF_2BITERR          =na
1427 CHECK MD1F.DMC_IF_BTOERR           =na
1428 CHECK MD1F.BTO_ERR                 =na
1429 CHECK MD1F.WDT_ERR                 =na
1430 CHECK MD1F.DPU_SELECT              =DPU1
1431 CHECK MD2F.DPU_HW_STATE1          =DPU_RUN
1432 CHECK MD2F.DPU_HW_STATE2          =CPUOFF_DPUBUS
1433 CHECK MD2F.DMC_IF_RMAPERR          =na
1434 CHECK MD2F.DMC_IF_2BITERR          =na
1435 CHECK MD2F.DMC_IF_BTOERR           =na
1436 CHECK MD2F.BTO_ERR                 =na
1437 CHECK MD2F.WDT_ERR                 =na
1438 CHECK MD2F.DPU_SELECT              =DPU2
1439 #
1440 # <<< MDP-DPU CPU_ON / RUN >>>
1441 DH.MDP_DPU12_CPU_RUN # DH.MC_EACH_EXE 0x009A
1442 #:          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
1443 #:          0x00 DH.ATMC_EACH_ENA 0x34
1444 #:          0x01 DH.SPW RTEFDIR_SET 0x1E
1445 #:          0x05 DH.ATMC_EACH_ENA 0x36
1446 #:          0x06 MD1F.CPU_ON
1447 #:          0x07 MD2F.CPU_ON
1448 #:          0x08 MD1F.CPU_RUN
1449 #:          0x09 MD2F.CPU_RUN
1450 #:          0x0A DH.ATMC_EACH_ENA 0x30
1451 #:          0x0F DH.ATMC_EACH_ENA 0x31
1452 WAIT_SEC 180
1453 # ここで 180 秒待機 (MDP MiddleWare による初期化終了待ち)

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1454 #
1455 # < CHECK >
1456 CHECK DH. SPW_FDIR_ENA_MDP1      =ENA
1457 CHECK DH. SPW_FDIR_ENA_MDP2      =ENA
1458 CHECK DH. SPW_FDIR_ENA_PCD       =ENA
1459 CHECK DH. SPW_FDIR_ENA_RMTRTR    =ENA
1460 CHECK MD1F.DPU_HW_STATE2          =CPU_RUN
1461 CHECK MD2F.DPU_HW_STATE2          =CPU_RUN
1462 #
1463 #
1464 # NECP で-updatewait は使用できないのでコメントアウト
1465 #
1466 # < MDP1 SATELITE TIME CHECK (0x200/s) >
1467 #
1468 # CHECK MD1F.SAT_TIME -updatewait 180
1469 # LET LocalA0 = {MD1F.SAT_TIME} & 0xFFFFFFFF
1470 # CHECK MD1F.SAT_TIME -updatewait 4
1471 # LET LocalA1 = {MD1F.SAT_TIME} & 0xFFFFFFFF
1472 # LET LocalA1 = LocalA1 - LocalA0
1473 # SYSTEM test {LocalA1} = 512
1474 #
1475 # < MDP1 SUNPULSE TIME CHECK (0x800-0xB00/4s) >
1476 #
1477 # CHECK MD1F.SUNPULSE_TIME -updatewait 8
1478 # LET LocalA0 = {MD1F.SUNPULSE_TIME} & 0xFFFFFFFF
1479 # CHECK MD1F.SUNPULSE_TIME -updatewait 8
1480 # LET LocalA1 = {MD1F.SUNPULSE_TIME} & 0xFFFFFFFF
1481 # LET LocalA1 = LocalA1 - LocalA0
1482 # SYSTEM test {LocalA1} -ge 2048 -a {LocalA1} -le 2816
1483 #
1484 # < MDP2 SATELITE TIME CHECK (0x200/s) >
1485 #
1486 # CHECK MD2F.SAT_TIME -updatewait 8
1487 # LET LocalA0 = {MD2F.SAT_TIME} & 0xFFFFFFFF
1488 # CHECK MD2F.SAT_TIME -updatewait 4
1489 # LET LocalA1 = {MD2F.SAT_TIME} & 0xFFFFFFFF
1490 # LET LocalA1 = LocalA1 - LocalA0
1491 # SYSTEM test {LocalA1} = 512
1492 #
1493 # < MDP2 SUNPULSE TIME CHECK (0x800-0xB00/4s) >
1494 #
1495 # CHECK MD2F.SUNPULSE_TIME -updatewait 8
1496 # LET LocalA0 = {MD2F.SUNPULSE_TIME} & 0xFFFFFFFF
1497 # CHECK MD2F.SUNPULSE_TIME -updatewait 8
1498 # LET LocalA1 = {MD2F.SUNPULSE_TIME} & 0xFFFFFFFF
1499 # LET LocalA1 = LocalA1 - LocalA0
1500 # SYSTEM test {LocalA1} -ge 2048 -a {LocalA1} -le 2816
1501 #
1502 #
1503 # <<< MDP-DPU Initial HK check (3): after the Initialization >>>
1504 # CPU_RUN 時、確率 7/8 で SLOT_ERR 起きるが問題ない(起動時の SLOT 番号齟齬)
1505 #

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|      |                            |          |
|------|----------------------------|----------|
| 1506 | # < CHECK >                |          |
| 1507 | CHECK MD1F.DPU_HW_STATE1   | =DPU_RUN |
| 1508 | CHECK MD1F.DPU_HW_STATE2   | =CPU_RUN |
| 1509 | CHECK MD1F.DMC_IF_RMAPERR  | =na      |
| 1510 | CHECK MD1F.DMC_IF_2BITERR  | =na      |
| 1511 | CHECK MD1F.DMC_IF_BTOERR   | =na      |
| 1512 | CHECK MD1F.BTO_ERR         | =na      |
| 1513 | CHECK MD1F.WDT_ERR         | =na      |
| 1514 | CHECK MD1F.DPU_SELECT      | =DPU1    |
| 1515 | CHECK MD1F.DPU_SW_STATE    | 2        |
| 1516 | CHECK MD1F.SW_CMD_CODE     | 0        |
| 1517 | CHECK MD1F.SW_CMD_CNT      | 0        |
| 1518 | CHECK MD1F.SW_CMD_REJ_CNT  | 0        |
| 1519 | CHECK MD1F.SW_CMD_REJ_CODE | 0        |
| 1520 | CHECK MD1F.MS_CMD_PRIO     | =DMC     |
| 1521 | CHECK MD1F.RDN_MODE        | =OFF     |
| 1522 | CHECK MD1F.SUNPULSE_MODE   | =NML     |
| 1523 | CHECK MD1F.LOAD_DUMP_DIS   | =DIS     |
| 1524 | CHECK MD1F.MEMLOAD_BUSY    | =OFF     |
| 1525 | CHECK MD1F.MEMLOAD_NODE    | 0        |
| 1526 | CHECK MD1F.MEMDUMP_BUSY    | =OFF     |
| 1527 | CHECK MD1F.MEMDUMP_NODE    | 0        |
| 1528 | CHECK MD1F.TASK_STAT_AP01  | =OFF     |
| 1529 | CHECK MD1F.TASK_STAT_AP02  | =OFF     |
| 1530 | CHECK MD1F.TASK_STAT_AP03  | =OFF     |
| 1531 | CHECK MD1F.TASK_STAT_AP04  | =OFF     |
| 1532 | CHECK MD1F.TASK_STAT_AP05  | =OFF     |
| 1533 | CHECK MD1F.TASK_STAT_AP06  | =OFF     |
| 1534 | CHECK MD1F.TASK_STAT_AP07  | =OFF     |
| 1535 | CHECK MD1F.TASK_STAT_AP08  | =OFF     |
| 1536 | CHECK MD1F.TASK_STAT_AP09  | =OFF     |
| 1537 | CHECK MD1F.TASK_STAT_AP10  | =OFF     |
| 1538 | CHECK MD1F.TASK_STAT_AP11  | =OFF     |
| 1539 | CHECK MD1F.TASK_STAT_AP12  | =OFF     |
| 1540 | CHECK MD1F.TASK_STAT_AP13  | =OFF     |
| 1541 | CHECK MD1F.TASK_STAT_AP14  | =OFF     |
| 1542 | CHECK MD1F.TASK_STAT_AP15  | =OFF     |
| 1543 | CHECK MD1F.TASK_STAT_AP16  | =OFF     |
| 1544 | CHECK MD1F.UNEXPECT_CNT    | 0        |
| 1545 | CHECK MD1F.UNEXPECT_CODE   | 0        |
| 1546 | CHECK MD1F.DMC_IF_ERR      | =na      |
| 1547 | CHECK MD1F.TIME_ERR        | =na      |
| 1548 | CHECK MD1F.INST_CACHE_ERR  | =na      |
| 1549 | CHECK MD1F.DATA_CACHE_ERR  | =na      |
| 1550 | CHECK MD1F.SRAM_1BERR      | =na      |
| 1551 | CHECK MD1F.CMD_BUFF_1BERR  | =na      |
| 1552 | CHECK MD1F.CMD_BUFF_2BERR  | =na      |
| 1553 | CHECK MD1F.TLM_BUFF_1BERR  | =na      |
| 1554 | CHECK MD1F.TLM_BUFF_2BERR  | =na      |
| 1555 | CHECK MD1F.COMP_1BERR      | =na      |
| 1556 | CHECK MD1F.DS_1BERR        | =na      |
| 1557 | CHECK MD1F.DS_2BERR        | =na      |

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|      |                            |          |
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| 1558 | CHECK MD1F.RDN_LINK_STAT   | =ON      |
| 1559 | CHECK MD1F.RIF_LINK_ERR    | =na      |
| 1560 | CHECK MD1F.RIF_ERR         | =na      |
| 1561 | CHECK MD1F.MS0_LINK_STAT   | =OFF     |
| 1562 | CHECK MD1F.MS1_LINK_STAT   | =OFF     |
| 1563 | CHECK MD1F.MS2_LINK_STAT   | =OFF     |
| 1564 | CHECK MD1F.MS3_LINK_STAT   | =OFF     |
| 1565 | CHECK MD1F.MS4_LINK_STAT   | =OFF     |
| 1566 | CHECK MD1F.MS5_LINK_STAT   | =OFF     |
| 1567 | CHECK MD1F.MS6_LINK_STAT   | =OFF     |
| 1568 | CHECK MD1F.MS7_LINK_STAT   | =OFF     |
| 1569 | CHECK MD1F.MS0_DAT_CLCT    | =OFF     |
| 1570 | CHECK MD1F.MS1_DAT_CLCT    | =OFF     |
| 1571 | CHECK MD1F.MS2_DAT_CLCT    | =OFF     |
| 1572 | CHECK MD1F.MS3_DAT_CLCT    | =OFF     |
| 1573 | CHECK MD1F.MS4_DAT_CLCT    | =OFF     |
| 1574 | CHECK MD1F.MS5_DAT_CLCT    | =OFF     |
| 1575 | CHECK MD1F.MS6_DAT_CLCT    | =OFF     |
| 1576 | CHECK MD1F.MS7_DAT_CLCT    | =OFF     |
| 1577 | CHECK MD1F.MS0_BUFF_SEL    | =L       |
| 1578 | CHECK MD1F.MS1_BUFF_SEL    | =L       |
| 1579 | CHECK MD1F.MS2_BUFF_SEL    | =L       |
| 1580 | CHECK MD1F.MS3_BUFF_SEL    | =L       |
| 1581 | CHECK MD1F.MS4_BUFF_SEL    | =L       |
| 1582 | CHECK MD1F.MS5_BUFF_SEL    | =L       |
| 1583 | CHECK MD1F.MS6_BUFF_SEL    | =L       |
| 1584 | CHECK MD1F.MS7_BUFF_SEL    | =L       |
| 1585 | CHECK MD1F.MS0_SGL_CMD_ERR | =na      |
| 1586 | CHECK MD1F.MS1_SGL_CMD_ERR | =na      |
| 1587 | CHECK MD1F.MS2_SGL_CMD_ERR | =na      |
| 1588 | CHECK MD1F.MS3_SGL_CMD_ERR | =na      |
| 1589 | CHECK MD1F.MS4_SGL_CMD_ERR | =na      |
| 1590 | CHECK MD1F.MS5_SGL_CMD_ERR | =na      |
| 1591 | CHECK MD1F.MS6_SGL_CMD_ERR | =na      |
| 1592 | CHECK MD1F.MS7_SGL_CMD_ERR | =na      |
| 1593 | CHECK MD1F.MS0_MTI_CMD_ERR | =na      |
| 1594 | CHECK MD1F.MS1_MTI_CMD_ERR | =na      |
| 1595 | CHECK MD1F.MS2_MTI_CMD_ERR | =na      |
| 1596 | CHECK MD1F.MS3_MTI_CMD_ERR | =na      |
| 1597 | CHECK MD1F.MS4_MTI_CMD_ERR | =na      |
| 1598 | CHECK MD1F.MS5_MTI_CMD_ERR | =na      |
| 1599 | CHECK MD1F.MS6_MTI_CMD_ERR | =na      |
| 1600 | CHECK MD1F.MS7_MTI_CMD_ERR | =na      |
| 1601 | CHECK MD2F.DPU_HW_STATE1   | =DPU_RUN |
| 1602 | CHECK MD2F.DPU_HW_STATE2   | =CPU_RUN |
| 1603 | CHECK MD2F.DMC_IF_RMAPERR  | =na      |
| 1604 | CHECK MD2F.DMC_IF_2BITERR  | =na      |
| 1605 | CHECK MD2F.DMC_IF_BTOERR   | =na      |
| 1606 | CHECK MD2F.BTO_ERR         | =na      |
| 1607 | CHECK MD2F.WDT_ERR         | =na      |
| 1608 | CHECK MD2F.DPU_SELECT      | =DPU2    |
| 1609 | CHECK MD2F.DPU_SW_STATE    | 2        |

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| 1610 | CHECK MD2F. SW_CMD_CODE     | 0    |
| 1611 | CHECK MD2F. SW_CMD_CNT      | 0    |
| 1612 | CHECK MD2F. SW_CMD_REJ_CNT  | 0    |
| 1613 | CHECK MD2F. SW_CMD_REJ_CODE | 0    |
| 1614 | CHECK MD2F. MS_CMD_PRIO     | =DMC |
| 1615 | CHECK MD2F. RDN_MODE        | =OFF |
| 1616 | CHECK MD2F. SUNPULSE_MODE   | =NML |
| 1617 | CHECK MD2F. LOAD_DUMP_DIS   | =DIS |
| 1618 | CHECK MD2F. MEMLOAD_BUSY    | =OFF |
| 1619 | CHECK MD2F. MEMLOAD_NODE    | 0    |
| 1620 | CHECK MD2F. MEMDUMP_BUSY    | =OFF |
| 1621 | CHECK MD2F. MEMDUMP_NODE    | 0    |
| 1622 | CHECK MD2F. TASK_STAT_AP01  | =OFF |
| 1623 | CHECK MD2F. TASK_STAT_AP02  | =OFF |
| 1624 | CHECK MD2F. TASK_STAT_AP03  | =OFF |
| 1625 | CHECK MD2F. TASK_STAT_AP04  | =OFF |
| 1626 | CHECK MD2F. TASK_STAT_AP05  | =OFF |
| 1627 | CHECK MD2F. TASK_STAT_AP06  | =OFF |
| 1628 | CHECK MD2F. TASK_STAT_AP07  | =OFF |
| 1629 | CHECK MD2F. TASK_STAT_AP08  | =OFF |
| 1630 | CHECK MD2F. TASK_STAT_AP09  | =OFF |
| 1631 | CHECK MD2F. TASK_STAT_AP10  | =OFF |
| 1632 | CHECK MD2F. TASK_STAT_AP11  | =OFF |
| 1633 | CHECK MD2F. TASK_STAT_AP12  | =OFF |
| 1634 | CHECK MD2F. TASK_STAT_AP13  | =OFF |
| 1635 | CHECK MD2F. TASK_STAT_AP14  | =OFF |
| 1636 | CHECK MD2F. TASK_STAT_AP15  | =OFF |
| 1637 | CHECK MD2F. TASK_STAT_AP16  | =OFF |
| 1638 | CHECK MD2F. UNEXPECT_CNT    | 0    |
| 1639 | CHECK MD2F. UNEXPECT_CODE   | 0    |
| 1640 | CHECK MD2F. DMC_IF_ERR      | =na  |
| 1641 | CHECK MD2F. TIME_ERR        | =na  |
| 1642 | CHECK MD2F. INST_CACHE_ERR  | =na  |
| 1643 | CHECK MD2F. DATA_CACHE_ERR  | =na  |
| 1644 | CHECK MD2F. SRAM_1BERR      | =na  |
| 1645 | CHECK MD2F. CMD_BUFF_1BERR  | =na  |
| 1646 | CHECK MD2F. CMD_BUFF_2BERR  | =na  |
| 1647 | CHECK MD2F. TLM_BUFF_1BERR  | =na  |
| 1648 | CHECK MD2F. TLM_BUFF_2BERR  | =na  |
| 1649 | CHECK MD2F. COMP_1BERR      | =na  |
| 1650 | CHECK MD2F. DS_1BERR        | =na  |
| 1651 | CHECK MD2F. DS_2BERR        | =na  |
| 1652 | CHECK MD2F. RDN_LINK_STAT   | =ON  |
| 1653 | CHECK MD2F. RIF_LINK_ERR    | =na  |
| 1654 | CHECK MD2F. RIF_ERR         | =na  |
| 1655 | CHECK MD2F. MS0_LINK_STAT   | =OFF |
| 1656 | CHECK MD2F. MS1_LINK_STAT   | =OFF |
| 1657 | CHECK MD2F. MS2_LINK_STAT   | =OFF |
| 1658 | CHECK MD2F. MS3_LINK_STAT   | =OFF |
| 1659 | CHECK MD2F. MS4_LINK_STAT   | =OFF |
| 1660 | CHECK MD2F. MS5_LINK_STAT   | =OFF |
| 1661 | CHECK MD2F. MS6_LINK_STAT   | =OFF |

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1662 CHECK MD2F.MS7_LINK_STAT =OFF
1663 CHECK MD2F.MS1_DAT_CLCT =OFF
1664 CHECK MD2F.MS3_DAT_CLCT =OFF
1665 CHECK MD2F.MS5_DAT_CLCT =OFF
1666 CHECK MD2F.MS6_DAT_CLCT =OFF
1667 CHECK MD2F.MS7_DAT_CLCT =OFF
1668 CHECK MD2F.MS1_BUFF_SEL =L
1669 CHECK MD2F.MS3_BUFF_SEL =L
1670 CHECK MD2F.MS5_BUFF_SEL =L
1671 CHECK MD2F.MS6_BUFF_SEL =L
1672 CHECK MD2F.MS7_BUFF_SEL =L
1673 CHECK MD2F.MS0_SGL_CMD_ERR =na
1674 CHECK MD2F.MS1_SGL_CMD_ERR =na
1675 CHECK MD2F.MS2_SGL_CMD_ERR =na
1676 CHECK MD2F.MS3_SGL_CMD_ERR =na
1677 CHECK MD2F.MS4_SGL_CMD_ERR =na
1678 CHECK MD2F.MS5_SGL_CMD_ERR =na
1679 CHECK MD2F.MS6_SGL_CMD_ERR =na
1680 CHECK MD2F.MS7_SGL_CMD_ERR =na
1681 CHECK MD2F.MS0_MTI_CMD_ERR =na
1682 CHECK MD2F.MS1_MTI_CMD_ERR =na
1683 CHECK MD2F.MS2_MTI_CMD_ERR =na
1684 CHECK MD2F.MS3_MTI_CMD_ERR =na
1685 CHECK MD2F.MS4_MTI_CMD_ERR =na
1686 CHECK MD2F.MS5_MTI_CMD_ERR =na
1687 CHECK MD2F.MS6_MTI_CMD_ERR =na
1688 CHECK MD2F.MS7_MTI_CMD_ERR =na
1689 #
1690 #
1691 . # # Mission Data を 1553B 通信の半分の帯域 (約 4kbps) で出力するモードとする
1692 . DH.MC_EACH_EXE 0x0036 # 1553B TLM mode 10 (SYS HK 1/16, BUS User HK 1/8, DUMP 1/16,
1693 # # MDP User HK 1/8, Mission User HK 1/8, Mission Data 1/2)
1694 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
1695 #: 0x00 DH.DMP_PERIOD 0x04
1696 #: 0x01 DH.DR_PTLOGCYC_SET 0x00
1697 #: 0x02 ACS.ACS_PKT_FMT_1
1698 #: 0x05 DH.OPE_MODE 0x0C 0x3E 0x2C 0x04 0x00000000000000000000000000000000
1699 #: 0x06 DH.TLMFMT1553B_CHG 0x0A
1700 #
1701 WAIT_SEC 40
1702 #
1703 CHECK DH.DMP_PERIOD =SEC_16
1704 CHECK DH.CATEGORY_TBL_NO 0x2C
1705 CHECK DH.DWNLNK_RATE =BPS16384
1706 CHECK DH.TLM1553BFMTNO 0x0A
1707 CHECK ACS.F_ACS_PKT_FMT =ACSHK1
1708 #
1709 #
1710 . # < MDP-DPU User-Task RUN : MDP-DPU1/2 APL START >
1711 . DH.MDP_DPU12_APL_START # DH.MC_EACH_EXE 0x009B
1712 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
1713 #: 0x00 DH.ATMC_EACH_ENA 0x32

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1714 #: 0x02 MD1F. APP01_START
1715 #: 0x03 MD1F. APP02_START
1716 #: 0x04 MD1F. APP03_START
1717 #: 0x05 MD1F. APP04_START
1718 #: 0x06 MD1F. APP05_START
1719 #: 0x07 DH. ATMC_EACH_ENA 0x33
1720 #: 0x08 MD2F. APP01_START
1721 #: 0x09 MD2F. APP02_START
1722 #: 0x0A MD2F. APP13_START
1723 #: 0x0B MD2F. APP14_START
1724 #: 0x0C MD2F. APP05_START
1725 #
1726 WAIT_SEC 40
1727 #
1728 #
1729 # < CHECK >
1730 CHECK DH. ATMC_ENA_DIS32 =ENA
1731 CHECK DH. ATMC_ENA_DIS33 =ENA
1732 #
1733 # QL      Check-Item      Spec      Check
1734 #-----+-----+-----+-----
1735 # MD1F    << DPU1 S/W HK >>
1736 #          SWCMD_CNT      (+5 increment)      N/A
1737 #          SWREJ_CNT      (no change)          N/A
1738 #          << APPLICATIONS >>
1739 #
1740 # < CHECK >
1741 CHECK MD1F. TASK_STAT_AP01 =ON
1742 CHECK MD1F. TASK_STAT_AP02 =ON
1743 CHECK MD1F. TASK_STAT_AP03 =ON
1744 CHECK MD1F. TASK_STAT_AP04 =ON
1745 CHECK MD1F. TASK_STAT_AP05 =ON
1746 #
1747 # QL      Check-Item      Spec      Check
1748 #-----+-----+-----+-----
1749 # MD2F    << DPU2 S/W HK >>
1750 #          SWCMD_CNT      (+5 increment)      N/A
1751 #          SWREJ_CNT      (no change)          N/A
1752 #          << APPLICATIONS >>
1753 #
1754 # < CHECK >
1755 CHECK MD2F. TASK_STAT_AP01 =ON
1756 CHECK MD2F. TASK_STAT_AP02 =ON
1757 CHECK MD2F. TASK_STAT_AP13 =ON
1758 CHECK MD2F. TASK_STAT_AP14 =ON
1759 CHECK MD2F. TASK_STAT_AP05 =ON
1760 #
1761 #
1762 # < MDP USER-TASK: Initial Sequence - MDP-DPU-INITSET>
1763 #
1764 #
1765 . DH. MDP_DPU12_INIT_SET      # DH. MC_EACH_EXE 0x080

```

1766 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)

1767 #: 0x00 MD1U. HK\_SET 0xFF 0x08

1768 #: 0x01 MD1U. HK\_MODE\_CHK

1769 #: 0x02 MD1U. TLM\_CMP 0xFF 0x00 0x11

1770 #: 0x03 MD2U. HK\_SET 0xFF 0x08

1771 #: 0x04 MD2U. HK\_MODE\_CHK

1772 #: 0x05 MD2U. TLM\_CMP 0xFF 0x00 0x11

1773 #: 0x0E MD1U. WDT\_ENA

1774 #: 0x0F MD2U. WDT\_ENA

1775 WAIT\_SEC 5

1776 MD1U. HK\_SET 0xFF 0x04

1777 WAIT\_SEC 2

1778 MD2U. HK\_SET 0xFF 0x04

1779 WAIT\_SEC 40

1780 #

| #    | QL                           | Check-Item       | Spec             | Check |
|------|------------------------------|------------------|------------------|-------|
| 1781 | #                            |                  |                  |       |
| 1782 | #                            |                  |                  |       |
| 1783 | # MD1U                       | U_CmdDmc_GetCNT  | (increment: +5)  | N/A   |
| 1784 | #                            | U_CmdDmc_ExecCNT | (increment: +5)  | N/A   |
| 1785 | #                            | U_App03_Load     | (checked by MDP) |       |
| 1786 | #                            |                  |                  |       |
| 1787 | # MD2U                       | U_CmdDmc_GetCNT  | (increment: +5)  | N/A   |
| 1788 | #                            | U_CmdDmc_ExecCNT | (increment: +5)  | N/A   |
| 1789 | #                            | U_App13_Load     | (checked by MDP) |       |
| 1790 | #                            |                  |                  |       |
| 1791 | #                            | < CHECK >        |                  |       |
| 1792 | CHECK MD1U. U_WdtEna         |                  | =ENA             |       |
| 1793 | CHECK MD1U. U_HkMode         |                  | =CHK             |       |
| 1794 | CHECK MD1U. U_Hk_RepInt_DPU1 |                  | 4 4              |       |
| 1795 | CHECK MD1U. U_Hk_RepInt_ENA  |                  | 4 4              |       |
| 1796 | CHECK MD1U. U_Hk_RepInt_HEPE |                  | 4 4              |       |
| 1797 | CHECK MD1U. U_Hk_RepInt_HEPI |                  | 4 4              |       |
| 1798 | CHECK MD1U. U_Hk_RepInt_MEA1 |                  | 4 4              |       |
| 1799 | CHECK MD1U. U_Hk_RepInt_MEA2 |                  | 4 4              |       |
| 1800 | CHECK MD1U. U_Hk_RepInt_MGFO |                  | 4 4              |       |
| 1801 | CHECK MD1U. U_Hk_RepInt_MIA  |                  | 4 4              |       |
| 1802 | CHECK MD1U. U_Hk_RepInt_MSA  |                  | 4 4              |       |
| 1803 | CHECK MD1U. U_TlmML_CMP_DPU1 |                  | =CMP             |       |
| 1804 | CHECK MD1U. U_TlmML_CMP_ENA  |                  | =USR             |       |
| 1805 | CHECK MD1U. U_TlmML_CMP_HEPE |                  | =ARO             |       |
| 1806 | CHECK MD1U. U_TlmML_CMP_HEPI |                  | =ARO             |       |
| 1807 | CHECK MD1U. U_TlmML_CMP_MEA1 |                  | =USR             |       |
| 1808 | CHECK MD1U. U_TlmML_CMP_MEA2 |                  | =USR             |       |
| 1809 | CHECK MD1U. U_TlmML_CMP_MGFO |                  | =ARO             |       |
| 1810 | CHECK MD1U. U_TlmML_CMP_MIA  |                  | =USR             |       |
| 1811 | CHECK MD1U. U_TlmML_CMP_MSA  |                  | =USR             |       |
| 1812 | CHECK MD1U. U_TlmH_CMP_DPU1  |                  | =CMP             |       |
| 1813 | CHECK MD1U. U_TlmH_CMP_HEPE  |                  | =ARO             |       |
| 1814 | CHECK MD1U. U_TlmH_CMP_HEPI  |                  | =ARO             |       |
| 1815 | CHECK MD1U. U_TlmH_CMP_MEA1  |                  | =USR             |       |
| 1816 | CHECK MD1U. U_TlmH_CMP_MEA2  |                  | =USR             |       |
| 1817 | CHECK MD1U. U_TlmH_CMP_MGFO  |                  | =ARO             |       |

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1818 CHECK MD1U.U_TImH_CMP_MIA =USR
1819 CHECK MD1U.U_TImH_CMP_MSA =USR
1820 CHECK MD2U.U_WdtEna =ENA
1821 CHECK MD2U.U_HkMode =CHK
1822 CHECK MD2U.U_Hk_RepInt_DPU2 4 4
1823 CHECK MD2U.U_Hk_RepInt_EWO 4 4
1824 CHECK MD2U.U_Hk_RepInt_MEF 4 4
1825 CHECK MD2U.U_Hk_RepInt_MGFI 4 4
1826 CHECK MD2U.U_Hk_RepInt_MSAS 4 4
1827 CHECK MD2U.U_Hk_RepInt_MWE 4 4
1828 CHECK MD2U.U_TImML_CMP_DPU2 =CMP
1829 CHECK MD2U.U_TImML_CMP_AM2P =ARO
1830 CHECK MD2U.U_TImML_CMP_EFD =ARO
1831 CHECK MD2U.U_TImML_CMP_EWOB =ARO
1832 CHECK MD2U.U_TImML_CMP_EWOE =ARO
1833 CHECK MD2U.U_TImML_CMP_MDM =ARO
1834 CHECK MD2U.U_TImML_CMP_MGFI =ARO
1835 CHECK MD2U.U_TImML_CMP_MSAS =ARO
1836 CHECK MD2U.U_TImML_CMP_SOR =ARO
1837 CHECK MD2U.U_TImH_CMP_DPU2 =CMP
1838 CHECK MD2U.U_TImH_CMP_EFD =ARO
1839 CHECK MD2U.U_TImH_CMP_EWOB =USR
1840 CHECK MD2U.U_TImH_CMP_EWOE =USR
1841 CHECK MD2U.U_TImH_CMP_MGFI =ARO
1842 #
1843 # QL Check-Item Spec Check
1844 # -----+-----+-----+
1845 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1846 #
1847 #
1848 # *****
1849 # 3. MEA2 初期高圧 CO
1850 # *****
1851 # =====
1852 # 3.1. MEA2 LV check
1853 # =====
1854 # -----
1855 # 3.1.1. MEA2 SWITCH ON
1856 # <<M16-5 Power ON Duration:5 min Day:70>>
1857 # -----
1858 #
1859 # # MEA2 の電源を ON する。
1860 # Power on MEA2 by switching on.
1861 #
1862 # PCD.ON_OFF_ENA
1863 WAIT_SEC 40
1864 #
1865 # < CHECK >
1866 CHECK PCD.ON_OFF_ENA_DIS =ENA
1867 #
1868 # PCD.PSU_MEA2_ON
1869 WAIT_SEC 40

```

```

1870 #
1871 #
1872 # < CHECK >
1873 CHECK PCD.PSU_MEA2_ON_OFF =ON
1874 #
1875 . PCD.ON_OFF_DIS
1876 WAIT_SEC 40
1877 #
1878 # < CHECK >
1879 CHECK PCD.ON_OFF_ENA_DIS =DIS
1880 CHECK MD1F.MS2_LINK_STAT =ON
1881 #
1882 # # SW Error clear
1883 . MD1F.DPU_SW_ERR_CLR
1884 WAIT_SEC 40
1885 #
1886 # < CHECK >
1887 CHECK MD1F.MS2_LINK_ERR =na
1888 CHECK MD1F.SLOT_ERR =na
1889 #
1890 #
1891 #
1892 WAIT_SEC 300
1893 #
1894 #
1895 # # MEA2 テレメトリ表示の確認(LINK 確立から 8 分以上経過後に実施)
1896 # Confirmation of the MEA2 telemetry indication
1897 # (More than 8 minutes after from LINK establishment)
1898 #
1899 CHECK MEA2.HK_I_P5V <110 mA
1900 CHECK MEA2.HK_I_P12V <17 # mA
1901 CHECK MEA2.HK_MCP 0 # V
1902 CHECK MEA2.I_MCP 0 # uA
1903 CHECK MEA2.HK_TMP_FPG <30 # deg. (depend on the environment,
maximum 85° C)
1904 CHECK MEA2.HK_TMP_MCP <30 # deg. (depend on the environment,
maximum 80° C)
1905 CHECK MEA2.Status_reg 0x80 # (initial value)
1906 CHECK MEA2.I1_cmd_exe =0xFFFF
1907 CHECK MEA2.I2_cmd_exe =0xFFFF
1908 #
1909 # QL Check-Item Spec Check
1910 # -----+-----+-----+-----
1911 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1912 #
1913 #
1914 # -----
1915 . # 3.1.2. MEM_DUMP
1916 # <<M16-6 Cal Check Duration:15 min Day:70>>
1917 #
1918 # CHECK ALL HV MEMORY TABLE in EEPROM to prevent errors, "corruption, delete...
1919 #

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1920 # -----
1921 #
1922 # # HV Table の確認    HV Table Verification
1923 # # MDP send a CMD MEA DUMP memory 0x2F 0x04 : HV Table 3eV -> 3KeV
1924 #   (transfert between EEPROM HV Table (n∞4) to SRAM Spacewire before
1925 #     the MDP RMAP dump packet)
1926 #
1927 . MEA2.Scan_OF
1928 WAIT_SEC 40
1929 #
1930 . MD1F.MEA2_CLCT_STOP
1931 WAIT_SEC 40
1932 #
1933 # < CHECK >
1934 CHECK MD1F.MS2_DAT_CLCT                =OFF
1935 #
1936 #
1937 #
1938 #
1939 # DUMP 1st HV table : Table 3eV-300eV
1940 #   1-Send Command to read EEPROM and transfer HV table in RAM1008
1941 #
1942 . MEA2.Dump_EEP          0x00
1943 WAIT_SEC 40
1944 #
1945 . DH.OPE_MODE 0x0C 0x3E 0x2C 0x04 0x00000000000000000000000000000000
1946 DH.DMP_PERIOD 0x02
1947 WAIT_SEC 40
1948 CHECK DH.DMP_PERIOD                =SEC_4
1949 CHECK DH.CATEGORY_TBL_NO          0x2C
1950 CHECK DH.DWNLNK_RATE              =BPS16384
1951 #
1952 . DH.TLMFMT1553B_CHG 0x0A
1953 WAIT_SEC 40
1954 #
1955 # < CHECK >
1956 CHECK MEA2.l1_cmd_exe              =0x2F00
1957 #
1958 . DH.TLMFMT1553B_CHG 0x06
1959 WAIT_SEC 40
1960 #
1961 # < CHECK >
1962 CHECK DH.TLM1553BFMTNO            0x06
1963 #
1964 # # MDP Read DUMP memory 1st packet 512Bytes
1965 #   MDP から MEA2 ROM を 512byte DUMP
1966 #
1967 #   2-Dump RAM to MDP (Adresse : 0x600 and size=512B
1968 #
1969 . MD1F.MEM_DUMP 0x01 0x06 0x00000600 0x000200
1970 WAIT_SEC 300
1971 #

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1972 . # # MEA 側にてダンプ結果を確認
1973 #
1974 # 3-Check the 1st HV Table
1975 #
1976 #
1977 #
1978 #
1979 # DUMP 2nd HV table : Table 3eV-25KeV
1980 # 1-Send Commant to read EEPROM and transfert HV table in RAM
1981 #
1982 . MEA2.Dump_EEP          0x01
1983 WAIT_SEC 40
1984 #
1985 . DH.TLMFMT1553B_CHG 0x0A
1986 WAIT_SEC 40
1987 #
1988 # < CHECK >
1989 CHECK DH.TLM1553BFMTNO          0x0A
1990 CHECK MEA2.l1_cmd_exe          =0x2F01
1991 CHECK MEA2.l2_cmd_exe          =0x2F00
1992 #
1993 . DH.TLMFMT1553B_CHG 0x06
1994 WAIT_SEC 40
1995 #
1996 # < CHECK >
1997 CHECK DH.TLM1553BFMTNO          0x06
1998 #
1999 # # MDP Read DUMP memory 1st packet 512Bytes
2000 # MDP から MEA2 ROM を 512byte DUMP
2001 #
2002 # 2-Dump RAM to MDP (Adresse : 0x600 and size=512B
2003 # => need 2 times (1st time is not a good packet, 2nd time is OK)
2004 #
2005 . MD1F.MEM_DUMP 0x01 0x06 0x00000600 0x000200
2006 . MD1F.MEM_DUMP 0x01 0x06 0x00000600 0x000200
2007 WAIT_SEC 300
2008 #
2009 . # # MEA 側にてダンプ結果を確認
2010 #
2011 # 3-Check the 2nd HV Table
2012 #
2013 #
2014 #
2015 #
2016 # DUMP 3rd HV table : Table 3eV-3KeV
2017 # 1-Send Commant to read EEPROM and transfert HV table in RAM
2018 #
2019 . MEA2.Dump_EEP          0x02
2020 WAIT_SEC 40
2021 #
2022 . DH.TLMFMT1553B_CHG 0x0A
2023 WAIT_SEC 40

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2024 #
2025 # < CHECK >
2026 CHECK DH. TLM1553BFMTNO 0x0A
2027 CHECK MEA2. I1_cmd_exe =0x2F02
2028 CHECK MEA2. I2_cmd_exe =0x2F01
2029 #
2030 . DH. TLMFMT1553B_CHG 0x06
2031 WAIT_SEC 40
2032 #
2033 # < CHECK >
2034 CHECK DH. TLM1553BFMTNO 0x06
2035 #
2036 # # MDP Read DUMP memory 1st packet 512Bytes
2037 # MDP から MEA2 ROM を 512byte DUMP
2038 #
2039 # 2-Dump RAM to MDP (Adresse : 0x600 and size=512B
2040 # => need 2 times (1st time is not a good packet, 2nd time is OK)
2041 #
2042 . MD1F.MEM_DUMP 0x01 0x06 0x00000600 0x000200
2043 . MD1F.MEM_DUMP 0x01 0x06 0x00000600 0x000200
2044 WAIT_SEC 300
2045 #
2046 . # # MEA 側にてダンプ結果を確認
2047 #
2048 # 3-Check the 3rd HV Table
2049 #
2050 #
2051 #
2052 #
2053 # DUMP 4th HV table : Table 3KeV-25KeV
2054 # 1-Send Commant to read EEPROM and transfert HV table in RAM
2055 #
2056 . MEA2. Dump_EEP 0x03
2057 WAIT_SEC 40
2058 #
2059 . DH. TLMFMT1553B_CHG 0x0A
2060 WAIT_SEC 40
2061 #
2062 # < CHECK >
2063 CHECK DH. TLM1553BFMTNO 0x0A
2064 CHECK MEA2. I1_cmd_exe =0x2F03
2065 CHECK MEA2. I2_cmd_exe =0x2F02
2066 #
2067 . DH. TLMFMT1553B_CHG 0x06
2068 WAIT_SEC 40
2069 #
2070 # < CHECK >
2071 CHECK DH. TLM1553BFMTNO 0x06
2072 #
2073 # # MDP Read DUMP memory 1st packet 512Bytes
2074 # MDP から MEA2 ROM を 512byte DUMP
2075 #

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2076 # 2-Dump RAM to MDP (Adresse : 0x600 and size=512B
2077 # => need 2 times (1st time is not a good packet, 2nd time is OK)
2078 #
2079 . MD1F.MEM_DUMP 0x01 0x06 0x00000600 0x000200
2080 . MD1F.MEM_DUMP 0x01 0x06 0x00000600 0x000200
2081 WAIT_SEC 300
2082 #
2083 . # # MEA 側にてダンプ結果を確認
2084 #
2085 # 3-Check the 4th HV Table
2086 #
2087 #
2088 #
2089 #
2090 . MD1F.MEA2_CLCT_START
2091 #
2092 WAIT_SEC 40
2093 #
2094 # < CHECK >
2095 CHECK MD1F.MS2_DAT_CLCT =ON
2096 #
2097 . # # Mission Data を 1553B 通信の半分の帯域 (約 4kbps) で出力するモードとする
2098 . DH.MC_EACH_EXE 0x0036 # 1553B TLM mode 10 (SYS HK 1/16, BUS User HK 1/8, DUMP 1/16,
2099 # # MDP User HK 1/8, Mission User HK 1/8, Mission Data 1/2)
2100 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
2101 #: 0x00 DH.DMP_PERIOD 0x04
2102 #: 0x01 DH.DR_PTLOGCYC_SET 0x00
2103 #: 0x02 ACS.ACS_PKT_FMT_1
2104 #: 0x05 DH.OPE_MODE 0x0C 0x3E 0x2C 0x04 0x00000000000000000000000000000000
2105 #: 0x06 DH.TLMFMT1553B_CHG 0x0A
2106 #
2107 WAIT_SEC 40
2108 #
2109 # < CHECK >
2110 CHECK DH.DMP_PERIOD =SEC_16
2111 CHECK DH.CATEGORY_TBL_NO 0x2C
2112 CHECK DH.DWNLNK_RATE =BPS16384
2113 CHECK DH.TLM1553BFMTNO 0x0A
2114 CHECK ACS.F_ACS_PKT_FMT =ACSHK1
2115 #
2116 #
2117 # QL Check-Item Spec Check
2118 # -----+-----+-----+-----+
2119 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2120 #
2121 # =====
2122 . # 3.2. SECURITY OFF
2123 # <<M16-7 DISABLE HV SECURITY Duration:5 min Day:70>>
2124 # =====
2125 # -----
2126 . # 3.2.1. DISABLE HV SECURITY (MCP + SPHERES), 10 min"
2127 # -----

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2128 #
2129 # Enable HV ALL (MCP + SPHERES)
2130 . MEA2.HV_EN
2131 WAIT_SEC 240
2132 #
2133 . # <CHECK> the HK
2134 CHECK MEA2.l1_cmd_exe =0x1D00
2135 # QL Check-Item Spec Check
2136 #
2137 # -----+-----+-----+-----
2138 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2139 #
2140 # -----
2141 . # 3.2.2. DISABLE MCP SECURITY
2142 # -----
2143 #
2144 # Before Disable MCP security, send 0V to MCP (prevent the big step
2145 # after security OFF
2146 . MEA2.MCP_ON 0x00
2147 WAIT_SEC 300
2148 #
2149 . # <CHECK>
2150 #
2151 # This command start the internal no regulated MCP HV at 3500V and its
2152 # not possible to decrease this value (not remoteled) and the parameter
2153 # of this command fixe the MCP HV outut voltage (at 0V for this parameter),
2154 # => So MCP is secure because MPC HV output is at 0V although the internal HV is at 3500V
2155 #
2156 CHECK MEA2.l1_cmd_exe =0x1C00
2157 CHECK MEA2.Status_reg 0xC0 # (bit n° 7 = 1 and Bit n° 6 = 1)
2158 # CHECK MEA2.HK_MCP : ~0 after 1 or 2 min
2159 #
2160 # QL Check-Item Spec Check
2161 # -----+-----+-----+-----
2162 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2163 #
2164 #
2165 # -----
2166 . # 3.2.3. Disable the MCP bit security (This function is checked after)
2167 # -----
2168 #
2169 . MEA2.SECRTY_OFF
2170 WAIT_SEC 300
2171 #
2172 . # <CHECK> the HK
2173 CHECK MEA2.l1_cmd_exe =0x1F00
2174 CHECK MEA2.Status_reg 0x40 # (bit n° 7 = 0)
2175 #
2176 # QL Check-Item Spec Check
2177 # -----+-----+-----+-----
2178 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2179 #

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2180 # =====
2181 # 3.3. SWITCH ON MCP (2750V in vacuum)
2182 # <<M16-8 SWITCH ON MCP Duration:140 min Day:70>>
2183 # =====
2184 # -----
2185 # 3.3.1. SWITCH ON MCP, 25 min to verify the MCP HV
2186 # -----
2187 #
2188 # Switch ON the MCP, depend on the environment :
2189 #
2190 # The step to change the value must be to 100V during 5 min so :
2191 # - for 2750 V, number of steps is 2750/100 + 1 => 29 Steps
2192 # OR
2193 # - for 400 V, number of steps is 400/100 + 1 => 5 Steps
2194 #
2195 # Notes : Maximum Value depend on the environnement
2196 # -> Vaccum Chamber or in Space : MCP Value is 2750 V (parameter 0c8)
2197 # -> ON air, MCP Value is 400 V (parameter 0x1d)
2198 # Notes : Duration, depend on the maximum value and environnement
2199 # -> 29 x 5 min = 145 min
2200 # -> 5 x 5 min = 25 min
2201 # Notes : Step size
2202 # -> Size of 100 V Step = 100 V - 0x07
2203 # -> Value 400 V - 0x1d
2204 # -> Value 2750 V - 0xc8
2205 # -> Minimum step 26 V - 0x01 and Internal step
2206 # -> Command_Value = (Value_MCP / 0.858) / 16
2207 # -> example : MCP=400V => 29 => 0x1d
2208 # Send Value to MCP and MCP ON
2209 #
2210 # WAIT 5 MINUTE BETWEEN EACH STEPS
2211 #
2212 . MEA2.MCP_ON 0x00
2213 WAIT_SEC 300
2214 #
2215 . # <CHECK> the HK
2216 CHECK MEA2.l1_cmd_exe =0x1c00
2217 # CHECK MEA2.HK_MCP : < 0V
2218 CHECK MEA2.Status_reg 0x40 # (bit n° 6 = 1)
2219 #
2220 #
2221 . MEA2.MCP_ON 0x07
2222 WAIT_SEC 40
2223 #
2224 . # <CHECK> the HK
2225 CHECK MEA2.l1_cmd_exe =0x1c07
2226 # CHECK MEA2.HK_MCP : ~ 100V
2227 #
2228 #
2229 . MEA2.MCP_ON 0x0E
2230 WAIT_SEC 300
2231 #

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2232 . # <CHECK> the HK
2233 CHECK MEA2.l1_cmd_exe =0x1c0E
2234 # CHECK MEA2.HK_MCP : ~ 200V
2235 #
2236 #
2237 . MEA2.MCP_ON 0x15
2238 WAIT_SEC 300
2239 #
2240 . # <CHECK> the HK
2241 CHECK MEA2.l1_cmd_exe =0x1c15
2242 # CHECK MEA2.HK_MCP : ~ 300V
2243 #
2244 #
2245 . MEA2.MCP_ON 0x1D
2246 WAIT_SEC 300
2247 #
2248 . # <CHECK> the HK
2249 CHECK MEA2.l1_cmd_exe =0x1c1d
2250 # CHECK MEA2.HK_MCP : ~ 400V
2251 #
2252 #
2253 # QL          Check-Item          Spec          Check
2254 # -----+-----+-----+-----+
2255 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2256 #
2257 #
2258 # -----
2259 . # 3.3.2. ENABLE MCP SECURITY, 5 min
2260 # -----
2261 #
2262 # Enable the MCP bit security, Value_MCP divide by 100, to verify the security MCP
2263 . MEA2.SECRTY_ON
2264 WAIT_SEC 300
2265 #
2266 . # <CHECK> the HK
2267 # CHECK MEA2.HK_MCP : ~ 0V after some minutes, 400V/100
2268 CHECK MEA2.l1_cmd_exe =0x1800
2269 CHECK MEA2.Status_reg 0xC0 # (bit n° 7 = 1 and 6 = 1)
2270 #
2271 # QL          Check-Item          Spec          Check
2272 # -----+-----+-----+-----+
2273 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2274 #
2275 #
2276 # -----
2277 . # 3.3.3 DISABLE MCP SECURITY, 5 min
2278 # -----
2279 #
2280 # Disbale the MCP bit security, MCP retrieve the previous value at 400V
2281 . MEA2.SECRTY_OFF
2282 WAIT_SEC 300
2283 #

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2284 . # <CHECK> the HK
2285 CHECK MEA2.l1_cmd_exe =0x1F00
2286 CHECK MEA2.Status_reg 0x40 # (bit n° 7 = 0)
2287 # CHECK MEA2.HK_MCP : ~ 400V
2288 #
2289 # QL Check-Item Spec Check
2290 # -----+-----+-----+-----
2291 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2292 #
2293 # -----
2294 . # 3.3.4. SWITCH OFF MCP, 25 min
2295 # -----
2296 #
2297 # Send Value to MCP and MCP ON
2298 #
2299 . # WAIT 5 MINUTE BETWEEN EACH STEPS
2300 . MEA2.MCP_ON 0x15
2301 WAIT_SEC 300
2302 #
2303 . # <CHECK> the HK
2304 CHECK MEA2.l1_cmd_exe =0x1c15
2305 # CHECK MEA2.HK_MCP : ~ 300V
2306 #
2307 #
2308 . MEA2.MCP_ON 0x0E
2309 WAIT_SEC 300
2310 #
2311 . # <CHECK> the HK
2312 CHECK MEA2.l1_cmd_exe =0x1c0E
2313 # CHECK MEA2.HK_MCP : ~ 200V
2314 #
2315 #
2316 . MEA2.MCP_ON 0x07
2317 WAIT_SEC 300
2318 #
2319 . # <CHECK> the HK
2320 CHECK MEA2.l1_cmd_exe =0x1c07
2321 # CHECK MEA2.HK_MCP : ~ 100V
2322 #
2323 #
2324 . MEA2.MCP_ON 0x00
2325 WAIT_SEC 300
2326 #
2327 . # <CHECK> the HK
2328 CHECK MEA2.l1_cmd_exe =0x1c00
2329 # CHECK MEA2.HK_MCP : < 0V after some minutes
2330 #
2331 #
2332 # Stop the MCP HV
2333 . MEA2.MCP_OFF
2334 WAIT_SEC 300
2335 #

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2336 . # <CHECK> the HK
2337 CHECK MEA2.l1_cmd_exe =0x1900
2338 CHECK MEA2.Status_reg 0x00 # (bit n° 6 = 0)
2339 #
2340 # QL Check-Item Spec Check
2341 # -----+-----+-----+-----
2342 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2343 #
2344 #
2345 # =====
2346 . # 3.4. SWITCH ON SCAN, 15 min
2347 # <<M16-9 SWITCH ON SCAN Duration:120 min Day:70>>
2348 # =====
2349 #
2350 # Start the scan of the sphere and Analyzer HV to verify the noise on A111 and MCP
2351 # Start the scan and verify if the instrument is synchronized or not
2352 #
2353 #
2354 . MEA2.Scan_ON
2355 WAIT_SEC 300
2356 #
2357 . # <CHECK> the HK
2358 CHECK MEA2.l1_cmd_exe =0x2500
2359 #
2360 #
2361 # Select the sun pulse synchronization
2362 . MEA2.Md_S_Auto
2363 WAIT_SEC 300
2364 #
2365 . # <CHECK> the HK
2366 CHECK MEA2.l1_cmd_exe =0x3100
2367 #
2368 #
2369 # Select energies mode : mode 32 energies/32 sweeps
2370 # -> HV table n° 2, 3eV-3KeV (~300V maxi on the analyzer)
2371 #
2372 . MEA2.mode_ROM_4 0x02
2373 WAIT_SEC 300
2374 #
2375 . # <CHECK> the HK
2376 CHECK MEA2.l1_cmd_exe =0x0302
2377 # Check HKP TmSStp = HKP TmSStp_dft after some secondes
2378 #
2379 ## *****
2380 # WAIT until (5 min maximum) HKP TmSStp = TmSStp_dft to indicate the synchronization
2381 # between sun plus and MEA.
2382 # If not, do not execute the next steps!!
2383 # Probably, sun pulse signal have a problem (cf previous test at ISAS)!!
2384 # If already executed, you can pass at the next step.
2385 ## *****
2386 #
2387 WAIT_SEC 300

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2388 . # Check the science Data to verify the value
2389 # - wait 10 min to verify the Science Data
2390 #
2391 # QL Check-Item Spec Check
2392 # -----+-----+-----+-----
2393 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2394 #
2395 #
2396 # =====
2397 . # 3.5. TEST MODE ON, 150 min
2398 # <<M16-10 TEST MODE ON Duration:30 min Day:70>>
2399 # =====
2400 #
2401 # Switch ON the test mode for the Amplifier A111
2402 # to verify the good working of this
2403 . MEA2.Md_Tst_ON
2404 WAIT_SEC 900
2405 #
2406 . # <CHECK> the HK
2407 CHECK MEA2.l1_cmd_exe =0x3900
2408 CHECK MEA2.Status_reg 0x01 # (bit n° 0 = 1)
2409 #
2410 # - wait 15 min to verify the Science Data
2411 # Check the science Data and compare with the previous value
2412 #
2413 # QL Check-Item Spec Check
2414 # -----+-----+-----+-----
2415 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2416 #
2417 #
2418 # =====
2419 . # 3.6. TEST MODE OFF, 10 min
2420 # <<M16-12 TEST MODE OFF Duration:5 min Day:70>>
2421 # =====
2422 #
2423 # Stop the A111 test Mode
2424 . MEA2.Md_Tst_OF
2425 WAIT_SEC 600
2426 #
2427 . # <CHECK> the HK
2428 CHECK MEA2.l1_cmd_exe =0x3800
2429 CHECK MEA2.Status_reg 0x00 # (bit n° 0 = 0)
2430 #
2431 # - wait 10 min to verify the Science Data are return to zero
2432 # Check the science Data and compare with the previous value
2433 #
2434 #
2435 # QL Check-Item Spec Check
2436 # -----+-----+-----+-----
2437 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2438 #
2439 #

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2440 # =====
2441 . # 3.7. ENABLE HV of ANALYZER (25 minutes)
2442 # <<M16-** Duration:** min Day:70>>
2443 # =====
2444 #
2445 # -> DO NOT EXECUTE THIS PART IF HV TABLE IS NOT n° 2
2446 # -> HV table n° 2 are selected, 3eV-3KeV (~300V maxi on the analyzer), verify this value
on HKP
2447 # -> Same thing than the MCP, When the analyzer is ON, a 2nd internal HV and no regulated
is ON
2448 # and her value is 3000V even if the output analyzer is maximum at 300V (depend on the
HV table)
2449 ## *****
2450 #
2451 # Deactive the current limiter switch OFF (just for switch ON the HV)
2452 . MEA2.ON_F12V
2453 WAIT_SEC 300
2454 #
2455 . # <CHECK> the HK
2456 CHECK MEA2.l1_cmd_exe =0x3F00
2457 #
2458 #
2459 #Switch ON the HV Spheres of analyzer
2460 . MEA2.Anlyz_ON
2461 WAIT_SEC 300
2462 #
2463 . # <CHECK> the HK
2464 CHECK MEA2.l1_cmd_exe =0x3F00
2465 CHECK MEA2.Status_reg 0x20 # (bit n° 5 = 1)
2466 #
2467 #
2468 # Re-active the current limiter switch OFF
2469 . MEA2.AUTO12V
2470 WAIT_SEC 300
2471 #
2472 . # <CHECK> the HK
2473 CHECK MEA2.l1_cmd_exe =0x3E00
2474 #
2475 #
2476 #
2477 WAIT_SEC 560
2478 ## *****
2479 . # Check the science Data to verify the value
2480 #
2481 # - wait 10 min to verify the Science Data
2482 #
2483 #
2484 ## *****
2485 #
2486 #
2487 # QL Check-Item Spec Check
2488 # -----+-----+-----+-----

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2489 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2490 #
2491 #
2492 # =====
2493 # 3.8. SWITCH ON MCP to verify the MCP noise (25 minutes)
2494 # <<M16-** Duration:** min Day:70>>
2495 # =====
2496 #
2497 # Switch ON the MCP, depend on the environment
2498 #
2499 # The step to change the value must be to 100V so :
2500 # - for 2750 V, number of steps is 2750/100 + 1 => 29 Steps
2501 # OR
2502 # - for 400 V, number of steps is 400/100 + 1 => 5 Steps"
2503 # 1010 10 a
2504 # Notes : Maximum Value depend on the environment
2505 # -> Vacuum Chamber or in Space : MCP Value is 2750 V (parameter 0c8)
2506 # -> ON air, MCP Value is 400 V (parameter 0x1d)"
2507 # Notes : Duration, depend on the maximum value and environment
2508 # -> 29 x 5 min = 145 min
2509 # -> 5 x 5 min = 25 min
2510 # Notes : Step size
2511 # -> Size of 100 V Step = 100 V - 0x07
2512 # -> Value 400 V - 0x1d
2513 # -> Value 2750 V - 0xc8
2514 # -> Minimum step 26 V - 0x01 and Internal step
2515 # -> Command_Value = (Value_MCP / 0.858) / 16
2516 # -> example : MCP=400V => 29 => 0x1d
2517 ## *****
2518 # Send Value to MCP and MCP ON
2519 #
2520 # MEA2.MCP_ON 0x00
2521 # WAIT_SEC 300
2522 #
2523 # <CHECK> the HK
2524 # CHECK MEA2.I1_cmd_exe =0x1c00
2525 # CHECK MEA2.HK_MCP : < 0V
2526 # CHECK MEA2.Status_reg 0x60 # (bit n° 6 = 1 and bit 5 = 1)
2527 #
2528 # Check the science Data to verify the noise
2529 #
2530 # - wait 5 min to verify the Science Data
2531 #
2532 #
2533 # MEA2.MCP_ON 0x07
2534 # WAIT_SEC 300
2535 #
2536 # <CHECK> the HK
2537 # CHECK MEA2.I1_cmd_exe 0x1c07
2538 # CHECK MEA2.HK_MCP : ~ 100V
2539 #
2540 # Check the science Data to verify the noise

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2541 #
2542 # - wait 5 min to verify the Science Data
2543 #
2544 #
2545 . MEA2.MCP_ON 0x0E
2546 WAIT_SEC 300
2547 #
2548 . # <CHECK> the HK
2549 CHECK MEA2.l1_cmd_exe =0x1c0E
2550 # CHECK MEA2.HK_MCP : ~ 200V
2551 #
2552 # Check the science Data to verify the noise
2553 #
2554 # - wait 5 min to verify the Science Data
2555 #
2556 #
2557 . MEA2.MCP_ON 0x15
2558 WAIT_SEC 300
2559 #
2560 . # <CHECK> the HK
2561 CHECK MEA2.l1_cmd_exe =0x1c15
2562 # CHECK MEA2.HK_MCP : ~ 300V
2563 #
2564 # Check the science Data to verify the noise
2565 #
2566 # - wait 5 min to verify the Science Data
2567 #
2568 #
2569 . #MEA2.MCP_ON 0x1D
2570 WAIT_SEC 300
2571 #
2572 . # <CHECK> the HK
2573 CHECK MEA2.l1_cmd_exe =0x1c1d
2574 # CHECK MEA2.HK_MCP : ~ 400V
2575 #
2576 # Check the science Data to verify the noise
2577 #
2578 # - wait 5 min to verify the Science Data
2579 #
2580 #
2581 ## *****
2582 ## *****
2583 ## TEST IS IN THE AIR ? -> IN THE AIR, STEP AT THE GF PART"
2584 ## *****
2585 ## *****
2586 #
2587 # QL Check-Item Spec Check
2588 # -----+-----+-----+-----
2589 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2590 #
2591 #
2592 # =====

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2593 . # 3.9. GF (20 minutes duration, 5 minutes per geometric factor)
2594 # <<M16-** Duration:** min Day:70>>
2595 # CHECK both HK and Science Data for each geometric factor
2596 # =====
2597 #
2598 # Change the geometrical factor to 0.8
2599 . MEA2.G_Factor 0x01
2600 WAIT_SEC 300
2601 #
2602 . # <CHECK> the HK
2603 CHECK MEA2.l1_cmd_exe =0x0801
2604 #
2605 # Check the science Data to verify the noise
2606 #
2607 # - wait 5 min to verify the Science Data
2608 #
2609 #
2610 # Change the geometrical factor to 0.53
2611 . MEA2.G_Factor 0x02
2612 WAIT_SEC 300
2613 #
2614 . # <CHECK> the HK
2615 CHECK MEA2.l1_cmd_exe =0x0802
2616 #
2617 # Check the science Data to verify the noise
2618 #
2619 # - wait 5 min to verify the Science Data
2620 #
2621 #
2622 # Change the geometrical factor to 0.35
2623 . MEA2.G_Factor 0x03
2624 WAIT_SEC 300
2625 #
2626 . # <CHECK> the HK
2627 CHECK MEA2.l1_cmd_exe =0x0803
2628 #
2629 # Check the science Data to verify the noise
2630 #
2631 # - wait 5 min to verify the Science Data
2632 #
2633 #
2634 # Change the geometrical factor to 0.27
2635 . MEA2.G_Factor 0x04
2636 WAIT_SEC 300
2637 #
2638 . # <CHECK> the HK
2639 CHECK MEA2.l1_cmd_exe =0x0804
2640 #
2641 # Check the science Data to verify the noise
2642 #
2643 # - wait 5 min to verify the Science Data
2644 #

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2645 #
2646 # QL                      Check-Item                      Spec                      Check
2647 # -----+-----+-----+-----+
2648 # CMD 出力装置  Message                      STATUS CHECK 結果 OK                      G  N
2649 #
2650 #
2651 # =====
2652 # 3.10. SWITCH OFF MCP in VACUUM (10 MINUTES)
2653 #   <<M16-**   Duration:** min Day:70>>
2654 #           CHECK both HK and Science Data for each geometric factor
2655 # =====
2656 #
2657 ## *****
2658 # Send Value to MCP and MCP ON
2659 #   - wait 5 min between each step (2x5 minutes = 10 minutes)
2660 ## *****
2661 # START HERE IN THE AIR
2662 # MEA2.MCP_ON 0x0E
2663 # WAIT_SEC 300
2664 #
2665 # <CHECK> the HK
2666 # CHECK MEA2.l1_cmd_exe                      =0x1c0E
2667 # CHECK MEA2.HK_MCP : ~ 200V
2668 #
2669 #
2670 # MEA2.MCP_ON 0x00
2671 # WAIT_SEC 300
2672 #
2673 # <CHECK> the HK
2674 # CHECK MEA2.l1_cmd_exe                      =0x1c00
2675 # CHECK MEA2.HK_MCP : < 0V
2676 #
2677 #
2678 # MEA2.MCP_OFF
2679 # WAIT_SEC 300
2680 #
2681 # <CHECK> the HK
2682 # CHECK MEA2.l1_cmd_exe                      =0x1900
2683 # CHECK MEA2.Status_reg                      0x22 # (bit n° 6 = 0 and bit 5 = 1)
2684 #
2685 # QL                      Check-Item                      Spec                      Check
2686 # -----+-----+-----+-----+
2687 # CMD 出力装置  Message                      STATUS CHECK 結果 OK                      G  N
2688 #
2689 #
2690 # =====
2691 # 3.11. SWITCH OFF the SCAN (15 minutes)
2692 #   <<M16-**   Duration:** min Day:70>>
2693 #           CHECK both HK and Science Data for each geometric factor
2694 # -----
2695 #
2696 # SWITCH OFF THE ANALYZER HV, after this NO HV inside MEA"

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2697 #
2698 #
2699 . MEA2.Anlyz_OF
2700 WAIT_SEC 300
2701 #
2702 . # <CHECK> the HK
2703 CHECK MEA2.l1_cmd_exe =0x2000
2704 CHECK MEA2.Status_reg 0x02 # (bit n° 6 = 0 and bit 5 = 0)
2705 #
2706 #
2707 # SWITCH OFF THE SCAN
2708 . MEA2.Scan_OF
2709 WAIT_SEC 300
2710 #
2711 . # <CHECK> the HK
2712 CHECK MEA2.l1_cmd_exe =0x2400
2713 #
2714 #
2715 # SWITCH ON THE MCP SECURITY
2716 . MEA2.SECRTY_ON
2717 WAIT_SEC 300
2718 #
2719 . # <CHECK> the HK
2720 CHECK MEA2.l1_cmd_exe =0x1800
2721 CHECK MEA2.Status_reg 0x82 # (bit n° 8 = 1)
2722 #
2723 # QL Check-Item Spec Check
2724 # -----+-----+-----+-----+
2725 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2726 #
2727 # *****
2728 . # 4. MEA2 SHUTDOWN
2729 # *****
2730 #
2731 . PCD.ON_OFF_ENA
2732 WAIT_SEC 40
2733 #
2734 # < CHECK >
2735 CHECK PCD.ON_OFF_ENA_DIS =ENA
2736 #
2737 . MD1F.MEA2_CLCT_STOP
2738 WAIT_SEC 40
2739 #
2740 # < CHECK >
2741 CHECK MD1F.MS2_DAT_CLCT =OFF
2742 #
2743 . PCD.PSU_MEA2_OFF
2744 WAIT_SEC 40
2745 #
2746 . # < CHECK >
2747 CHECK PCD.PSU_MEA2_ON_OFF =OFF
2748 CHECK MD1F.MS2_LINK_STAT =OFF

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2749 #
2750 . PCD.ON_OFF_DIS
2751 WAIT_SEC 40
2752 #
2753 . # < CHECK >
2754 CHECK PCD.ON_OFF_ENA_DIS =DIS
2755 #
2756 #
2757 #
2758 #
2759 # QL          Check-Item          Spec          Check
2760 # -----+-----+-----+-----+
2761 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2762 #
2763 #
2764 # *****
2765 . # 5. 立ち下げ手順
2766 # *****
2767 # =====
2768 . # 5.1. MDP 立ち下げ
2769 # =====
2770 #
2771 . # <<< MDP-DPU RST/OFF >>>
2772 . DH.MDP_DPU12_CPU_OFF          # DH.MC_EACH_EXE 0x00B1
2773 #:          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
2774 #:          0x00 DH.ATMC_EACH_DIS 0x32
2775 #:          0x03 DH.ATMC_EACH_DIS 0x33
2776 #:          0x06 DH.ATMC_EACH_DIS 0x30
2777 #:          0x09 DH.ATMC_EACH_DIS 0x31
2778 #:          0x0A MD1F.CPU_RESET_ENA
2779 #:          0x0B MD1F.CPU_RESET
2780 #:          0x0C MD1F.CPU_OFF
2781 #:          0x0D MD2F.CPU_RESET_ENA
2782 #:          0x0E MD2F.CPU_RESET
2783 #:          0x0F MD2F.CPU_OFF
2784 #
2785 . # < CHECK >
2786 CHECK DH.ATMC_ENA_DIS30          =DIS
2787 CHECK DH.ATMC_ENA_DIS31          =DIS
2788 CHECK DH.ATMC_ENA_DIS32          =DIS
2789 CHECK DH.ATMC_ENA_DIS33          =DIS
2790 #
2791 . # QL          Check-Item          Spec          Check
2792 # -----+-----+-----+-----+
2793 # MD1F_OBS_HK  << DPU1 H/W-HK >>
2794 #              HW_STATE2          CPU_RST_ENA (short)          N/A
2795 #              HWCMD_CODE          0x0801 (short)          N/A
2796 #              HW_STATE2          STANDBY (short)          N/A
2797 #              HWCMD_CODE          0x0702 (short)          N/A
2798 #
2799 . # < CHECK >
2800 CHECK MD1F.DPU_HW_STATE2          =CPUOFF_DPUBUS

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2801 CHECK MD1F.DPU_CMD_CODE 0x0202
2802 #
2803 # QL Check-Item Spec Check
2804 # -----+-----+-----+-----
2805 # MD2F_OBS_HK << DPU2 H/W-HK >>
2806 # HW_STATE2 CPU_RST_ENA (short) N/A
2807 # HWCMD_CODE 0x0801 (short) N/A
2808 # HW_STATE2 STANDBY (short) N/A
2809 # HWCMD_CODE 0x0702 (short) N/A
2810 #
2811 # < CHECK >
2812 CHECK MD2F.DPU_HW_STATE2 =CPUOFF_DPUBUS
2813 CHECK MD2F.DPU_CMD_CODE 0x0202
2814 #
2815 . DH.MC_EACH_EXE 0x002C # 1553B TLM mode 0 (SYS HK 1)
2816 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
2817 #: 0x00 DH.DMP_PERIOD 0x04
2818 #: 0x04 ACS.ACS_PKT_FMT_1
2819 #: 0x05 DH.OPE_MODE 0x0C 0x0A 0x00 0x00 0x00000000000000000000000000000000
2820 #: 0x06 DH.TLMFMT1553B_CHG 0x00
2821 #
2822 # <CHECK>
2823 CHECK DH.DMP_PERIOD =SEC_16
2824 CHECK DH.CATEGORY_TBL_NO 0x00
2825 CHECK DH.DWNLNK_RATE =BPS16384
2826 CHECK DH.TLM1553BFMTNO 0x00
2827 #
2828 # <<< MDP-DPU1/2 Power OFF >>>
2829 . DH.MDP_DPU12_OFF # DH.MC_EACH_EXE 0x00B0
2830 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
2831 #: 0x00 DH.ATMC_EACH_DIS 0x34
2832 #: 0x01 DH.SPW_RTEFDIR_SET 0x18
2833 #: 0x03 DH.ATMC_EACH_DIS 0x36
2834 #: 0x06 DH.ATMC_EACH_DIS 0x32
2835 #: 0x09 DH.ATMC_EACH_DIS 0x33
2836 #: 0x0A PCD.ON_OFF_ENA
2837 #: 0x0B PCD.PSU_MDP1_OFF
2838 #: 0x0C DH.ATMC_EACH_DIS 0x30
2839 #: 0x0D PCD.PSU_MDP2_OFF
2840 #: 0x0E PCD.ON_OFF_DIS
2841 #: 0x0F DH.ATMC_EACH_DIS 0x31
2842 #
2843 # < CHECK >
2844 # CHECK DH.ATMC_ENA_DIS30 =DIS
2845 # CHECK DH.ATMC_ENA_DIS31 =DIS
2846 # CHECK DH.ATMC_ENA_DIS32 =DIS
2847 # CHECK DH.ATMC_ENA_DIS33 =DIS
2848 # CHECK DH.ATMC_ENA_DIS34 =DIS
2849 # CHECK DH.ATMC_ENA_DIS36 =DIS
2850 CHECK DH.SPW_FDIR_ENA_MDP1 =DIS
2851 CHECK DH.SPW_FDIR_ENA_MDP2 =DIS
2852 CHECK DH.SPW_FDIR_ENA_PCD =ENA

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2853 CHECK DH. SPW_FDIR_ENA_RMTRTR =ENA
2854 CHECK PCD. PSU_MDP1_ON_OFF =OFF
2855 CHECK PCD. PSU_MDP2_ON_OFF =OFF
2856 CHECK PCD. ON_OFF_ENA_DIS =DIS
2857 #
2858 . # QL Check-Item Spec Check
2859 # -----+-----+-----+-----
2860 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2861 #
2862 # =====
2863 . # 5.2. TCS 立ち下げ(HTR CTRL OFF/DRU_OFF)
2864 # =====
2865 . #
2866 . DH.MC_EACH_EXE 0x0032 # 1553B TLM mode 6 (SYS HK 1/2, BUS User HK 1/4, DUMP 1/4)
2867 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
2868 #: 0x00 DH. DMP_PERIOD 0x02
2869 #: 0x02 ACS. ACS_PKT_FMT_1
2870 #: 0x05 DH. OPE_MODE 0x0C 0x26 0x28 0x03 0x00000000000000000000000000000000
2871 #: 0x06 DH. TLMFMT1553B_CHG 0x06
2872 #
2873 WAIT_SEC 40
2874 CHECK DH. DMP_PERIOD =SEC_4
2875 CHECK DH. CATEGORY_TBL_NO 0x28
2876 CHECK DH. DWNLNK_RATE =BPS16384
2877 CHECK DH. TLM1553BFMTNO 0x06
2878 CHECK ACS. F_ACS_PKT_FMT =ACSHK1
2879 #
2880 . # < CHECK >
2881 CHECK DH. ATMC_ENA_DIS30 =DIS
2882 CHECK DH. ATMC_ENA_DIS31 =DIS
2883 CHECK DH. ATMC_ENA_DIS32 =DIS
2884 CHECK DH. ATMC_ENA_DIS33 =DIS
2885 CHECK DH. ATMC_ENA_DIS34 =DIS
2886 CHECK DH. ATMC_ENA_DIS35 =DIS
2887 CHECK DH. ATMC_ENA_DIS36 =DIS
2888 CHECK DH. ATMC_ENA_DIS37 =DIS
2889 . #
2890 . DH. ATMC_EACH_DIS 0x09 # //分離前 CPU リセット時 TCS モード再度 NMNL 制御
2891 #
2892 . # < CHECK >
2893 CHECK DH. ATMC_ENA_DIS09 =DIS
2894 . #
2895 . TCS. MODE_STBY
2896 #
2897 . # < CHECK >
2898 CHECK TCS. MODE_ST =STBY
2899 . #
2900 . TCS. CTRL_HTR_P1_DIS
2901 WAIT_SEC 2
2902 TCS. CTRL_HTR_P2_DIS
2903 WAIT_SEC 2
2904 TCS. CTRL_HTR_P3_DIS

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2905 WAIT_SEC 2
2906 TCS. CTRL_HTR_P4_DIS
2907 #
2908 . # < CHECK >
2909 CHECK TCS. HTR01_P_VOL_ST =OFF
2910 CHECK TCS. HTR09_P_VOL_ST =OFF
2911 CHECK TCS. HTR17_P_VOL_ST =OFF
2912 CHECK TCS. HTR25_P_VOL_ST =OFF
2913 . #
2914 . ACS. DRU_SEL_NA
2915 #
2916 . # < CHECK >
2917 CHECK ACS. DRU_SEL_PT =NA
2918 CHECK ACS. SYS. DRU_SEL_PT =NA
2919 CHECK ACS. BIT_DRU_SEL_PT =NA
2920 . #
2921 . PCD. ON_OFF_ENA
2922 #
2923 . # < CHECK >
2924 CHECK PCD. ON_OFF_ENA_DIS =ENA
2925 . #
2926 . PCD. DRU_PSU_A_OFF
2927 #
2928 . # < CHECK >
2929 CHECK PCD. DRU_PSU_A_ON_OFF =OFF
2930 . #
2931 . PCD. ON_OFF_DIS
2932 #
2933 . # < CHECK >
2934 CHECK PCD. ON_OFF_ENA_DIS =DIS
2935 #
2936 . # QL          Check-Item          Spec          Check
2937 # -----+-----+-----+-----+
2938 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
2939 #
2940 # =====
2941 . # 5.3. MC_DIS
2942 # =====
2943 . #
2944 . # << PCS 系個別 ATMC DIS >>
2945 . #
2946 . DH. ATMC_EACH_DIS 0x01 # // BAT UVC in Cruise mode
2947 . DH. ATMC_EACH_DIS 0x2B # //PCD NIC リセットモニタ
2948 #
2949 . # < CHECK >
2950 CHECK DH. ATMC_ENA_DIS01 =DIS
2951 CHECK DH. ATMC_ENA_DIS2B =DIS
2952 #
2953 . #
2954 . DH. ATMC_DIS
2955 #
2956 . # < CHECK >

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2957 CHECK DH. ATMC_ENA_DIS =DIS
2958 . #
2959 . DH. RQMC_DIS
2960 . #
2961 . # < CHECK >
2962 CHECK DH. RQMC_ENA_DIS =DIS
2963 . #
2964 . DH. SYSTM_R_DIS
2965 . #
2966 . # < CHECK >
2967 CHECK DH. SYSTM_R_ENA_DIS =DIS
2968 . #
2969 . DH. ALL_MC_STOP
2970 . #
2971 . # < CHECK >
2972 CHECK DH. MC_ENA_DIS =DIS
2973 . #
2974 . # QL Check-Item Spec Check
2975 . # -----+-----+-----+-----
2976 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2977 . #
2978 . # =====
2979 . # 5. 4. 衛星立ち下げ
2980 . # =====
2981 . #
2982 . # QL Check-Item Spec Check
2983 . # -----+-----+-----+-----
2984 . # PCS1 PCD. CHG_LR1_ON_OFF OFF G N
2985 . # PCD. CHG_LR2_ON_OFF OFF G N
2986 . # DMC-PSU-A, -B, XTRP-A, B, SV-Heater 以外が OFF であること G N
2987 . # TCIU DH. XTRP_A_TX_ON_OFF OFF G N
2988 . # DH. XTRP_B_TX_ON_OFF OFF G N
2989 . # 局管制 Uplink OFF G N
2990 . #
2991 . #
2992 . # 【確認】 Check
2993 . # BUS-V > BAT-V であることを確認する。 G N
2994 . #
2995 . PCD. INT_EXT_ENA
2996 . PCD. BAT_EXT
2997 . #
2998 . # < CHECK >
2999 CHECK PCD. INT_EXT_ENA_DIS =DIS
3000 CHECK PCD. BAT_INT_EXT =EXT
3001 . #
3002 . PCD. CHG_BYPS_DIS
3003 . #
3004 . # < CHECK >
3005 CHECK PCD. CHG_BYPS_ENA_DIS 255
3006 CHECK PCD. SA_V -
3007 CHECK PCD. SA_I -
3008 CHECK PCD. APR1_I -

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3009 CHECK PCD. APR2_I -
3010 CHECK PCD. BUS_V -
3011 CHECK PCD. LOAD_I -
3012 CHECK PCD. CHG_I -
3013 CHECK PCD. DCHG_I -
3014 CHECK PCD. BAT_V -
3015 CHECK PCD. CEL1_V -
3016 CHECK PCD. CEL2_V -
3017 CHECK PCD. CEL3_V -
3018 CHECK PCD. CEL4_V -
3019 CHECK PCD. CEL5_V -
3020 CHECK PCD. CEL6_V -
3021 CHECK PCD. CEL7_V -
3022 CHECK PCD. CEL8_V -
3023 CHECK PCD. CEL9_V -
3024 CHECK PCD. CEL10_V -
3025 CHECK PCD. CEL11_V -
3026 CHECK PCD. CEL12_V -
3027 CHECK PCD. BAT_CV -
3028 CHECK PCD. BAT_CV_ON_OFF -
3029 . #
3030 . PCD. CEL_V_MONI_OFF # 【BAT 管理手順書 (TPP-7S8238-03K) へ記録】
3031 . #
3032 . # < CHECK >
3033 CHECK PCD. CEL_V_MONI_ON_OFF =OFF
3034 CHECK PCD. BAT_CV -
3035 CHECK PCD. BAT_CV_ON_OFF -
3036 . #
3037 . # 【MIS コマンド操作】
3038 . # MIS 電源出力 A 系 OFF
3039 . # "SYSTEM Control → PFE → Modules[2] → PRI → ON → YES"
3040 . #
3041 . # =====
3042 . # 5.5. 試験コンフィギュレーションの解除
3043 . # =====
3044 . #
3045 . # 【操作】
3046 . # 下記手順に従い、MIS にて 1553B バスモニタを停止する。
3047 . # (1) 1553B Bus Monitor Main Menu: Mode → Stop Acquisition クリック
3048 . # (2) 1553B Bus Monitor Main Menu: Mode → Off-Line Mode クリック
3049 . # (3) 1553B Bus Monitor Main Menu: File → Exit → OK をクリック
3050 . #
3051 . # 【MIS(MPO Interface Simulator)】
3052 . # (1) System Control → MFE → Bus profile
3053 . # → Polling Sequence Table Control → stop
3054 . # (2) System Control → PFE → Power Control Bus A → ON をクリック → OFF
3055 . # (3) External Bus Power Supply の、DC VOLTS 表示=OFF を確認する
3056 . # (4) PFE Platform の Under Voltage の LED=点灯を確認する
3057 . # (5) Main Menu: Mode → System Off-Line Mode を選択する
3058 . # (6) Confirm → YES
3059 . # (7) Main Menu: File → Exit を選択する
3060 . # (8) Exit System 画面において、以下を選択し、OK ボタンをクリック

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3061 # Power FE : S/W Shutdown  
3062 # MIL-1553B FE : Shutdown  
3063 # Discrete FE : Shutdown  
3064 # CMDVS Core Environment : S/W Shutdown  
3065 # (9) MIS 本体の電源を以下の順に OFF する  
3066 # 1. MIL-1553/Discrete Front-End  
3067 # 2. PFE Platform  
3068 # 3. External Bus Power Supply (Agilent N5776A)  
3069 # (10) PC をシャットダウンする  
3070 #  
3071 . # 【テレメトリ I/F 変換装置】  
3072 # PC をシャットダウンする  
3073 #  
3074 . # 【コネクタの着脱】(引き続き電気試験がある場合は下記作業は実施しなくてもよい)  
3075 . # 以下のコネクタの取り外しを実施する。  
3076 . # なお、試験系接続を外した後、コネクタの外観検査を実施すること。  
3077 . #  
3078 . # 【コネクタの着脱】  
3079 . #  
3080 . # 本手順書で変更したコンフィギュレーションを戻す。  
3081 . #  
3082 . # 【衛星接地】  
3083 . # 試験コンフィギュレーション解除後、システムグラウンドの接地を 1MΩ に切替  
3084 # えること。  
3085 #  
3086 . # \*\*\*\*\*  
3087 . # END OF PROCEDURE  
3088 . # \*\*\*\*\*

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表 4-1 ソフトウェアバージョン管理表

| Component     | Software(オブジェクト)/Table | Version             | Program (ソース) | Revision |
|---------------|------------------------|---------------------|---------------|----------|
| DMC           | IPLソフトウェア              | 1.00.00             | IPL           | 77       |
|               | DMCソフトウェア              | 1.01.23             | PLTS          | 1850     |
|               |                        |                     | DHFS          | 1615     |
|               |                        |                     | ACFS          | 1553     |
|               |                        |                     | TCFS          | 1443     |
|               |                        |                     | T-Kernel      | 293      |
|               |                        |                     | T-Monitor     | 110      |
|               | MC-S_EEPROM            | 5.37                | -             | -        |
|               | MC-L_EEPROM            | 5.36                | -             | -        |
|               | ST_EEPROM              | 5.36                | -             | -        |
|               | AT_EEPROM              | 5.35                | -             | -        |
|               | RQ_EEPROM              | 5                   | -             | -        |
|               | CATEGORY_TABLE_EEPROM  | 3                   | -             | -        |
| MDP<br>(DPU1) | Middleware             | 2017/1/20(Ver. 6.2) | -             | -        |
|               | Application software   | 2017/1/20(Ver. 6.2) | -             | -        |
| MDP<br>(DPU2) | Middleware             | 2017/1/20(Ver. 6.2) | -             | -        |
|               | Application software   | 2017/1/20(Ver. 6.2) | -             | -        |
| MSA           | -                      | 2017/1/25(Ver. 35)  | -             | -        |
| MIA           | -                      | 2015/2/13           | -             | -        |
| MEA1          | -                      | 2012/12/19          | -             | -        |
| MEA2          | -                      | 2012/12/19          | -             | -        |

\*供試体がない場合はN/Aとすること。

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5      コンフィギュレーション

コネクタの接続については、実施者と非実施者によるダブルチェックを実施し記録を残すこと

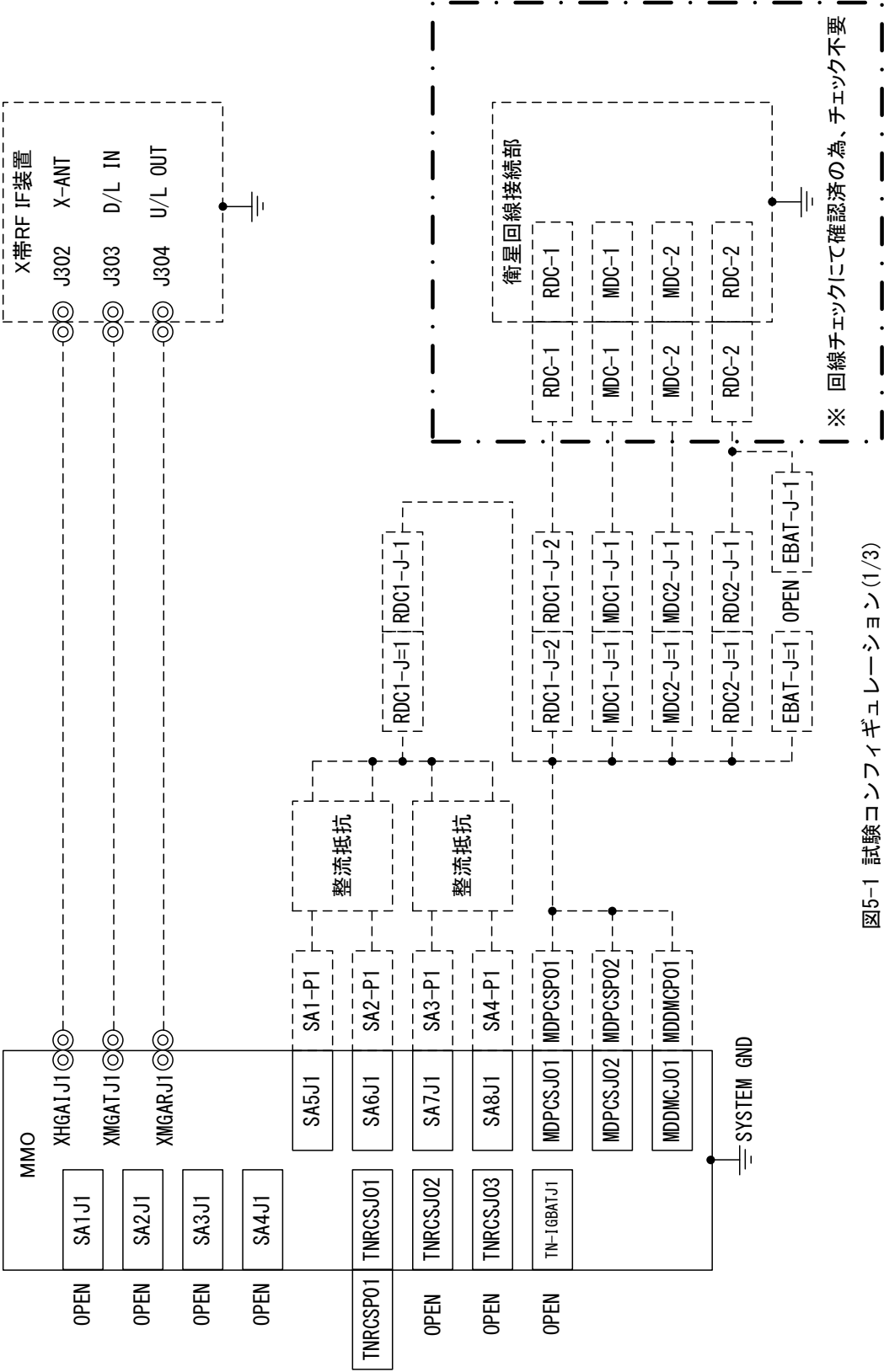
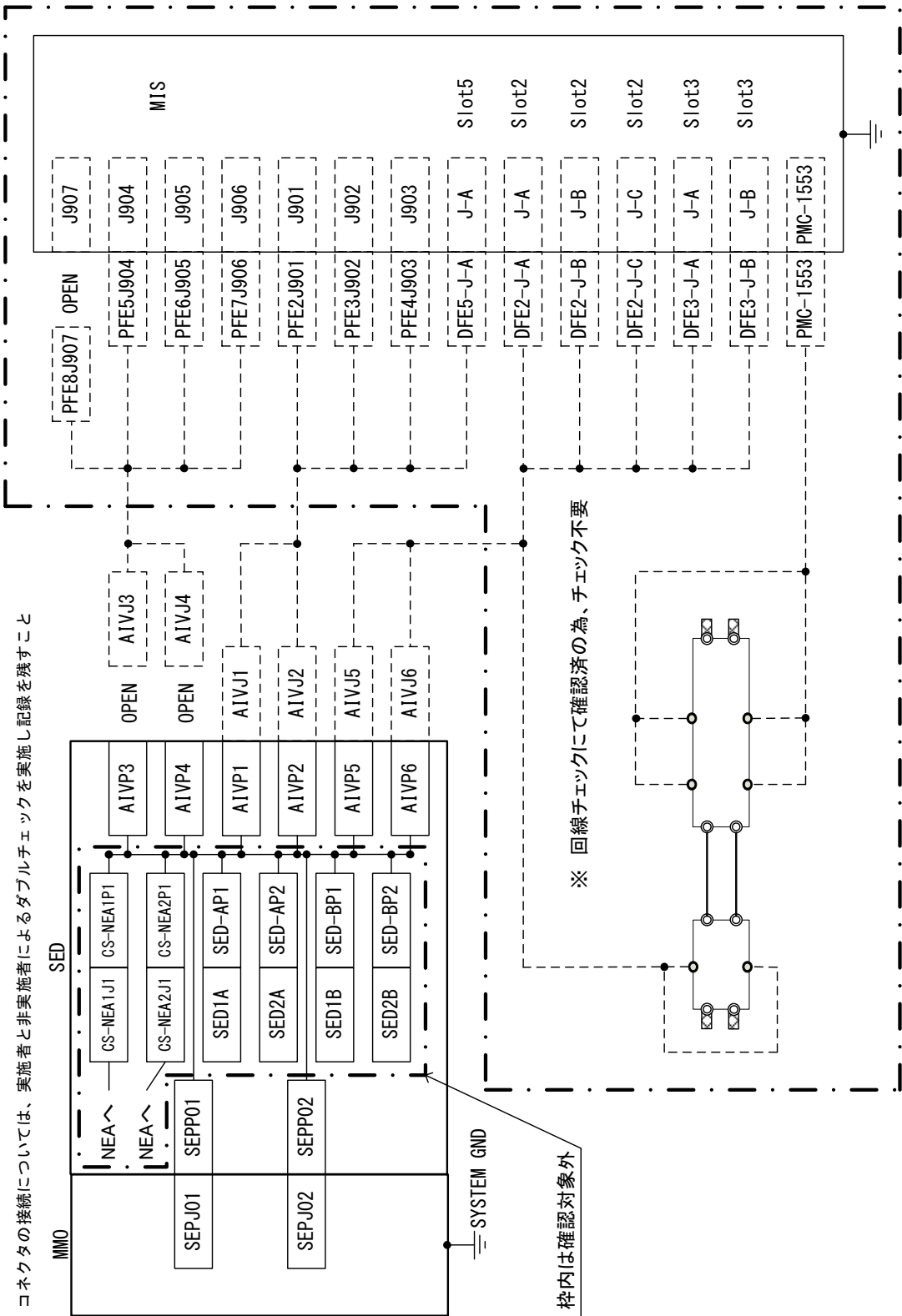


図5-1 試験コンフィギュレーション(1/3)



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コネクタの接続については、実施者と非実施者によるダブルチェックを実施し記録を残すこと

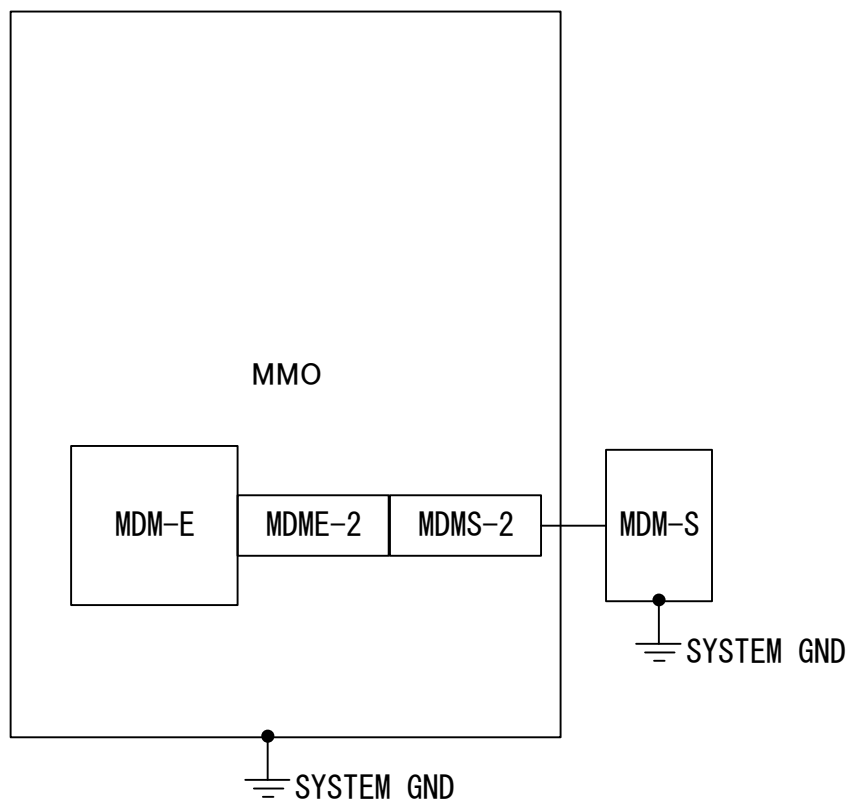


図5-1 試験コンフィギュレーション (3/3)

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表 5-1 コネクタ嵌合一覧(共通 I/F)

| IFコネクタ名称   | 嵌合○/<br>非嵌合× | 用途                                                      | 備考                   |
|------------|--------------|---------------------------------------------------------|----------------------|
| TNRCSJ01   | ○            | 圧力センサモニタ<br>HLV OPEN/CLOSEモニタ<br>タンク温度モニタ               | RCS圧力センサ確認のため嵌合      |
| TNRCSJ02   | ×            | HLVドライブ                                                 | 誤動作リスク低減のためOPEN      |
| TNRCSJ03   | ×            | THVドライブ                                                 | 誤動作リスク低減のためOPEN      |
| TN-IGBATJ1 | ×            | NEADドライブ(HGA/MGA)                                       | NEA誤動作リスクを低減するためOPEN |
| SA1J1      | ×            | SAP1電源入力                                                | 使用しないI/FのためOPEN      |
| SA2J1      | ×            | SAP2電源入力                                                | 使用しないI/FのためOPEN      |
| SA3J1      | ×            | SAP3電源入力                                                | 使用しないI/FのためOPEN      |
| SA4J1      | ×            | SAP4電源入力                                                | 使用しないI/FのためOPEN      |
| SA5J1      | ○            | SAP5電源入力                                                | EXT-PS入力のため地上試験装置と接続 |
| SA6J1      | ○            | SAP6電源入力                                                | EXT-PS入力のため地上試験装置と接続 |
| SA7J1      | ○            | SAP7電源入力                                                | EXT-PS入力のため地上試験装置と接続 |
| SA8J1      | ○            | SAP8電源入力                                                | EXT-PS入力のため地上試験装置と接続 |
| XHGAIJ1    | ○            | HGA TX/RX                                               | 終端のため試験装置と接続         |
| XMGATJ1    | ○            | MGA-TX TX                                               | 終端のため試験装置と接続         |
| XMGARJ1    | ○            | MGA RX                                                  | 終端のため試験装置と接続         |
| MDPCSJ01   | ○            | MD(電流/電圧値)                                              | モニタ用のため地上試験装置と接続     |
| MDPCSJ02   | ○            | MD(電流/AGC値)                                             | モニタ用のため地上試験装置と接続     |
| MDDMCJ01   | ○            | RD(CMD通信)                                               | RDによる通信のため地上試験装置と接続  |
| AIVP1      | ○            | 電源(28V/SEDヒーター)(主系)(CNV-A)<br>CNV PWR SWドライブ(主系)(CNV-A) | MIS電源使用のため嵌合         |
| AIVP2      | ○            | 電源(28V/SEDヒーター)(主系)(CNV-B)<br>CNV PWR SWドライブ(主系)(CNV-B) | MIS電源使用のため嵌合         |
| AIVP3      | ×            | NEA/Pyroドライブ(CSM/SED)(主系)                               | 該当ラインを使用しないためOPEN    |
| AIVP4      | ×            | NEA/Pyroドライブ(CSM/SED)(従系)                               | 該当ラインを使用しないためOPEN    |
| AIVP5      | ○            | 分離ステータス/温度モニタ(主系)<br>1553B通信(主系)                        | 1553B I/F使用のため嵌合     |
| AIVP6      | ○            | 分離ステータス/温度モニタ(従系)<br>1553B通信(従系)                        | 1553B I/F使用のため嵌合     |

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6      データシート

N/A

