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3.7 試験/作業項目 (試験フロー)

1. CNV 電源入力 / 1553B I/F 立ち上げ確認
 - 1.1. セットアップ
 - 1.2. 1553B バスモニタ開始
 - 1.2.3. 衛星電源立ち上げ (CNV 入力)
 - 1.2.4. CMD/TLM LINK 確認 (1553B I/F)
2. TCFS Table Load (@ MPO I/F)
 - 2.1. TCFS Table Load/Dump
 - 2.2. 立ち下げ
 - 2.2.1. 1553B BUS OFF
 - 2.2.2. MIS (MPO) BUS OFF
3. SETUP/衛星立ち上げ (EXT-PS 電源入力 / RD I/F)
 - 3.1. SETUP
 - 3.2. 電源系立ち上げ
 - 3.2.1. 衛星電源立ち上げ/初期チェック
 - 3.2.2. BAT INT/BUS SW ON
 - 3.2.3. K1/K2/CD/CV 設定
 - 3.3. インデックスパルス出力
 - 3.4. DR 設定
4. TCFS CHECK
 - 4.1. DRU 立ち上げ
 - 4.2. TCS Table Check
5. MDP Load/Dump
 - 5.1. MDP 立ち上げ
 - 5.2. MDP プログラム書き換え
 - 5.2.1. DPU1
 - 5.2.2. DPU2
 - 5.3. MDP 立ち下げ
6. MDP Check
 - 6.1. MDP 立ち上げ
 - 6.2. SI 機器立ち上げ
 - 6.2.1. PME 立ち上げ/初期化
 - 6.2.2. MGF 立ち上げ/初期化
 - 6.2.3. ENA 立ち上げ/初期化
 - 6.2.4. MEA1 立ち上げ/初期化
 - 6.2.5. MEA2 立ち上げ/初期化
 - 6.2.6. MSA 立ち上げ/初期化
 - 6.2.7. MIA 立ち上げ/初期化
 - 6.2.8. PWI 立ち上げ/初期化 [EWO, SORBET, AM2P, MEFISTO]
 - 6.2.9. MAST 立ち上げ/初期化
 - 6.2.10. MOTOR-PSU チェック
 - 6.2.12. MSASI 立ち上げ/初期化

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6. 4. HEP Check
 6. 4. 1. HEP-E 立ち上げ／初期化
 6. 4. 2. HEP-I 立ち上げ／初期化
 6. 4. 3. HEP-E L/M-Mode check
 6. 4. 4. HEP-I L/M-Mode check
 6. 4. 5. HEP-I 立ち下げ
 6. 4. 6. HEP-E 立ち下げ
 6. 5. MEA1 Check
 6. 5. 1. MEA1 function check
 6. 5. 1-1. MEA1 Startup

<<M15-1 Initial Power ON Duration:10 min Day:35>>
 6. 5. 1-2. SWITCH ON SCAN, 10 min
 6. 5. 1-3. TEST MODE ON, 10 min
 6. 5. 1-4. TEST MODE OFF, 5 min
 6. 5. 1-5. SWITCH OFF the SCAN (5 minutes)
 6. 5. 2. MEA1 SHUTDOWN
 6. 6. MEA2 Check
 6. 6. 1. MEA2 function check
 6. 6. 1-1. MEA2 Startup

<<M16-1 Initial Power ON Duration:10 min Day:35>>
 6. 6. 1-2. SWITCH ON SCAN, 10 min
 6. 6. 1-3. TEST MODE ON, 10 min
 6. 6. 1-4. TEST MODE OFF, 10 min
 6. 6. 1-5. SWITCH OFF the SCAN (5 minutes)
 6. 6. 2. MEA2 SHUTDOWN
 6. 7 DPU2 伸展 HK ソフト動作確認
 6. 7. 1. MDP 伸展 HK 生成ソフト起動 (extension mode)
 6. 7. 2. 伸展 HK ソフト停止 (exit extension mode)
7. 立ち下げ
 7. 1. SI 機器立ち下げ
 7. 1. 1. MSASI 立ち下げ
 7. 1. 2. MDM 立ち下げ
 7. 1. 3. MGF 立ち下げ
 7. 1. 4. MAST 立ち下げ
 7. 1. 5. PWI 立ち下げ
 7. 1. 6. PME 立ち下げ
 7. 1. 7. MIA 立ち下げ
 7. 1. 8. MSA 立ち下げ
 7. 1. 9. MEA2 立ち下げ
 7. 1. 10. MEA1 立ち下げ
 7. 1. 11. ENA 立ち下げ
 7. 2. MDP 立ち下げ
 7. 3. 衛星立ち下げ
 7. 3. 1. MC_DIS
 7. 3. 2. 衛星立ち下げ
 7. 4. CNV-A/B SV-HTR 選択
 7. 5. MIS 立ち下げ
 7. 6. 試験コンフィギュレーションの解除

[illegible]

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0050  #
0051  # 【MIS(MPO Interface Simulator)】
0052  # 下記手順により立ち上げる。
0053  # (1)PC の電源 ON
0054  # (2)PC にログインする
0055  #     ID = CMDVS_user
0056  #     Password = CMDVS
0057  # (3)MIS 本体の電源を、以下の順に ON する
0058  #     (a)External Bus Power Supply
0059  #     (b)PFE Platform
0060  #     (c)MIL-1553/Discrete Front-End
0061  # (4)1 分待つ
0062  # (5)PC で、Start CMDVS アイコンをダブルクリック
0063  #     (CMDVS ソフトウェアが起動する)
0064  # (6)Main Menu: Mode -> System On-Line Mode を選択
0065  #     (PFE の、System Enabled の LED が点灯)
0066  # (7)Main Menu: Control -> System Control を選択
0067  # (8)System Control 画面の FE status Storage selection において
0068  #     ☐ PFE
0069  #     ☐ MFE
0070  #     ☐ DFE
0071  #     をクリックする。チェックマークが表示される
0072  # (9)Enabled Selected Categories ボタンをクリック
0073  #     (ログ取得開始)
0074  # (10)System Control 画面で“CCI”をクリック
0075  # (11)Interface Management:
0076  #     Open Interface
0077  #     TM Distribution: Enable
0078  #     TC Handling: Enable
0079  #     をクリック
0080  # (12)I/F 変換装置の Operation Log 画面で以下のメッセージを確認する
0081  #     Socket opened CCS/MIS-EGSE 3000
0082  #     Socket opened CCS/MIS-EGSE 3001
0083  # (13)System Control 画面で“MFE”をクリック
0084  # (14)Bus profile ボタンをクリック
0085  # (15)Polling Sequence Table Selection and Verification において
0086  #     ファイル BCMIS_MMO PST defs\BCMIS_MMO.PST を選択し、Verify ボタンをクリック
0087  #     →load ボタンをクリック
0088  # (16)Polling Sequence Table Control において、start ボタンをクリック
0089  #     (1553B バスへの Sync メッセージ出力開始)
0090  # (17)System Control 画面で“PFE”をクリック
0091  # (18)Power Control Bus A の“OFF”をクリック
0092  #     “OFF”→“ON”になる
0093  # (19)External Bus Power Supply の、DC VOLTS 表示 = 28.00 を確認する
0094  # (20)PFE Platform の、Under Voltage の LED＝消灯を確認する
0095  #
0096  # 【共通 QL 設定】
0097  # MPO_RL_X 選択
0098  #
0099  # =====
0100  # 1.2. 立ち上げ(CNV 入力)
0101  # =====

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0102 . # -----
0103 . # 1.2.1. CNV-A/B MAIN 選択
0104 . # -----
0105 . #
0106 . # 【操作】
0107 . # 下記手順に従い、MIS にて “HPC_Command2 (MIS 出力 ch37)” 及び “HPC_Command3
0108 . # (MIS 出力 ch39)” を出力し、CNV-A/B の MODE を “MAIN” へ切り替える。
0109 . #
0110 . #
0111 . # (1) SYSTEM Control 画面の DFE 画面を開く。
0112 . # (2) IO Activation Control の IO Number に ch37 を入力し、Activate ボタンを押下する
0113 . # ディスクリットコマンドのパルスが出力される。
0114 . # (3) IO Activation Control の IO Number に ch39 を入力し、Activate ボタンを押下する
0115 . # ディスクリットコマンドのパルスが出力される。
0116 . # (4) 以下の流れでコマンド送信後のステータスを確認する。
0117 . # (a) Main Menu: View -> TM/TC Data Monitor
0118 . # (b) TM/TC Data Monitor 画面の、FE Data Filter で、“DFE TM Packet” をクリック
0119 . # チェックマークがつく
0120 . # (c) SYSTEM Control 画面の DFE をクリック
0121 . # (d) Acquisition Control の START ボタンをクリック
0122 . #
0123 . # MIS          Check-Item          Spec          Check
0124 . # -----+-----+-----+-----
0125 . # Relay      CNV-A MAIN (RLY6)      SHORT:32767 (dec) 以下  G  N
0126 . # status     CNV-A HTR (RLY7)      OPEN :32768 (dec) 以上  G  N
0127 . #           CNV-B MAIN (RLY13)     SHORT:32767 (dec) 以下  G  N
0128 . #           CNV-B HTR (RLY14)     OPEN :32768 (dec) 以上  G  N
0129 . #
0130 . # -----
0131 . # 1.2.2. 1553B バスモニタ開始
0132 . # -----
0133 . #
0134 . # 【操作】
0135 . # 下記手順に従い、MIS にて 1553B バスモニタを開始する。
0136 . # (1) start 1553 BM client アイコンをダブルクリック
0137 . # Bus Monitor が起動する
0138 . # (2) Main Menu: Mode -> On-Line Mode クリック
0139 . # (3) Main Menu: Mode -> Start New Acquisition クリック
0140 . #
0141 . # -----
0142 . # 1.2.3. 衛星電源立ち上げ (CNV 入力)
0143 . # -----
0144 . #
0145 . # 【操作】
0146 . # 下記手順により MIS 電源出力 A 系 ON (MAIN) する。
0147 . #
0148 . # (1) SYSTEM Control 画面の PFE 画面で、Modules[2] (主系 PRI) を選択する
0149 . # (2) “OFF” をクリック
0150 . # (3) Confirm 画面で、“YES” をクリック
0151 . # “OFF” → “ON” になる
0152 . #
0153 . # -----

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0154 . # 1.2.4. CMD/TLM LINK 確認(1553B I/F)
0155 . # -----
0156 . #
0157 . DH. OPE_MODE 0x0D 0x00 0x23 0x01 0x01 0x01 0x01 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00
0x00 0x00
0158 . DH. TLMFMT1553B_CHG 0x06
0159 . #
0160 . # < CHECK >
0161 . CHECK DH. TLM1553BFMTNO 0x06
0162 . CHECK DH. CATEGORY_TBL_NO 0x23
0163 . CHECK DH. DWNLNK_RATE =BPS32768
0164 . CHECK DH. ATMC_FMT_ERR =OK
0165 . CHECK DH. ATMC_SUM_STAT =OK
0166 . CHECK DH. MC_FMT_ERR =OK
0167 . CHECK DH. MC_SUM_STAT =OK
0168 . CHECK DH. RQMC_FMT_ERR =OK
0169 . CHECK DH. RQMC_SUM_STAT =OK
0170 . CHECK DH. SYSTM_R_FMT_ERR =OK
0171 . CHECK DH. SYSTM_R_SUM_STAT =OK
0172 . CHECK DH. TL_FMT_ERR =OK
0173 . CHECK DH. TL_SUM_STAT =OK
0174 . CHECK DH. CATGRY_SUM_STAT =OK
0175 . CHECK DH. TM1TBL_SUM_STAT =OK
0176 . CHECK DH. TM2TBL_SUM_STAT =OK
0177 . CHECK DH. ONOFKY_SUMERR =OK
0178 . CHECK DH. CPU_CMD_RESET =NON
0179 . CHECK DH. CPUA_ECC_2BE =NON
0180 . CHECK DH. CPUA_SW_RST =NON
0181 . CHECK DH. CPUA_WDT_OVF =NON
0182 . CHECK DH. CPUB_ECC_2BE =NON
0183 . CHECK DH. CPUB_SW_RST =NON
0184 . CHECK DH. CPUB_WDT_OVF =NON
0185 . CHECK DH. CPU_AB =CPU_A
0186 . CHECK DH. HK_ACF_S_VALID =ENA
0187 . CHECK DH. HK_DHFS_VALID =ENA
0188 . CHECK DH. HK_SPW_VALID =ENA
0189 . CHECK DH. HK_TCF_S_VALID =ENA
0190 . CHECK DH. NML_IPL =NML
0191 . CHECK DH. TC1U_ECC_2BE =NON
0192 . CHECK DH. DR_SUM_STAT =OK
0193 . CHECK PCD. APR1_ON_OFF =OFF
0194 . #
0195 . # < CHECK >
0196 . CHECK DH. ENA_TEMP -
0197 . CHECK DH. TANK_TEMP -
0198 . CHECK DH. ADM_TEMP -
0199 . CHECK DH. APM_TEMP -
0200 . CHECK DH. UP_PNL2_TEMP -
0201 . CHECK DH. UP_PNL3_TEMP -
0202 . CHECK DH. UP_PNL6_TEMP -
0203 . CHECK DH. UP_PNL7_TEMP -
0204 . CHECK DH. UP_PNL8_TEMP -

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0205 CHECK DH. LO_PNL1_TEMP -
0206 CHECK DH. LO_PNL2_TEMP -
0207 CHECK DH. LO_PNL5_TEMP -
0208 CHECK DH. LO_PNL6_TEMP -
0209 CHECK DH. LO_PNL8_TEMP -
0210 CHECK DH. SSAS_TEMP -
0211 CHECK DH. THR_T1T2_TEMP -
0212 CHECK DH. SSC_TEMP1 -
0213 CHECK DH. ACM1_TEMP -
0214 CHECK DH. THR_T3T4_TEMP -
0215 CHECK DH. ACM2_TEMP -
0216 CHECK DH. THR_A1_TEMP -
0217 CHECK DH. THR_A2_TEMP -
0218 CHECK DH. VLV_M_TEMP -
0219 CHECK DH. EPC_A_TEMP -
0220 CHECK DH. EPC_B_TEMP -
0221 CHECK DH. TWT_A_TEMP -
0222 CHECK DH. TWT_B_TEMP -
0223 CHECK DH. XTRP_A_TEMP -
0224 CHECK DH. XTRP_B_TEMP -
0225 CHECK DH. BAT_TEMP1 -
0226 CHECK DH. BAT_TEMP2 -
0227 CHECK DH. SSC_TEMP2 -
0228 CHECK DH. SIDE1_TEMP -
0229 CHECK DH. SIDE2_TEMP -
0230 CHECK DH. SIDE3_TEMP -
0231 CHECK DH. SIDE4_TEMP -
0232 CHECK DH. UP_PNL1_TEMP -
0233 CHECK DH. UP_PNL5_TEMP -
0234 CHECK DH. MAST_MGF_TEMP -
0235 CHECK DH. LO_PNL3_TEMP -
0236 CHECK DH. LO_PNL7_TEMP -
0237 CHECK DH. SIDE5_TEMP -
0238 CHECK DH. SIDE6_TEMP -
0239 CHECK DH. SIDE7_TEMP -
0240 CHECK DH. SIDE8_TEMP -
0241 #
0242 . #
0243 . DH. MC_START
0244 #
0245 . # < CHECK >
0246 CHECK DH. MC_ENA_DIS =ENA
0247 CHECK PCD. ON_OFF_ENA_DIS =DIS
0248 CHECK PCD. SA_V 86 94
0249 CHECK PCD. BUS_V 48 52
0250 #
0251 . # QL Check-Item Spec Check
0252 . # -----+-----+-----+-----
0253 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0254 . #
0255 #
0256 . # *****

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0257 . # 2. TCFS Table Load(@ MPO I/F)
0258 . # *****
0259 . # =====
0260 . # 2.1. TCFS Table Load/Dump
0261 . # =====
0262 . #
0263 . LOAD ram-030      :TCFS TABLE ROM 最適化 ON # Ver. 2.3
0264 . #
0265 . #
0266 . # < TCFS TABLE DUMP CHECK >
0267 . DH.MEM_DUMP 0x00 0x01 0xB0B60000 0x000600
0268 . #
0269 . # QL          Check-Item          Spec          Check
0270 . # -----+-----+-----+-----
0271 . #              照合結果              OK              G  N
0272 . #
0273 . # =====
0274 . # 2.2. 立ち下げ
0275 . # =====
0276 . #
0277 . # QL          Check-Item          Spec          Check
0278 . # -----+-----+-----+-----
0279 . # PCS1        PCD. CHG_LR1_ON_OFF    OFF              G  N
0280 . #              PCD. CHG_LR2_ON_OFF    OFF              G  N
0281 . #              DMC-PSU-A, -B, XTRP-A, B, SV-Heater 以外が OFF であること    G  N
0282 . # TCIU        DH. XTRP_A_TX_ON_OFF    OFF              G  N
0283 . #              DH. XTRP_B_TX_ON_OFF    OFF              G  N
0284 . # 局管制      Uplink                  OFF              G  N
0285 . #
0286 . # < CHECK >
0287 . CHECK PCD. SA_V              -
0288 . CHECK PCD. SA_I              -
0289 . CHECK PCD. APR1_I            -
0290 . CHECK PCD. APR2_I            -
0291 . CHECK PCD. BUS_V              -
0292 . CHECK PCD. LOAD_I            -
0293 . #
0294 . # QL          Check-Item          Spec          Check
0295 . # -----+-----+-----+-----
0296 . # CMD 出力装置  Message          STATUS CHECK 結果 OK    G  N
0297 . #
0298 . # -----
0299 . # 2.2.1. 1553B BUS OFF
0300 . # -----
0301 . #
0302 . # 【操作】
0303 . # 下記手順に従い、MIS にて 1553B バスモニタを停止する。
0304 . # (1)1553B Bus Monitor Main Menu: Mode -> Stop Acquisition クリック
0305 . # (2)1553B Bus Monitor Main Menu: Mode -> Off-Line Mode クリック
0306 . # (3)1553B Bus Monitor Main Menu: File -> Exit -> OK をクリック
0307 . #
0308 . # -----

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0309 . # 2.2.2. MIS(MPO) BUS OFF
0310 . # -----
0311 . #
0312 . # 【操作】
0313 . # 下記手順により MIS 電源出力 A 系 OFF (MAIN) する。
0314 . # (1)SYSTEM Control 画面の PFE 画面で、Modules[2] (主系 PRI) を選択する
0315 . # (2)“ON”をクリック
0316 . # (3)Confirm 画面で、“YES”をクリック
0317 . # “ON”→“OFF”になる
0318 . #
0319 . # *****
0320 . # 3. SETUP/衛星立ち上げ(EXT-PS 電源入力 / RD I/F)
0321 . # *****
0322 . # =====
0323 . # 3.1. SETUP
0324 . # =====
0325 . #
0326 . # 【局管制 設定】
0327 . # 初期設定 : INIT_MMO_RF -> INIT_MMO_RD
0328 . # 設備状況 : 正常動作(緑色表示)
0329 . #
0330 . # 【衛星管制 設定】
0331 . # 接続 CMD : cccmd2
0332 . # 接続 TLM : cctlm2
0333 . # プリチェック : OFF
0334 . # センドベリファイ : 強制 Type-B
0335 . # サクセスベリファイ: OFF
0336 . # RF/LINE 切り替え : LINE
0337 . #
0338 . # 【地上装置設定】
0339 . # EXT-PS CV: 90.0V
0340 . # CC: 6.0A
0341 . # OVP 54.0V
0342 . # UVP 32.0V
0343 . #
0344 . # 【共通 QL 設定】
0345 . # MMO_RL_X_Rel 選択
0346 . #
0347 . # =====
0348 . # 3.2. 電源系立ち上げ
0349 . # =====
0350 . # -----
0351 . # 3.2.1. 衛星電源立ち上げ/初期チェック
0352 . # -----
0353 . #
0354 . # 【操作】
0355 . # EXT-PS ON
0356 . # RD LINK ON
0357 . # BAT_MEASURE START (BAT 温度計測開始)
0358 . #
0359 . # テレメトリ 1553B → TCIM 変更
0360 . # DH.TLM_REATCIMSEL

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0361 WAIT_SEC 2
0362 # Operation mode change (HK mode: Real 32kbps)
0363 # RATE CHK CATE DHFS
0364 DH. OPE_MODE 0x0D 0x4F 0x23 0x00 0x0000000000000000000000000000
0365 #
0366 # < CHECK >
0367 CHECK DH. CATEGORY_TBL_NO 0x23
0368 CHECK DH. DWNLNK_RATE =BPS32768
0369 CHECK DH. ATMC_FMT_ERR =OK
0370 CHECK DH. ATMC_SUM_STAT =OK
0371 CHECK DH. MC_FMT_ERR =OK
0372 CHECK DH. MC_SUM_STAT =OK
0373 CHECK DH. RQMC_FMT_ERR =OK
0374 CHECK DH. RQMC_SUM_STAT =OK
0375 CHECK DH. SYSTM_R_FMT_ERR =OK
0376 CHECK DH. SYSTM_R_SUM_STAT =OK
0377 CHECK DH. TL_FMT_ERR =OK
0378 CHECK DH. TL_SUM_STAT =OK
0379 CHECK DH. CATGRY_SUM_STAT =OK
0380 CHECK DH. TM1TBL_SUM_STAT =OK
0381 CHECK DH. TM2TBL_SUM_STAT =OK
0382 CHECK DH. ONOFKY_SUMERR =OK
0383 CHECK DH. CPU_CMD_RESET =NON
0384 CHECK DH. CPUA_ECC_2BE =NON
0385 CHECK DH. CPUA_SW_RST =NON
0386 CHECK DH. CPUA_WDT_OVF =NON
0387 CHECK DH. CPUB_ECC_2BE =NON
0388 CHECK DH. CPUB_SW_RST =NON
0389 CHECK DH. CPUB_WDT_OVF =NON
0390 CHECK DH. CPU_AB =CPU_A
0391 CHECK DH. HK_ACFS_VALID =ENA
0392 CHECK DH. HK_DHFS_VALID =ENA
0393 CHECK DH. HK_SPW_VALID =ENA
0394 CHECK DH. HK_TCFS_VALID =ENA
0395 CHECK DH. NML_IPL =NML
0396 CHECK DH. TC1U_ECC_2BE =NON
0397 CHECK DH. DR_SUM_STAT =OK
0398 CHECK PCD. APR1_ON_OFF =OFF
0399 CHECK PCD. APR2_ON_OFF =ON
0400 #
0401 # < CHECK >
0402 CHECK DH. ENA_TEMP -
0403 CHECK DH. TANK_TEMP -
0404 CHECK DH. ADM_TEMP -
0405 CHECK DH. APM_TEMP -
0406 CHECK DH. UP_PNL2_TEMP -
0407 CHECK DH. UP_PNL3_TEMP -
0408 CHECK DH. UP_PNL6_TEMP -
0409 CHECK DH. UP_PNL7_TEMP -
0410 CHECK DH. UP_PNL8_TEMP -
0411 CHECK DH. LO_PNL1_TEMP -
0412 CHECK DH. LO_PNL2_TEMP -

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0413	CHECK DH. LO_PNL5_TEMP	-
0414	CHECK DH. LO_PNL6_TEMP	-
0415	CHECK DH. LO_PNL8_TEMP	-
0416	CHECK DH. SSAS_TEMP	-
0417	CHECK DH. THR_T1T2_TEMP	-
0418	CHECK DH. SSC_TEMP1	-
0419	CHECK DH. ACM1_TEMP	-
0420	CHECK DH. THR_T3T4_TEMP	-
0421	CHECK DH. ACM2_TEMP	-
0422	CHECK DH. THR_A1_TEMP	-
0423	CHECK DH. THR_A2_TEMP	-
0424	CHECK DH. VLV_M_TEMP	-
0425	CHECK DH. EPC_A_TEMP	-
0426	CHECK DH. EPC_B_TEMP	-
0427	CHECK DH. TWT_A_TEMP	-
0428	CHECK DH. TWT_B_TEMP	-
0429	CHECK DH. XTRP_A_TEMP	-
0430	CHECK DH. XTRP_B_TEMP	-
0431	CHECK DH. BAT_TEMP1	-
0432	CHECK DH. BAT_TEMP2	-
0433	CHECK DH. SSC_TEMP2	-
0434	CHECK DH. SIDE1_TEMP	-
0435	CHECK DH. SIDE2_TEMP	-
0436	CHECK DH. SIDE3_TEMP	-
0437	CHECK DH. SIDE4_TEMP	-
0438	CHECK DH. UP_PNL1_TEMP	-
0439	CHECK DH. UP_PNL5_TEMP	-
0440	CHECK DH. MAST_MGF_TEMP	-
0441	CHECK DH. LO_PNL3_TEMP	-
0442	CHECK DH. LO_PNL7_TEMP	-
0443	CHECK DH. SIDE5_TEMP	-
0444	CHECK DH. SIDE6_TEMP	-
0445	CHECK DH. SIDE7_TEMP	-
0446	CHECK DH. SIDE8_TEMP	-
0447	#	
0448	. # < TI_PKT_REQ >	
0449	. DH. TI_PKT_REQ	
0450	DH. TI_PKT_REQ	
0451	WAIT_SEC 1	
0452	DH. MC_START	
0453	. #	
0454	. # < CHECK >	
0455	CHECK DH. MC_ENA_DIS	=ENA
0456	CHECK PCD. ON_OFF_ENA_DIS	=DIS
0457	CHECK PCD. SA_V	86 94
0458	CHECK PCD. BUS_V	48 52
0459	#	
0460	. # 【衛星管制設定】	
0461	# プリチェック	: ON
0462	# センドベリファイ	: ON
0463	# サクセスベリファイ	: ON
0464	. # COP N(S)値設定	

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0465 #
0466 # QL          Check-Item          Spec          Check
0467 # -----+-----+-----+-----
0468 # CMD 出力装置  Message          STATUS CHECK 結果 OK    G    N
0469 #
0470 # -----
0471 # 3.2.2. BAT INT/BUS SW ON
0472 # -----
0473 #
0474 # PCD.CEL_V_MONI_ON          # 【BAT 管理手順書 (TPP-7S8238-03K) へ記録】
0475 # WAIT_SEC 1
0476 # PCD.BAT_OT_DIS
0477 # WAIT_SEC 1
0478 # PCD.BAT_OT_ON_SET 0xBD    # 28degC
0479 # WAIT_SEC 1
0480 # DH.BAT_SM_EXE          # DH.MC_EACH_EXE 0x0054
0481 #:          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
0482 #:          0x00 PCD.BAT_SM_EXE
0483 #:          0x02 PCD.MGA_P_DIS
0484 # WAIT_SEC 1
0485 # PCD.BAT_OT_OFF_SET 0xC5   # 26degC
0486 # WAIT_SEC 1
0487 # DH.BAT_SM_EXE          # DH.MC_EACH_EXE 0x0054
0488 #:          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
0489 #:          0x00 PCD.BAT_SM_EXE
0490 #:          0x02 PCD.MGA_P_DIS
0491 #
0492 # < CHECK >
0493 # CHECK PCD.CEL1_V          >3.705
0494 # CHECK PCD.CEL2_V          >3.705
0495 # CHECK PCD.CEL3_V          >3.705
0496 # CHECK PCD.CEL4_V          >3.705
0497 # CHECK PCD.CEL5_V          >3.705
0498 # CHECK PCD.CEL6_V          >3.705
0499 # CHECK PCD.CEL7_V          >3.705
0500 # CHECK PCD.CEL8_V          >3.705
0501 # CHECK PCD.CEL9_V          >3.705
0502 # CHECK PCD.CEL10_V         >3.705
0503 # CHECK PCD.CEL11_V         >3.705
0504 # CHECK PCD.CEL12_V         >3.705
0505 # CHECK PCD.BAT_INT_EXT     =EXT
0506 # CHECK PCD.BUS_SW_ON_OFF   =OFF
0507 # CHECK PCD.BAT_OT_ENA_DIS   =DIS
0508 #
0509 # CHECK PCD.BAT_V            -
0510 # CHECK PCD.CEL1_V          -
0511 # CHECK PCD.CEL2_V          -
0512 # CHECK PCD.CEL3_V          -
0513 # CHECK PCD.CEL4_V          -
0514 # CHECK PCD.CEL5_V          -
0515 # CHECK PCD.CEL6_V          -
0516 # CHECK PCD.CEL7_V          -

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0517	CHECK PCD.CEL8_V	-	
0518	CHECK PCD.CEL9_V	-	
0519	CHECK PCD.CEL10_V	-	
0520	CHECK PCD.CEL11_V	-	
0521	CHECK PCD.CEL12_V	-	
0522	#		
0523	. # < CHECK >		
0524	CHECK PCD.OT_ON_SET	27.5 28.5	
0525	CHECK PCD.OT_OFF_SET	25.5 26.5	
0526	. #		
0527	. PCD.BAT_OT_ENA		
0528	#		
0529	. # < CHECK >		
0530	CHECK PCD.BAT_OT_ENA_DIS	=ENA	
0531	CHECK PCD.BAT_OT_ON_OFF	=OFF	
0532	#		
0533	. # 【確認】		Check
0534	. # BUS-V > BAT-V であることを確認する。		G N
0535	. #		
0536	. PCD.INT_EXT_ENA		
0537	. PCD.BAT_INT		
0538	#		
0539	. # < CHECK >		
0540	CHECK PCD.INT_EXT_ENA_DIS	=DIS	
0541	CHECK PCD.BAT_INT_EXT	=INT	
0542	CHECK PCD.BUS_SW_ENA_DIS	=DIS	
0543	CHECK PCD.BUS_SW_ON_OFF	=OFF	
0544	CHECK PCD.CHG_I	-0.1 0.1	
0545	CHECK PCD.DCHG_I	-0.2 0.2	
0546	. #		
0547	. PCD.CHG_LR1_ON		
0548	#		
0549	. # < CHECK >		
0550	CHECK PCD.CHG_LR1_ON_OFF	=ON	
0551	CHECK PCD.CHG_LR2_ON_OFF	=OFF	
0552	CHECK PCD.CHG_I	0.1 0.3	
0553	CHECK PCD.DCHG_I	-0.2 0.2	
0554	. #		
0555	. PCD.CHG_ENA		
0556	#		
0557	. # < CHECK >		
0558	CHECK PCD.CHG_ENA_DIS	=ENA	
0559	. #		
0560	. PCD.CHG_CC_L0		
0561	#		
0562	. # < CHECK >		
0563	CHECK PCD.CCCV_MODE	=L0	
0564	. #		
0565	. PCD.CHG_DIS		
0566	#		
0567	. # < CHECK >		
0568	CHECK PCD.CHG_ENA_DIS	=DIS	

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0569	CHECK PCD. CHG_I	-0.1 0.1		
0570	CHECK PCD. DCHG_I	-0.2 0.2		
0571	#			
0572	# 【確認】			Check
0573	# BUS-V と BAT-V の電位差が 2V 以下であることを確認する。			G N
0574	#			
0575	PCD. BUS_SW_ENA			
0576	PCD. BUS_SW_ON			
0577	#			
0578	# < CHECK >			
0579	CHECK PCD. INT_EXT_ENA_DIS	=DIS		
0580	CHECK PCD. BAT_INT_EXT	=INT		
0581	CHECK PCD. BUS_SW_ENA_DIS	=DIS		
0582	CHECK PCD. BUS_SW_ON_OFF	=ON		
0583	#			
0584	PCD. CHG_LR_OFF			
0585	#			
0586	# < CHECK >			
0587	CHECK PCD. CHG_LR1_ON_OFF	=OFF		
0588	CHECK PCD. CHG_LR2_ON_OFF	=OFF		
0589	#			
0590	# QL	Check-Item	Spec	Check
0591	#			
0592	# CMD 出力装置	Message	STATUS CHECK 結果 OK	G N
0593	#			
0594	#			
0595	# 3.2.3. K1/K2/CD/CV 設定			
0596	#			
0597	#			
0598	PCD. BAT_K1_SET 0xA7	# K1 167		
0599	WAIT_SEC 1			
0600	DH. BAT_SM_EXE	# DH. MC_EACH_EXE 0x0054		
0601	#:	<< MACRO >> 内容について以下に示す (DH. DUMMY は省略)		
0602	#:	0x00 PCD. BAT_SM_EXE		
0603	#:	0x02 PCD. MGA_P_DIS		
0604	WAIT_SEC 1			
0605	PCD. BAT_K2_SET 0xA5	# K2 165		
0606	WAIT_SEC 1			
0607	DH. BAT_SM_EXE	# DH. MC_EACH_EXE 0x0054		
0608	#:	<< MACRO >> 内容について以下に示す (DH. DUMMY は省略)		
0609	#:	0x00 PCD. BAT_SM_EXE		
0610	#:	0x02 PCD. MGA_P_DIS		
0611	WAIT_SEC 1			
0612	PCD. BAT_CD_SET 0x80	# 10. 56Ah		
0613	WAIT_SEC 1			
0614	DH. BAT_SM_EXE	# DH. MC_EACH_EXE 0x0054		
0615	#:	<< MACRO >> 内容について以下に示す (DH. DUMMY は省略)		
0616	#:	0x00 PCD. BAT_SM_EXE		
0617	#:	0x02 PCD. MGA_P_DIS		
0618	WAIT_SEC 1			
0619	PCD. BAT_CV_SET 0xCC	# 48. 047V (SOC フル充電)		
0620	WAIT_SEC 1			

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0621  DH. BAT_SM_EXE          # DH. MC_EACH_EXE 0x0054
0622  #:                    << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
0623  #:                    0x00 PCD. BAT_SM_EXE
0624  #:                    0x02 PCD. MGA_P_DIS
0625  #
0626  . # < CHECK >
0627  CHECK PCD. BAT_K1          167
0628  CHECK PCD. BAT_K2          165
0629  CHECK PCD. BAT_CD          10.55 10.57
0630  CHECK PCD. BAT_CV          47.97 48.12
0631  #
0632  . # QL                  Check-Item          Spec          Check
0633  # -----+-----+-----+-----+
0634  # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
0635  #
0636  . # =====
0637  . # 3.3. インデックスパルス出力
0638  . # =====
0639  #
0640  . # < CHECK >
0641  CHECK ACS. F_ACS_PKT_FMT          =ACSHK1
0642  CHECK ACS. STS_IP_SRC              =NULL
0643  CHECK ACS. STS_IP_OUT              =DIS
0644  CHECK ACS. STS_IP_GNRT            =DIS
0645  CHECK ACS. I_IPCHK                =RST
0646  CHECK ACS. I_IP_GNRT              =SELF
0647  CHECK ACS_SYS. I_IPCHK            =RST
0648  CHECK ACS_SYS. I_IP_GNRT          =SELF
0649  CHECK ACS. BIT_I_IPCHK            =RST
0650  #
0651  . # SSAS を擬似モードにする
0652  . ACS. SSAS_SEL_IMT
0653  #
0654  # 疑似サンパルスを発生
0655  . # (SIN:2.5V, COS:5.0V, START:0x80, COARSE:0x10, Period:4.138sec)
0656  . ACS. DUM_SP_ON 2047 4095 128 16 8475
0657  #
0658  . # < CHECK >
0659  CHECK ACS. SSAS_ST_PT              =IMT
0660  CHECK ACS. SSAS_ST                  =IMT
0661  CHECK ACS. F_SPN_ERR_SEL            =OFF
0662  CHECK ACS. P_SPIN_SSAS_SEL          4.137 4.139
0663  CHECK ACS. THT_CRS_SEL              0x10
0664  CHECK ACS. SUN_ANG_COS              4.999 5.001
0665  CHECK ACS. SUN_ANG_SIN              2.499 2.501
0666  CHECK ACS. SUN_ANG_SEL              -2 2
0667  CHECK ACS_SYS. SSAS_ST              =IMT
0668  CHECK ACS_SYS. P_SPIN_SSAS_SEL      4.137 4.139
0669  CHECK ACS_SYS. THT_CRS_SEL          0x10
0670  CHECK ACS_SYS. SUN_ANG_SEL          -2 2
0671  CHECK ACS. BIT_SSAS_ST              =IMT
0672  CHECK ACS. BIT_P_SPIN_SSAS_SEL      4.013 4.263

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0673 CHECK ACS.BIT_SUN_ANG_SEL -0.125 0.125
0674 #
0675 . # ACS HK パケットフォーマットを ACS_HK4 に設定
0676 . ACS.ACS_PKT_FMT_4
0677 #
0678 . # < CHECK >
0679 CHECK ACS.SSAS_IMT_SP_STS =ON
0680 CHECK ACS.E_SSAS_SPIN 0.0999 0.1001
0681 CHECK ACS.N_SPINRATE_AVE 5 5
0682 CHECK ACS_SYS.F_ECL =OFF
0683 CHECK ACS_SYS.I_ATT =STBY
0684 CHECK ACS_SYS.P_B_AVE 4.137 4.139
0685 #
0686 . # IP フィードバックゲインを設定する。
0687 . ACS.IP_K_DT 0x3C9BA5E3 0x3C9BA5E3
0688 #
0689 . # < CHECK >
0690 CHECK ACS.K_DT0 0.018999 0.019001
0691 CHECK ACS.K_DT1 0.018999 0.019001
0692 #
0693 . # IP 源泉を IP にする
0694 . ACS.IP_SRC_IP
0695 # IP 生成機能を ON にする
0696 ACS.IP_GNRT_ON
0697 # MDP への IP 出力を開始する
0698 ACS.IP_OUT_ENA
0699 # ACS HK パケットフォーマットを ACS_HK1 に設定
0700 ACS.ACS_PKT_FMT_1
0701 #
0702 . # < CHECK >
0703 CHECK ACS.STS_IP_SRC =IP
0704 CHECK ACS.STS_IP_OUT =ENA
0705 CHECK ACS.STS_IP_GNRT =ENA
0706 CHECK ACS.I_IP_GNRT =SSAS
0707 CHECK ACS_SYS.I_IP_GNRT =SSAS
0708 #
0709 . # QL Check-Item Spec Check
0710 # -----+-----+-----+-----+
0711 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0712 #
0713 #
0714 . # =====
0715 . # 3.4. DR 設定
0716 . # =====
0717 . #
0718 . # << DR STATUS CHECK >>
0719 . #
0720 . # < CHECK >
0721 CHECK DH.DR_STATE =STANDBY
0722 CHECK DH.DR_CHK_ON_OFF =OFF
0723 . #
0724 . DH.DR_ERRCLR

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0725  #
0726  . # # パーティション分割状況通知パケットを出力
0727  . #
0728  . DH. DR_PARTDVNOTIFY
0729  . #
0730  . # # 各パーティション記録数通知のクリア
0731  . DH. DR_RECNTCLRALL
0732  . #
0733  . DH. DR_PTRLOG_ENA
0734  #
0735  . # < CHECK >
0736  CHECK DH. DR_PTRLOG_EN_DS                      =ENA
0737  #
0738  . # QL          Check-Item          Spec          Check
0739  # -----+-----+-----+-----+
0740  # CMD 出力装置  Message          STATUS CHECK 結果 OK    G    N
0741  #
0742  . # *****
0743  . # 4. TCFS CHECK
0744  . # *****
0745  # =====
0746  . # 4.1. DRU 立ち上げ
0747  # =====
0748  . #
0749  . PCD. ON_OFF_ENA
0750  . #
0751  . # < CHECK >
0752  CHECK PCD. ON_OFF_ENA_DIS                      =ENA
0753  . #
0754  . PCD. DRU_PSU_A_ON
0755  . #
0756  . # < CHECK >
0757  CHECK PCD. DRU_PSU_A_ON_OFF                    =ON
0758  . #
0759  . PCD. ON_OFF_DIS
0760  . #
0761  . # < CHECK >
0762  CHECK PCD. ON_OFF_ENA_DIS                      =DIS
0763  . #
0764  . ACS. DRU_SEL_A
0765  . #
0766  . # < CHECK >
0767  CHECK ACS. DRU_SEL_PT                          =A
0768  CHECK ACS. SYS. DRU_SEL_PT                      =A
0769  CHECK ACS. BIT_DRU_SEL_PT                      =A
0770  . #
0771  . ACS. ACS_IOPERR_CLR
0772  . #
0773  . # < CHECK >
0774  CHECK ACS. DRU_A_ONOFF_ST                      =ON
0775  CHECK ACS. DRU_B_ONOFF_ST                      =OFF
0776  CHECK ACS. DRU_ACIMIF_A_ST                    =ON

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0777	CHECK ACS.DRU_ACIMIF_B_ST	=OFF
0778	CHECK ACS.SRAM1_MEMST	=MEMO
0779	CHECK ACS.SRAM2_MEMST	=MEMO
0780	CHECK ACS.C_DRU_CMD_ECC2	0
0781	CHECK ACS.C_DRU_TLM_ECC2	0
0782	CHECK ACS.C_LBUSU_ECC1	0
0783	CHECK ACS.C_LBUSL_ECC1	0
0784	CHECK ACS.C_DRU_CMD_PrtY	0
0785	CHECK ACS.C_DRU_TLM_PrtY	0
0786	CHECK ACS.DATA_ERR_STATUS	=OK
0787	CHECK ACS.STS_DRUAPM_ON	=OFF
0788	CHECK ACS.I_ADMCE_A_PWR	=OFF
0789	CHECK ACS.I_ADMCE_B_PWR	=OFF
0790	CHECK ACS.ADM_SEL_PORT1	=A
0791	CHECK ACS.SRAM_ST	=SRAM1
0792	CHECK ACS_SYS.DRU_A_ONOFF_ST	=ON
0793	CHECK ACS_SYS.DRU_B_ONOFF_ST	=OFF
0794	CHECK ACS_SYS.DRU_ACIMIF_A_ST	=ON
0795	CHECK ACS_SYS.DRU_ACIMIF_B_ST	=OFF
0796	CHECK ACS_SYS.SRAM1_MEMST	=MEMO
0797	CHECK ACS_SYS.SRAM2_MEMST	=MEMO
0798	CHECK ACS_SYS.C_DRU_CMD_ECC2	0
0799	CHECK ACS_SYS.C_DRU_TLM_ECC2	0
0800	CHECK ACS_SYS.C_LBUSU_ECC1	0
0801	CHECK ACS_SYS.C_LBUSL_ECC1	0
0802	CHECK ACS_SYS.C_DRU_CMD_PrtY	0
0803	CHECK ACS_SYS.C_DRU_TLM_PrtY	0
0804	CHECK ACS_SYS.DATA_ERR_STATUS	=OK
0805	CHECK ACS_SYS.STS_DRUAPM_ON	=OFF
0806	CHECK ACS_SYS.I_ADMCE_A_PWR	=OFF
0807	CHECK ACS_SYS.I_ADMCE_B_PWR	=OFF
0808	CHECK ACS_SYS.ADM_SEL_PORT1	=A
0809	CHECK ACS_SYS.SRAM_ST	=SRAM1
0810	CHECK ACS.BIT_DRU_A_ONOFF_ST	=ON
0811	CHECK ACS.BIT_DRU_B_ONOFF_ST	=OFF
0812	CHECK ACS.BIT_I_ADMCE_A_PWR	=OFF
0813	CHECK ACS.BIT_I_ADMCE_B_PWR	=OFF
0814	. #	
0815	. ACS.ACS_PKT_FMT_4	
0816	. #	
0817	. # < CHECK >	
0818	CHECK ACS.F_ACS_PKT_FMT	=ACSHK4
0819	CHECK ACS.I_ECCERR_S_D	0
0820	CHECK ACS.I_ECCERR_S_L	0
0821	CHECK ACS.I_DRU_ACIMIF1	0
0822	CHECK ACS.I_DRU_ACIMIF2	0
0823	CHECK ACS.I_DRU_ACIMIF3	0
0824	CHECK ACS.I_DRU_ACIMIF4	0
0825	CHECK ACS.DRU_CMD_ECC2	0
0826	CHECK ACS.DRU_TLM_ECC2	0
0827	CHECK ACS.DRU_CMD_Parity1	0
0828	CHECK ACS.DRU_CMD_Parity2	0

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0829 CHECK ACS.DRU_TLM_Parity1 0
0830 CHECK ACS.DRU_TLM_Parity2 0
0831 CHECK ACS.I_CMD_FLAG1 0x02ec0c00
0832 CHECK ACS.I_CMD_FLAG2 0
0833 . #
0834 . ACS.ACS_PKT_FMT_1
0835 . #
0836 . # < CHECK >
0837 CHECK ACS.F_ACS_PKT_FMT =ACSHK1
0838 #
0839 . # QL Check-Item Spec Check
0840 # -----+-----+-----+-----
0841 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
0842 #
0843 #
0844 # =====
0845 . # 4.2. TCS Table Check
0846 # =====
0847 #
0848 . # << TCFS TABLE CHECK >>
0849 . #
0850 . TCS.ONESHOT_TLM_0x11 # BACKUP DTY TABLE1
0851 WAIT_SEC 5
0852 TCS.ONESHOT_TLM_0x12 # BACKUP DTY TABLE2
0853 WAIT_SEC 5
0854 TCS.ONESHOT_TLM_0x13 # BACKUP DTY TABLE3
0855 WAIT_SEC 5
0856 TCS.ONESHOT_TLM_0x14 # BACKUP DTY TABLE4
0857 WAIT_SEC 5
0858 TCS.ONESHOT_TLM_0x15 # BACKUP PRI TABLE1
0859 WAIT_SEC 5
0860 TCS.ONESHOT_TLM_0x16 # BACKUP PRI TABLE2
0861 WAIT_SEC 5
0862 TCS.ONESHOT_TLM_0x17 # BACKUP PRI TABLE3
0863 WAIT_SEC 5
0864 TCS.ONESHOT_TLM_0x18 # BACKUP PRI TABLE4
0865 WAIT_SEC 5
0866 TCS.ONESHOT_TLM_0x19 # NMNL DTY TABLE1
0867 WAIT_SEC 5
0868 TCS.ONESHOT_TLM_0x1A # NMNL DTY TABLE2
0869 WAIT_SEC 5
0870 TCS.ONESHOT_TLM_0x1B # NMNL DTY TABLE3
0871 WAIT_SEC 5
0872 TCS.ONESHOT_TLM_0x1C # NMNL PRI TABLE1
0873 WAIT_SEC 5
0874 TCS.ONESHOT_TLM_0x1D # NMNL PRI TABLE2
0875 WAIT_SEC 5
0876 TCS.ONESHOT_TLM_0x1E # NMNL PRI TABLE3
0877 WAIT_SEC 5
0878 TCS.ONESHOT_TLM_0x1F # NMNL THR TABLE1
0879 WAIT_SEC 5
0880 TCS.ONESHOT_TLM_0x20 # NMNL THR TABLE2

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0881	WAIT_SEC 5	
0882	TCS. ONESHOT_TLM_0x21	# NMNL THR TABLE3
0883	WAIT_SEC 5	
0884	TCS. ONESHOT_TLM_0x22	# ATSF TABLE
0885	WAIT_SEC 5	
0886	TCS. ONESHOT_TLM_0x23	# MASF TABLE
0887	WAIT_SEC 5	
0888	TCS. ONESHOT_TLM_0x24	# HTR PWR TABLE
0889	WAIT_SEC 5	
0890	TCS. ONESHOT_TLM_0x25	# HTR ENA/DIS TABLE
0891	WAIT_SEC 5	
0892	TCS. ONESHOT_TLM_0x26	# SEN/HTR SEL TABLE
0893	WAIT_SEC 5	
0894	TCS. ONESHOT_TLM_0x27	# RNG TABLE
0895	WAIT_SEC 5	
0896	TCS. ONESHOT_TLM_0x28	# SEN CH TABLE
0897	WAIT_SEC 5	
0898	#	
0899	# < CHECK >	
0900	# 0x11	
0901	CHECK TCS. BK_DTY_TBL1_01CH	51 51
0902	CHECK TCS. BK_DTY_TBL1_02CH	51 51
0903	CHECK TCS. BK_DTY_TBL1_03CH	54 54
0904	CHECK TCS. BK_DTY_TBL1_04CH	51 51
0905	CHECK TCS. BK_DTY_TBL1_05CH	228 228
0906	CHECK TCS. BK_DTY_TBL1_06CH	128 128
0907	CHECK TCS. BK_DTY_TBL1_07CH	256 256
0908	CHECK TCS. BK_DTY_TBL1_08CH	207 207
0909	CHECK TCS. BK_DTY_TBL1_09CH	256 256
0910	CHECK TCS. BK_DTY_TBL1_10CH	51 51
0911	CHECK TCS. BK_DTY_TBL1_11CH	256 256
0912	CHECK TCS. BK_DTY_TBL1_12CH	110 110
0913	CHECK TCS. BK_DTY_TBL1_13CH	128 128
0914	CHECK TCS. BK_DTY_TBL1_14CH	0 0
0915	CHECK TCS. BK_DTY_TBL1_15CH	0 0
0916	CHECK TCS. BK_DTY_TBL1_16CH	0 0
0917	CHECK TCS. BK_DTY_TBL1_17CH	51 51
0918	CHECK TCS. BK_DTY_TBL1_18CH	51 51
0919	CHECK TCS. BK_DTY_TBL1_19CH	56 56
0920	CHECK TCS. BK_DTY_TBL1_20CH	51 51
0921	CHECK TCS. BK_DTY_TBL1_21CH	95 95
0922	CHECK TCS. BK_DTY_TBL1_22CH	128 128
0923	CHECK TCS. BK_DTY_TBL1_23CH	256 256
0924	CHECK TCS. BK_DTY_TBL1_24CH	177 177
0925	CHECK TCS. BK_DTY_TBL1_25CH	177 177
0926	CHECK TCS. BK_DTY_TBL1_26CH	118 118
0927	CHECK TCS. BK_DTY_TBL1_27CH	174 174
0928	CHECK TCS. BK_DTY_TBL1_28CH	102 102
0929	CHECK TCS. BK_DTY_TBL1_29CH	128 128
0930	CHECK TCS. BK_DTY_TBL1_30CH	0 0
0931	CHECK TCS. BK_DTY_TBL1_31CH	0 0
0932	CHECK TCS. BK_DTY_TBL1_32CH	0 0

文 書 番 号	シート No.	版 数
	34	1

0933	#	
0934	# 0x12	
0935	CHECK TCS. BK_DTY_TBL2_01CH	59 59
0936	CHECK TCS. BK_DTY_TBL2_02CH	59 59
0937	CHECK TCS. BK_DTY_TBL2_03CH	61 61
0938	CHECK TCS. BK_DTY_TBL2_04CH	59 59
0939	CHECK TCS. BK_DTY_TBL2_05CH	256 256
0940	CHECK TCS. BK_DTY_TBL2_06CH	148 148
0941	CHECK TCS. BK_DTY_TBL2_07CH	256 256
0942	CHECK TCS. BK_DTY_TBL2_08CH	241 241
0943	CHECK TCS. BK_DTY_TBL2_09CH	256 256
0944	CHECK TCS. BK_DTY_TBL2_10CH	59 59
0945	CHECK TCS. BK_DTY_TBL2_11CH	256 256
0946	CHECK TCS. BK_DTY_TBL2_12CH	125 125
0947	CHECK TCS. BK_DTY_TBL2_13CH	148 148
0948	CHECK TCS. BK_DTY_TBL2_14CH	0 0
0949	CHECK TCS. BK_DTY_TBL2_15CH	0 0
0950	CHECK TCS. BK_DTY_TBL2_16CH	0 0
0951	CHECK TCS. BK_DTY_TBL2_17CH	59 59
0952	CHECK TCS. BK_DTY_TBL2_18CH	59 59
0953	CHECK TCS. BK_DTY_TBL2_19CH	67 67
0954	CHECK TCS. BK_DTY_TBL2_20CH	59 59
0955	CHECK TCS. BK_DTY_TBL2_21CH	110 110
0956	CHECK TCS. BK_DTY_TBL2_22CH	148 148
0957	CHECK TCS. BK_DTY_TBL2_23CH	256 256
0958	CHECK TCS. BK_DTY_TBL2_24CH	205 205
0959	CHECK TCS. BK_DTY_TBL2_25CH	205 205
0960	CHECK TCS. BK_DTY_TBL2_26CH	138 138
0961	CHECK TCS. BK_DTY_TBL2_27CH	202 202
0962	CHECK TCS. BK_DTY_TBL2_28CH	120 120
0963	CHECK TCS. BK_DTY_TBL2_29CH	148 148
0964	CHECK TCS. BK_DTY_TBL2_30CH	0 0
0965	CHECK TCS. BK_DTY_TBL2_31CH	0 0
0966	CHECK TCS. BK_DTY_TBL2_32CH	0 0
0967	#	
0968	# 0x13	
0969	CHECK TCS. BK_DTY_TBL3_01CH	256 256
0970	CHECK TCS. BK_DTY_TBL3_02CH	256 256
0971	CHECK TCS. BK_DTY_TBL3_03CH	256 256
0972	CHECK TCS. BK_DTY_TBL3_04CH	256 256
0973	CHECK TCS. BK_DTY_TBL3_05CH	256 256
0974	CHECK TCS. BK_DTY_TBL3_06CH	256 256
0975	CHECK TCS. BK_DTY_TBL3_07CH	256 256
0976	CHECK TCS. BK_DTY_TBL3_08CH	256 256
0977	CHECK TCS. BK_DTY_TBL3_09CH	256 256
0978	CHECK TCS. BK_DTY_TBL3_10CH	256 256
0979	CHECK TCS. BK_DTY_TBL3_11CH	256 256
0980	CHECK TCS. BK_DTY_TBL3_12CH	256 256
0981	CHECK TCS. BK_DTY_TBL3_13CH	256 256
0982	CHECK TCS. BK_DTY_TBL3_14CH	0 0
0983	CHECK TCS. BK_DTY_TBL3_15CH	0 0
0984	CHECK TCS. BK_DTY_TBL3_16CH	0 0

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	35	1

0985	CHECK TCS. BK_DTY_TBL3_17CH	256	256
0986	CHECK TCS. BK_DTY_TBL3_18CH	256	256
0987	CHECK TCS. BK_DTY_TBL3_19CH	256	256
0988	CHECK TCS. BK_DTY_TBL3_20CH	256	256
0989	CHECK TCS. BK_DTY_TBL3_21CH	256	256
0990	CHECK TCS. BK_DTY_TBL3_22CH	256	256
0991	CHECK TCS. BK_DTY_TBL3_23CH	256	256
0992	CHECK TCS. BK_DTY_TBL3_24CH	256	256
0993	CHECK TCS. BK_DTY_TBL3_25CH	256	256
0994	CHECK TCS. BK_DTY_TBL3_26CH	256	256
0995	CHECK TCS. BK_DTY_TBL3_27CH	256	256
0996	CHECK TCS. BK_DTY_TBL3_28CH	256	256
0997	CHECK TCS. BK_DTY_TBL3_29CH	256	256
0998	CHECK TCS. BK_DTY_TBL3_30CH	0	0
0999	CHECK TCS. BK_DTY_TBL3_31CH	0	0
1000	CHECK TCS. BK_DTY_TBL3_32CH	0	0
1001	#		
1002	# 0x14		
1003	CHECK TCS. BK_DTY_TBL4_01CH	256	256
1004	CHECK TCS. BK_DTY_TBL4_02CH	256	256
1005	CHECK TCS. BK_DTY_TBL4_03CH	256	256
1006	CHECK TCS. BK_DTY_TBL4_04CH	256	256
1007	CHECK TCS. BK_DTY_TBL4_05CH	256	256
1008	CHECK TCS. BK_DTY_TBL4_06CH	256	256
1009	CHECK TCS. BK_DTY_TBL4_07CH	256	256
1010	CHECK TCS. BK_DTY_TBL4_08CH	256	256
1011	CHECK TCS. BK_DTY_TBL4_09CH	256	256
1012	CHECK TCS. BK_DTY_TBL4_10CH	256	256
1013	CHECK TCS. BK_DTY_TBL4_11CH	256	256
1014	CHECK TCS. BK_DTY_TBL4_12CH	256	256
1015	CHECK TCS. BK_DTY_TBL4_13CH	256	256
1016	CHECK TCS. BK_DTY_TBL4_14CH	0	0
1017	CHECK TCS. BK_DTY_TBL4_15CH	0	0
1018	CHECK TCS. BK_DTY_TBL4_16CH	0	0
1019	CHECK TCS. BK_DTY_TBL4_17CH	256	256
1020	CHECK TCS. BK_DTY_TBL4_18CH	256	256
1021	CHECK TCS. BK_DTY_TBL4_19CH	256	256
1022	CHECK TCS. BK_DTY_TBL4_20CH	256	256
1023	CHECK TCS. BK_DTY_TBL4_21CH	256	256
1024	CHECK TCS. BK_DTY_TBL4_22CH	256	256
1025	CHECK TCS. BK_DTY_TBL4_23CH	256	256
1026	CHECK TCS. BK_DTY_TBL4_24CH	256	256
1027	CHECK TCS. BK_DTY_TBL4_25CH	256	256
1028	CHECK TCS. BK_DTY_TBL4_26CH	256	256
1029	CHECK TCS. BK_DTY_TBL4_27CH	256	256
1030	CHECK TCS. BK_DTY_TBL4_28CH	256	256
1031	CHECK TCS. BK_DTY_TBL4_29CH	256	256
1032	CHECK TCS. BK_DTY_TBL4_30CH	0	0
1033	CHECK TCS. BK_DTY_TBL4_31CH	0	0
1034	CHECK TCS. BK_DTY_TBL4_32CH	0	0
1035	#		
1036	# 0x15		

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	36	1

1037	CHECK TCS. BK_PRI_TBL1_01CH	21	21
1038	CHECK TCS. BK_PRI_TBL1_02CH	22	22
1039	CHECK TCS. BK_PRI_TBL1_03CH	23	23
1040	CHECK TCS. BK_PRI_TBL1_04CH	24	24
1041	CHECK TCS. BK_PRI_TBL1_05CH	4	4
1042	CHECK TCS. BK_PRI_TBL1_06CH	5	5
1043	CHECK TCS. BK_PRI_TBL1_07CH	6	6
1044	CHECK TCS. BK_PRI_TBL1_08CH	7	7
1045	CHECK TCS. BK_PRI_TBL1_09CH	13	13
1046	CHECK TCS. BK_PRI_TBL1_10CH	9	9
1047	CHECK TCS. BK_PRI_TBL1_11CH	10	10
1048	CHECK TCS. BK_PRI_TBL1_12CH	11	11
1049	CHECK TCS. BK_PRI_TBL1_13CH	12	12
1050	CHECK TCS. BK_PRI_TBL1_14CH	26	26
1051	CHECK TCS. BK_PRI_TBL1_15CH	27	27
1052	CHECK TCS. BK_PRI_TBL1_16CH	28	28
1053	CHECK TCS. BK_PRI_TBL1_17CH	8	8
1054	CHECK TCS. BK_PRI_TBL1_18CH	14	14
1055	CHECK TCS. BK_PRI_TBL1_19CH	15	15
1056	CHECK TCS. BK_PRI_TBL1_20CH	16	16
1057	CHECK TCS. BK_PRI_TBL1_21CH	17	17
1058	CHECK TCS. BK_PRI_TBL1_22CH	18	18
1059	CHECK TCS. BK_PRI_TBL1_23CH	19	19
1060	CHECK TCS. BK_PRI_TBL1_24CH	20	20
1061	CHECK TCS. BK_PRI_TBL1_25CH	0	0
1062	CHECK TCS. BK_PRI_TBL1_26CH	1	1
1063	CHECK TCS. BK_PRI_TBL1_27CH	2	2
1064	CHECK TCS. BK_PRI_TBL1_28CH	3	3
1065	CHECK TCS. BK_PRI_TBL1_29CH	25	25
1066	CHECK TCS. BK_PRI_TBL1_30CH	29	29
1067	CHECK TCS. BK_PRI_TBL1_31CH	30	30
1068	CHECK TCS. BK_PRI_TBL1_32CH	31	31
1069	#		
1070	# 0x16		
1071	CHECK TCS. BK_PRI_TBL2_01CH	21	21
1072	CHECK TCS. BK_PRI_TBL2_02CH	22	22
1073	CHECK TCS. BK_PRI_TBL2_03CH	23	23
1074	CHECK TCS. BK_PRI_TBL2_04CH	24	24
1075	CHECK TCS. BK_PRI_TBL2_05CH	4	4
1076	CHECK TCS. BK_PRI_TBL2_06CH	5	5
1077	CHECK TCS. BK_PRI_TBL2_07CH	6	6
1078	CHECK TCS. BK_PRI_TBL2_08CH	7	7
1079	CHECK TCS. BK_PRI_TBL2_09CH	13	13
1080	CHECK TCS. BK_PRI_TBL2_10CH	9	9
1081	CHECK TCS. BK_PRI_TBL2_11CH	10	10
1082	CHECK TCS. BK_PRI_TBL2_12CH	11	11
1083	CHECK TCS. BK_PRI_TBL2_13CH	12	12
1084	CHECK TCS. BK_PRI_TBL2_14CH	26	26
1085	CHECK TCS. BK_PRI_TBL2_15CH	27	27
1086	CHECK TCS. BK_PRI_TBL2_16CH	28	28
1087	CHECK TCS. BK_PRI_TBL2_17CH	8	8
1088	CHECK TCS. BK_PRI_TBL2_18CH	14	14

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	37	1

1089	CHECK TCS. BK_PRI_TBL2_19CH	15 15
1090	CHECK TCS. BK_PRI_TBL2_20CH	16 16
1091	CHECK TCS. BK_PRI_TBL2_21CH	17 17
1092	CHECK TCS. BK_PRI_TBL2_22CH	18 18
1093	CHECK TCS. BK_PRI_TBL2_23CH	19 19
1094	CHECK TCS. BK_PRI_TBL2_24CH	20 20
1095	CHECK TCS. BK_PRI_TBL2_25CH	0 0
1096	CHECK TCS. BK_PRI_TBL2_26CH	1 1
1097	CHECK TCS. BK_PRI_TBL2_27CH	2 2
1098	CHECK TCS. BK_PRI_TBL2_28CH	3 3
1099	CHECK TCS. BK_PRI_TBL2_29CH	25 25
1100	CHECK TCS. BK_PRI_TBL2_30CH	29 29
1101	CHECK TCS. BK_PRI_TBL2_31CH	30 30
1102	CHECK TCS. BK_PRI_TBL2_32CH	31 31
1103	#	
1104	# 0x17	
1105	CHECK TCS. BK_PRI_TBL3_01CH	21 21
1106	CHECK TCS. BK_PRI_TBL3_02CH	22 22
1107	CHECK TCS. BK_PRI_TBL3_03CH	23 23
1108	CHECK TCS. BK_PRI_TBL3_04CH	24 24
1109	CHECK TCS. BK_PRI_TBL3_05CH	4 4
1110	CHECK TCS. BK_PRI_TBL3_06CH	5 5
1111	CHECK TCS. BK_PRI_TBL3_07CH	6 6
1112	CHECK TCS. BK_PRI_TBL3_08CH	7 7
1113	CHECK TCS. BK_PRI_TBL3_09CH	13 13
1114	CHECK TCS. BK_PRI_TBL3_10CH	9 9
1115	CHECK TCS. BK_PRI_TBL3_11CH	10 10
1116	CHECK TCS. BK_PRI_TBL3_12CH	11 11
1117	CHECK TCS. BK_PRI_TBL3_13CH	12 12
1118	CHECK TCS. BK_PRI_TBL3_14CH	26 26
1119	CHECK TCS. BK_PRI_TBL3_15CH	27 27
1120	CHECK TCS. BK_PRI_TBL3_16CH	28 28
1121	CHECK TCS. BK_PRI_TBL3_17CH	8 8
1122	CHECK TCS. BK_PRI_TBL3_18CH	14 14
1123	CHECK TCS. BK_PRI_TBL3_19CH	15 15
1124	CHECK TCS. BK_PRI_TBL3_20CH	16 16
1125	CHECK TCS. BK_PRI_TBL3_21CH	17 17
1126	CHECK TCS. BK_PRI_TBL3_22CH	18 18
1127	CHECK TCS. BK_PRI_TBL3_23CH	19 19
1128	CHECK TCS. BK_PRI_TBL3_24CH	20 20
1129	CHECK TCS. BK_PRI_TBL3_25CH	0 0
1130	CHECK TCS. BK_PRI_TBL3_26CH	1 1
1131	CHECK TCS. BK_PRI_TBL3_27CH	2 2
1132	CHECK TCS. BK_PRI_TBL3_28CH	3 3
1133	CHECK TCS. BK_PRI_TBL3_29CH	25 25
1134	CHECK TCS. BK_PRI_TBL3_30CH	29 29
1135	CHECK TCS. BK_PRI_TBL3_31CH	30 30
1136	CHECK TCS. BK_PRI_TBL3_32CH	31 31
1137	#	
1138	# 0x18	
1139	CHECK TCS. BK_PRI_TBL4_01CH	21 21
1140	CHECK TCS. BK_PRI_TBL4_02CH	22 22

文 書 番 号	シート No.	版 数
	38	1

1141	CHECK TCS. BK_PRI_TBL4_03CH	23 23
1142	CHECK TCS. BK_PRI_TBL4_04CH	24 24
1143	CHECK TCS. BK_PRI_TBL4_05CH	4 4
1144	CHECK TCS. BK_PRI_TBL4_06CH	5 5
1145	CHECK TCS. BK_PRI_TBL4_07CH	6 6
1146	CHECK TCS. BK_PRI_TBL4_08CH	7 7
1147	CHECK TCS. BK_PRI_TBL4_09CH	13 13
1148	CHECK TCS. BK_PRI_TBL4_10CH	9 9
1149	CHECK TCS. BK_PRI_TBL4_11CH	10 10
1150	CHECK TCS. BK_PRI_TBL4_12CH	11 11
1151	CHECK TCS. BK_PRI_TBL4_13CH	12 12
1152	CHECK TCS. BK_PRI_TBL4_14CH	26 26
1153	CHECK TCS. BK_PRI_TBL4_15CH	27 27
1154	CHECK TCS. BK_PRI_TBL4_16CH	28 28
1155	CHECK TCS. BK_PRI_TBL4_17CH	8 8
1156	CHECK TCS. BK_PRI_TBL4_18CH	14 14
1157	CHECK TCS. BK_PRI_TBL4_19CH	15 15
1158	CHECK TCS. BK_PRI_TBL4_20CH	16 16
1159	CHECK TCS. BK_PRI_TBL4_21CH	17 17
1160	CHECK TCS. BK_PRI_TBL4_22CH	18 18
1161	CHECK TCS. BK_PRI_TBL4_23CH	19 19
1162	CHECK TCS. BK_PRI_TBL4_24CH	20 20
1163	CHECK TCS. BK_PRI_TBL4_25CH	0 0
1164	CHECK TCS. BK_PRI_TBL4_26CH	1 1
1165	CHECK TCS. BK_PRI_TBL4_27CH	2 2
1166	CHECK TCS. BK_PRI_TBL4_28CH	3 3
1167	CHECK TCS. BK_PRI_TBL4_29CH	25 25
1168	CHECK TCS. BK_PRI_TBL4_30CH	29 29
1169	CHECK TCS. BK_PRI_TBL4_31CH	30 30
1170	CHECK TCS. BK_PRI_TBL4_32CH	31 31
1171	#	
1172	# 0x19	
1173	CHECK TCS. NM_DTY_TBL1_01CH	51 51
1174	CHECK TCS. NM_DTY_TBL1_02CH	51 51
1175	CHECK TCS. NM_DTY_TBL1_03CH	54 54
1176	CHECK TCS. NM_DTY_TBL1_04CH	51 51
1177	CHECK TCS. NM_DTY_TBL1_05CH	228 228
1178	CHECK TCS. NM_DTY_TBL1_06CH	128 128
1179	CHECK TCS. NM_DTY_TBL1_07CH	256 256
1180	CHECK TCS. NM_DTY_TBL1_08CH	207 207
1181	CHECK TCS. NM_DTY_TBL1_09CH	256 256
1182	CHECK TCS. NM_DTY_TBL1_10CH	51 51
1183	CHECK TCS. NM_DTY_TBL1_11CH	256 256
1184	CHECK TCS. NM_DTY_TBL1_12CH	110 110
1185	CHECK TCS. NM_DTY_TBL1_13CH	128 128
1186	CHECK TCS. NM_DTY_TBL1_14CH	0 0
1187	CHECK TCS. NM_DTY_TBL1_15CH	0 0
1188	CHECK TCS. NM_DTY_TBL1_16CH	0 0
1189	CHECK TCS. NM_DTY_TBL1_17CH	51 51
1190	CHECK TCS. NM_DTY_TBL1_18CH	51 51
1191	CHECK TCS. NM_DTY_TBL1_19CH	56 56
1192	CHECK TCS. NM_DTY_TBL1_20CH	51 51

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	39	1

1193	CHECK TCS. NM_DTY_TBL1_21CH	95 95
1194	CHECK TCS. NM_DTY_TBL1_22CH	128 128
1195	CHECK TCS. NM_DTY_TBL1_23CH	256 256
1196	CHECK TCS. NM_DTY_TBL1_24CH	177 177
1197	CHECK TCS. NM_DTY_TBL1_25CH	177 177
1198	CHECK TCS. NM_DTY_TBL1_26CH	118 118
1199	CHECK TCS. NM_DTY_TBL1_27CH	174 174
1200	CHECK TCS. NM_DTY_TBL1_28CH	102 102
1201	CHECK TCS. NM_DTY_TBL1_29CH	128 128
1202	CHECK TCS. NM_DTY_TBL1_30CH	0 0
1203	CHECK TCS. NM_DTY_TBL1_31CH	0 0
1204	CHECK TCS. NM_DTY_TBL1_32CH	0 0
1205	#	
1206	# 0x1A	
1207	CHECK TCS. NM_DTY_TBL2_01CH	59 59
1208	CHECK TCS. NM_DTY_TBL2_02CH	59 59
1209	CHECK TCS. NM_DTY_TBL2_03CH	61 61
1210	CHECK TCS. NM_DTY_TBL2_04CH	59 59
1211	CHECK TCS. NM_DTY_TBL2_05CH	256 256
1212	CHECK TCS. NM_DTY_TBL2_06CH	148 148
1213	CHECK TCS. NM_DTY_TBL2_07CH	256 256
1214	CHECK TCS. NM_DTY_TBL2_08CH	241 241
1215	CHECK TCS. NM_DTY_TBL2_09CH	256 256
1216	CHECK TCS. NM_DTY_TBL2_10CH	59 59
1217	CHECK TCS. NM_DTY_TBL2_11CH	256 256
1218	CHECK TCS. NM_DTY_TBL2_12CH	125 125
1219	CHECK TCS. NM_DTY_TBL2_13CH	148 148
1220	CHECK TCS. NM_DTY_TBL2_14CH	0 0
1221	CHECK TCS. NM_DTY_TBL2_15CH	0 0
1222	CHECK TCS. NM_DTY_TBL2_16CH	0 0
1223	CHECK TCS. NM_DTY_TBL2_17CH	59 59
1224	CHECK TCS. NM_DTY_TBL2_18CH	59 59
1225	CHECK TCS. NM_DTY_TBL2_19CH	67 67
1226	CHECK TCS. NM_DTY_TBL2_20CH	59 59
1227	CHECK TCS. NM_DTY_TBL2_21CH	110 110
1228	CHECK TCS. NM_DTY_TBL2_22CH	148 148
1229	CHECK TCS. NM_DTY_TBL2_23CH	256 256
1230	CHECK TCS. NM_DTY_TBL2_24CH	205 205
1231	CHECK TCS. NM_DTY_TBL2_25CH	205 205
1232	CHECK TCS. NM_DTY_TBL2_26CH	138 138
1233	CHECK TCS. NM_DTY_TBL2_27CH	202 202
1234	CHECK TCS. NM_DTY_TBL2_28CH	120 120
1235	CHECK TCS. NM_DTY_TBL2_29CH	148 148
1236	CHECK TCS. NM_DTY_TBL2_30CH	0 0
1237	CHECK TCS. NM_DTY_TBL2_31CH	0 0
1238	CHECK TCS. NM_DTY_TBL2_32CH	0 0
1239	#	
1240	# 0x1B	
1241	CHECK TCS. NM_DTY_TBL3_01CH	256 256
1242	CHECK TCS. NM_DTY_TBL3_02CH	256 256
1243	CHECK TCS. NM_DTY_TBL3_03CH	256 256
1244	CHECK TCS. NM_DTY_TBL3_04CH	256 256

文 書 番 号	シート No.	版 数
	40	1

1245	CHECK TCS. NM_DTY_TBL3_05CH	256	256
1246	CHECK TCS. NM_DTY_TBL3_06CH	256	256
1247	CHECK TCS. NM_DTY_TBL3_07CH	256	256
1248	CHECK TCS. NM_DTY_TBL3_08CH	256	256
1249	CHECK TCS. NM_DTY_TBL3_09CH	256	256
1250	CHECK TCS. NM_DTY_TBL3_10CH	256	256
1251	CHECK TCS. NM_DTY_TBL3_11CH	256	256
1252	CHECK TCS. NM_DTY_TBL3_12CH	256	256
1253	CHECK TCS. NM_DTY_TBL3_13CH	256	256
1254	CHECK TCS. NM_DTY_TBL3_14CH	0	0
1255	CHECK TCS. NM_DTY_TBL3_15CH	0	0
1256	CHECK TCS. NM_DTY_TBL3_16CH	0	0
1257	CHECK TCS. NM_DTY_TBL3_17CH	256	256
1258	CHECK TCS. NM_DTY_TBL3_18CH	256	256
1259	CHECK TCS. NM_DTY_TBL3_19CH	256	256
1260	CHECK TCS. NM_DTY_TBL3_20CH	256	256
1261	CHECK TCS. NM_DTY_TBL3_21CH	256	256
1262	CHECK TCS. NM_DTY_TBL3_22CH	256	256
1263	CHECK TCS. NM_DTY_TBL3_23CH	256	256
1264	CHECK TCS. NM_DTY_TBL3_24CH	256	256
1265	CHECK TCS. NM_DTY_TBL3_25CH	256	256
1266	CHECK TCS. NM_DTY_TBL3_26CH	256	256
1267	CHECK TCS. NM_DTY_TBL3_27CH	256	256
1268	CHECK TCS. NM_DTY_TBL3_28CH	256	256
1269	CHECK TCS. NM_DTY_TBL3_29CH	256	256
1270	CHECK TCS. NM_DTY_TBL3_30CH	0	0
1271	CHECK TCS. NM_DTY_TBL3_31CH	0	0
1272	CHECK TCS. NM_DTY_TBL3_32CH	0	0
1273	#		
1274	# 0x1C		
1275	CHECK TCS. NM_PRI_TBL1_01CH	3	3
1276	CHECK TCS. NM_PRI_TBL1_02CH	3	3
1277	CHECK TCS. NM_PRI_TBL1_03CH	3	3
1278	CHECK TCS. NM_PRI_TBL1_04CH	3	3
1279	CHECK TCS. NM_PRI_TBL1_05CH	3	3
1280	CHECK TCS. NM_PRI_TBL1_06CH	3	3
1281	CHECK TCS. NM_PRI_TBL1_07CH	3	3
1282	CHECK TCS. NM_PRI_TBL1_08CH	3	3
1283	CHECK TCS. NM_PRI_TBL1_09CH	3	3
1284	CHECK TCS. NM_PRI_TBL1_10CH	3	3
1285	CHECK TCS. NM_PRI_TBL1_11CH	3	3
1286	CHECK TCS. NM_PRI_TBL1_12CH	3	3
1287	CHECK TCS. NM_PRI_TBL1_13CH	3	3
1288	CHECK TCS. NM_PRI_TBL1_14CH	0	0
1289	CHECK TCS. NM_PRI_TBL1_15CH	0	0
1290	CHECK TCS. NM_PRI_TBL1_16CH	0	0
1291	CHECK TCS. NM_PRI_TBL1_17CH	3	3
1292	CHECK TCS. NM_PRI_TBL1_18CH	3	3
1293	CHECK TCS. NM_PRI_TBL1_19CH	3	3
1294	CHECK TCS. NM_PRI_TBL1_20CH	3	3
1295	CHECK TCS. NM_PRI_TBL1_21CH	3	3
1296	CHECK TCS. NM_PRI_TBL1_22CH	3	3

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	41	1

1297	CHECK TCS. NM_PRI_TBL1_23CH	3 3
1298	CHECK TCS. NM_PRI_TBL1_24CH	3 3
1299	CHECK TCS. NM_PRI_TBL1_25CH	3 3
1300	CHECK TCS. NM_PRI_TBL1_26CH	3 3
1301	CHECK TCS. NM_PRI_TBL1_27CH	3 3
1302	CHECK TCS. NM_PRI_TBL1_28CH	3 3
1303	CHECK TCS. NM_PRI_TBL1_29CH	3 3
1304	CHECK TCS. NM_PRI_TBL1_30CH	0 0
1305	CHECK TCS. NM_PRI_TBL1_31CH	0 0
1306	CHECK TCS. NM_PRI_TBL1_32CH	0 0
1307	#	
1308	# 0x1D	
1309	CHECK TCS. NM_PRI_TBL2_01CH	3 3
1310	CHECK TCS. NM_PRI_TBL2_02CH	3 3
1311	CHECK TCS. NM_PRI_TBL2_03CH	3 3
1312	CHECK TCS. NM_PRI_TBL2_04CH	3 3
1313	CHECK TCS. NM_PRI_TBL2_05CH	3 3
1314	CHECK TCS. NM_PRI_TBL2_06CH	3 3
1315	CHECK TCS. NM_PRI_TBL2_07CH	3 3
1316	CHECK TCS. NM_PRI_TBL2_08CH	3 3
1317	CHECK TCS. NM_PRI_TBL2_09CH	3 3
1318	CHECK TCS. NM_PRI_TBL2_10CH	3 3
1319	CHECK TCS. NM_PRI_TBL2_11CH	3 3
1320	CHECK TCS. NM_PRI_TBL2_12CH	3 3
1321	CHECK TCS. NM_PRI_TBL2_13CH	3 3
1322	CHECK TCS. NM_PRI_TBL2_14CH	0 0
1323	CHECK TCS. NM_PRI_TBL2_15CH	0 0
1324	CHECK TCS. NM_PRI_TBL2_16CH	0 0
1325	CHECK TCS. NM_PRI_TBL2_17CH	3 3
1326	CHECK TCS. NM_PRI_TBL2_18CH	3 3
1327	CHECK TCS. NM_PRI_TBL2_19CH	3 3
1328	CHECK TCS. NM_PRI_TBL2_20CH	3 3
1329	CHECK TCS. NM_PRI_TBL2_21CH	3 3
1330	CHECK TCS. NM_PRI_TBL2_22CH	3 3
1331	CHECK TCS. NM_PRI_TBL2_23CH	3 3
1332	CHECK TCS. NM_PRI_TBL2_24CH	3 3
1333	CHECK TCS. NM_PRI_TBL2_25CH	3 3
1334	CHECK TCS. NM_PRI_TBL2_26CH	3 3
1335	CHECK TCS. NM_PRI_TBL2_27CH	3 3
1336	CHECK TCS. NM_PRI_TBL2_28CH	3 3
1337	CHECK TCS. NM_PRI_TBL2_29CH	3 3
1338	CHECK TCS. NM_PRI_TBL2_30CH	0 0
1339	CHECK TCS. NM_PRI_TBL2_31CH	0 0
1340	CHECK TCS. NM_PRI_TBL2_32CH	0 0
1341	#	
1342	# 0x1E	
1343	CHECK TCS. NM_PRI_TBL3_01CH	3 3
1344	CHECK TCS. NM_PRI_TBL3_02CH	3 3
1345	CHECK TCS. NM_PRI_TBL3_03CH	3 3
1346	CHECK TCS. NM_PRI_TBL3_04CH	3 3
1347	CHECK TCS. NM_PRI_TBL3_05CH	3 3
1348	CHECK TCS. NM_PRI_TBL3_06CH	3 3

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	42	1

1349	CHECK TCS. NM_PRI_TBL3_07CH	3 3
1350	CHECK TCS. NM_PRI_TBL3_08CH	3 3
1351	CHECK TCS. NM_PRI_TBL3_09CH	3 3
1352	CHECK TCS. NM_PRI_TBL3_10CH	3 3
1353	CHECK TCS. NM_PRI_TBL3_11CH	3 3
1354	CHECK TCS. NM_PRI_TBL3_12CH	3 3
1355	CHECK TCS. NM_PRI_TBL3_13CH	3 3
1356	CHECK TCS. NM_PRI_TBL3_14CH	0 0
1357	CHECK TCS. NM_PRI_TBL3_15CH	0 0
1358	CHECK TCS. NM_PRI_TBL3_16CH	0 0
1359	CHECK TCS. NM_PRI_TBL3_17CH	3 3
1360	CHECK TCS. NM_PRI_TBL3_18CH	3 3
1361	CHECK TCS. NM_PRI_TBL3_19CH	3 3
1362	CHECK TCS. NM_PRI_TBL3_20CH	3 3
1363	CHECK TCS. NM_PRI_TBL3_21CH	3 3
1364	CHECK TCS. NM_PRI_TBL3_22CH	3 3
1365	CHECK TCS. NM_PRI_TBL3_23CH	3 3
1366	CHECK TCS. NM_PRI_TBL3_24CH	3 3
1367	CHECK TCS. NM_PRI_TBL3_25CH	3 3
1368	CHECK TCS. NM_PRI_TBL3_26CH	3 3
1369	CHECK TCS. NM_PRI_TBL3_27CH	3 3
1370	CHECK TCS. NM_PRI_TBL3_28CH	3 3
1371	CHECK TCS. NM_PRI_TBL3_29CH	3 3
1372	CHECK TCS. NM_PRI_TBL3_30CH	0 0
1373	CHECK TCS. NM_PRI_TBL3_31CH	0 0
1374	CHECK TCS. NM_PRI_TBL3_32CH	0 0
1375	#	
1376	# 0x1F	
1377	CHECK TCS. NM_ON_TBL1_01CH	-18. 73 -17. 73
1378	CHECK TCS. NM_ON_TBL1_02CH	-18. 73 -17. 73
1379	CHECK TCS. NM_ON_TBL1_03CH	-18. 73 -17. 73
1380	CHECK TCS. NM_ON_TBL1_04CH	-18. 73 -17. 73
1381	CHECK TCS. NM_ON_TBL1_05CH	-18. 73 -17. 73
1382	CHECK TCS. NM_ON_TBL1_06CH	6. 41 7. 41
1383	CHECK TCS. NM_ON_TBL1_07CH	-13. 6 -12. 6
1384	CHECK TCS. NM_ON_TBL1_08CH	-7. 99 -6. 99
1385	CHECK TCS. NM_ON_TBL1_09CH	-18. 73 -17. 73
1386	CHECK TCS. NM_ON_TBL1_10CH	-18. 73 -17. 73
1387	CHECK TCS. NM_ON_TBL1_11CH	-18. 5 -17. 5
1388	CHECK TCS. NM_ON_TBL1_12CH	-18. 73 -17. 73
1389	CHECK TCS. NM_ON_TBL1_13CH	6. 41 7. 41
1390	# CHECK TCS. NM_ON_TBL1_14CH	-50. 42 -50. 40
1391	# CHECK TCS. NM_ON_TBL1_15CH	-50. 42 -50. 40
1392	# CHECK TCS. NM_ON_TBL1_16CH	-50. 42 -50. 40
1393	CHECK TCS. NM_ON_TBL1_17CH	-18. 73 -17. 73
1394	CHECK TCS. NM_ON_TBL1_18CH	-18. 73 -17. 73
1395	CHECK TCS. NM_ON_TBL1_19CH	-18. 73 -17. 73
1396	CHECK TCS. NM_ON_TBL1_20CH	-18. 73 -17. 73
1397	CHECK TCS. NM_ON_TBL1_21CH	-18. 73 -17. 73
1398	CHECK TCS. NM_ON_TBL1_22CH	6. 41 7. 41
1399	CHECK TCS. NM_ON_TBL1_23CH	-13. 6 -12. 6
1400	CHECK TCS. NM_ON_TBL1_24CH	49. 5 50. 5

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	43	1

1401	CHECK TCS. NM_ON_TBL1_25CH	-18.73 -17.73
1402	CHECK TCS. NM_ON_TBL1_26CH	-18.73 -17.73
1403	CHECK TCS. NM_ON_TBL1_27CH	-18.73 -17.73
1404	CHECK TCS. NM_ON_TBL1_28CH	-18.72 -17.72
1405	CHECK TCS. NM_ON_TBL1_29CH	6.41 7.41
1406	# CHECK TCS. NM_ON_TBL1_30CH	-50.42 -50.40
1407	# CHECK TCS. NM_ON_TBL1_31CH	-50.42 -50.40
1408	# CHECK TCS. NM_ON_TBL1_32CH	-50.42 -50.40
1409	CHECK TCS. NM_OFF_TBL1_01CH	-18.73 -17.73
1410	CHECK TCS. NM_OFF_TBL1_02CH	-18.73 -17.73
1411	CHECK TCS. NM_OFF_TBL1_03CH	-18.73 -17.73
1412	CHECK TCS. NM_OFF_TBL1_04CH	-18.73 -17.73
1413	CHECK TCS. NM_OFF_TBL1_05CH	-18.73 -17.73
1414	CHECK TCS. NM_OFF_TBL1_06CH	6.41 7.41
1415	CHECK TCS. NM_OFF_TBL1_07CH	-13.6 -12.6
1416	CHECK TCS. NM_OFF_TBL1_08CH	-7.99 -6.99
1417	CHECK TCS. NM_OFF_TBL1_09CH	-18.73 -17.73
1418	CHECK TCS. NM_OFF_TBL1_10CH	-18.73 -17.73
1419	CHECK TCS. NM_OFF_TBL1_11CH	-18.5 -17.5
1420	CHECK TCS. NM_OFF_TBL1_12CH	-18.73 -17.73
1421	CHECK TCS. NM_OFF_TBL1_13CH	6.41 7.41
1422	# CHECK TCS. NM_OFF_TBL1_14CH	-50.42 -50.40
1423	# CHECK TCS. NM_OFF_TBL1_15CH	-50.42 -50.40
1424	# CHECK TCS. NM_OFF_TBL1_16CH	-50.42 -50.40
1425	CHECK TCS. NM_OFF_TBL1_17CH	-18.73 -17.73
1426	CHECK TCS. NM_OFF_TBL1_18CH	-18.73 -17.73
1427	CHECK TCS. NM_OFF_TBL1_19CH	-18.73 -17.73
1428	CHECK TCS. NM_OFF_TBL1_20CH	-18.73 -17.73
1429	CHECK TCS. NM_OFF_TBL1_21CH	-18.73 -17.73
1430	CHECK TCS. NM_OFF_TBL1_22CH	6.41 7.41
1431	CHECK TCS. NM_OFF_TBL1_23CH	-13.6 -12.6
1432	CHECK TCS. NM_OFF_TBL1_24CH	69.51 70.51
1433	CHECK TCS. NM_OFF_TBL1_25CH	-18.73 -17.73
1434	CHECK TCS. NM_OFF_TBL1_26CH	-18.73 -17.73
1435	CHECK TCS. NM_OFF_TBL1_27CH	-18.73 -17.73
1436	CHECK TCS. NM_OFF_TBL1_28CH	-18.72 -17.72
1437	CHECK TCS. NM_OFF_TBL1_29CH	6.41 7.41
1438	# CHECK TCS. NM_OFF_TBL1_30CH	-50.42 -50.40
1439	# CHECK TCS. NM_OFF_TBL1_31CH	-50.42 -50.40
1440	# CHECK TCS. NM_OFF_TBL1_32CH	-50.42 -50.40
1441	#	
1442	# 0x20	
1443	CHECK TCS. NM_ON_TBL2_01CH	-18.73 -17.73
1444	CHECK TCS. NM_ON_TBL2_02CH	-18.73 -17.73
1445	CHECK TCS. NM_ON_TBL2_03CH	-18.73 -17.73
1446	CHECK TCS. NM_ON_TBL2_04CH	-18.73 -17.73
1447	CHECK TCS. NM_ON_TBL2_05CH	-18.73 -17.73
1448	CHECK TCS. NM_ON_TBL2_06CH	6.41 7.41
1449	CHECK TCS. NM_ON_TBL2_07CH	-13.6 -12.6
1450	CHECK TCS. NM_ON_TBL2_08CH	-7.99 -6.99
1451	CHECK TCS. NM_ON_TBL2_09CH	-18.73 -17.73
1452	CHECK TCS. NM_ON_TBL2_10CH	-18.73 -17.73

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	44	1

1453	CHECK TCS. NM_ON_TBL2_11CH	-18.5 -17.5
1454	CHECK TCS. NM_ON_TBL2_12CH	-18.73 -17.73
1455	CHECK TCS. NM_ON_TBL2_13CH	6.41 7.41
1456	# CHECK TCS. NM_ON_TBL2_14CH	-50.42 -50.40
1457	# CHECK TCS. NM_ON_TBL2_15CH	-50.42 -50.40
1458	# CHECK TCS. NM_ON_TBL2_16CH	-50.42 -50.40
1459	CHECK TCS. NM_ON_TBL2_17CH	-18.73 -17.73
1460	CHECK TCS. NM_ON_TBL2_18CH	-18.73 -17.73
1461	CHECK TCS. NM_ON_TBL2_19CH	-18.73 -17.73
1462	CHECK TCS. NM_ON_TBL2_20CH	-18.73 -17.73
1463	CHECK TCS. NM_ON_TBL2_21CH	-18.73 -17.73
1464	CHECK TCS. NM_ON_TBL2_22CH	6.41 7.41
1465	CHECK TCS. NM_ON_TBL2_23CH	-13.6 -12.6
1466	CHECK TCS. NM_ON_TBL2_24CH	49.5 50.5
1467	CHECK TCS. NM_ON_TBL2_25CH	-18.73 -17.73
1468	CHECK TCS. NM_ON_TBL2_26CH	-18.73 -17.73
1469	CHECK TCS. NM_ON_TBL2_27CH	-18.73 -17.73
1470	CHECK TCS. NM_ON_TBL2_28CH	-18.72 -17.72
1471	CHECK TCS. NM_ON_TBL2_29CH	6.41 7.41
1472	# CHECK TCS. NM_ON_TBL2_30CH	-50.42 -50.40
1473	# CHECK TCS. NM_ON_TBL2_31CH	-50.42 -50.40
1474	# CHECK TCS. NM_ON_TBL2_32CH	-50.42 -50.40
1475	CHECK TCS. NM_OFF_TBL2_01CH	-18.73 -17.73
1476	CHECK TCS. NM_OFF_TBL2_02CH	-18.73 -17.73
1477	CHECK TCS. NM_OFF_TBL2_03CH	-18.73 -17.73
1478	CHECK TCS. NM_OFF_TBL2_04CH	-18.73 -17.73
1479	CHECK TCS. NM_OFF_TBL2_05CH	-18.73 -17.73
1480	CHECK TCS. NM_OFF_TBL2_06CH	6.41 7.41
1481	CHECK TCS. NM_OFF_TBL2_07CH	21.29 22.29
1482	CHECK TCS. NM_OFF_TBL2_08CH	-7.99 -6.99
1483	CHECK TCS. NM_OFF_TBL2_09CH	-18.73 -17.73
1484	CHECK TCS. NM_OFF_TBL2_10CH	-18.73 -17.73
1485	CHECK TCS. NM_OFF_TBL2_11CH	-18.5 -17.5
1486	CHECK TCS. NM_OFF_TBL2_12CH	-18.73 -17.73
1487	CHECK TCS. NM_OFF_TBL2_13CH	6.41 7.41
1488	# CHECK TCS. NM_OFF_TBL2_14CH	-50.42 -50.40
1489	# CHECK TCS. NM_OFF_TBL2_15CH	-50.42 -50.40
1490	# CHECK TCS. NM_OFF_TBL2_16CH	-50.42 -50.40
1491	CHECK TCS. NM_OFF_TBL2_17CH	-18.73 -17.73
1492	CHECK TCS. NM_OFF_TBL2_18CH	-18.73 -17.73
1493	CHECK TCS. NM_OFF_TBL2_19CH	-18.73 -17.73
1494	CHECK TCS. NM_OFF_TBL2_20CH	-18.73 -17.73
1495	CHECK TCS. NM_OFF_TBL2_21CH	-18.73 -17.73
1496	CHECK TCS. NM_OFF_TBL2_22CH	6.41 7.41
1497	CHECK TCS. NM_OFF_TBL2_23CH	21.29 22.29
1498	CHECK TCS. NM_OFF_TBL2_24CH	69.51 70.51
1499	CHECK TCS. NM_OFF_TBL2_25CH	-18.73 -17.73
1500	CHECK TCS. NM_OFF_TBL2_26CH	-18.73 -17.73
1501	CHECK TCS. NM_OFF_TBL2_27CH	-18.73 -17.73
1502	CHECK TCS. NM_OFF_TBL2_28CH	-18.72 -17.72
1503	CHECK TCS. NM_OFF_TBL2_29CH	6.41 7.41
1504	# CHECK TCS. NM_OFF_TBL2_30CH	-50.42 -50.40

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	45	1

1505	# CHECK TCS.NM_OFF_TBL2_31CH	-50.42 -50.40
1506	# CHECK TCS.NM_OFF_TBL2_32CH	-50.42 -50.40
1507	#	
1508	# 0x21	
1509	CHECK TCS.NM_ON_TBL3_01CH	-18.73 -17.73
1510	CHECK TCS.NM_ON_TBL3_02CH	-18.73 -17.73
1511	CHECK TCS.NM_ON_TBL3_03CH	-18.73 -17.73
1512	CHECK TCS.NM_ON_TBL3_04CH	-18.73 -17.73
1513	CHECK TCS.NM_ON_TBL3_05CH	-18.73 -17.73
1514	CHECK TCS.NM_ON_TBL3_06CH	6.41 7.41
1515	CHECK TCS.NM_ON_TBL3_07CH	-13.6 -12.6
1516	CHECK TCS.NM_ON_TBL3_08CH	-7.99 -6.99
1517	CHECK TCS.NM_ON_TBL3_09CH	-18.73 -17.73
1518	CHECK TCS.NM_ON_TBL3_10CH	-18.73 -17.73
1519	CHECK TCS.NM_ON_TBL3_11CH	-18.5 -17.5
1520	CHECK TCS.NM_ON_TBL3_12CH	-18.73 -17.73
1521	CHECK TCS.NM_ON_TBL3_13CH	6.41 7.41
1522	# CHECK TCS.NM_ON_TBL3_14CH	-50.42 -50.40
1523	# CHECK TCS.NM_ON_TBL3_15CH	-50.42 -50.40
1524	# CHECK TCS.NM_ON_TBL3_16CH	-50.42 -50.40
1525	CHECK TCS.NM_ON_TBL3_17CH	-18.73 -17.73
1526	CHECK TCS.NM_ON_TBL3_18CH	-18.73 -17.73
1527	CHECK TCS.NM_ON_TBL3_19CH	-18.73 -17.73
1528	CHECK TCS.NM_ON_TBL3_20CH	-18.73 -17.73
1529	CHECK TCS.NM_ON_TBL3_21CH	-18.73 -17.73
1530	CHECK TCS.NM_ON_TBL3_22CH	6.41 7.41
1531	CHECK TCS.NM_ON_TBL3_23CH	-13.6 -12.6
1532	CHECK TCS.NM_ON_TBL3_24CH	49.5 50.5
1533	CHECK TCS.NM_ON_TBL3_25CH	-18.73 -17.73
1534	CHECK TCS.NM_ON_TBL3_26CH	-18.73 -17.73
1535	CHECK TCS.NM_ON_TBL3_27CH	-18.73 -17.73
1536	CHECK TCS.NM_ON_TBL3_28CH	-18.72 -17.72
1537	CHECK TCS.NM_ON_TBL3_29CH	6.41 7.41
1538	# CHECK TCS.NM_ON_TBL3_30CH	-50.42 -50.40
1539	# CHECK TCS.NM_ON_TBL3_31CH	-50.42 -50.40
1540	# CHECK TCS.NM_ON_TBL3_32CH	-50.42 -50.40
1541	CHECK TCS.NM_OFF_TBL3_01CH	-18.73 -17.73
1542	CHECK TCS.NM_OFF_TBL3_02CH	-18.73 -17.73
1543	CHECK TCS.NM_OFF_TBL3_03CH	-18.73 -17.73
1544	CHECK TCS.NM_OFF_TBL3_04CH	-18.73 -17.73
1545	CHECK TCS.NM_OFF_TBL3_05CH	-18.73 -17.73
1546	CHECK TCS.NM_OFF_TBL3_06CH	6.41 7.41
1547	CHECK TCS.NM_OFF_TBL3_07CH	-13.6 -12.6
1548	CHECK TCS.NM_OFF_TBL3_08CH	-7.99 -6.99
1549	CHECK TCS.NM_OFF_TBL3_09CH	-18.73 -17.73
1550	CHECK TCS.NM_OFF_TBL3_10CH	-18.73 -17.73
1551	CHECK TCS.NM_OFF_TBL3_11CH	-18.5 -17.5
1552	CHECK TCS.NM_OFF_TBL3_12CH	-18.73 -17.73
1553	CHECK TCS.NM_OFF_TBL3_13CH	6.41 7.41
1554	# CHECK TCS.NM_OFF_TBL3_14CH	-50.42 -50.40
1555	# CHECK TCS.NM_OFF_TBL3_15CH	-50.42 -50.40
1556	# CHECK TCS.NM_OFF_TBL3_16CH	-50.42 -50.40

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	46	1

1557	CHECK TCS. NM_OFF_TBL3_17CH	-18.73 -17.73
1558	CHECK TCS. NM_OFF_TBL3_18CH	-18.73 -17.73
1559	CHECK TCS. NM_OFF_TBL3_19CH	-18.73 -17.73
1560	CHECK TCS. NM_OFF_TBL3_20CH	-18.73 -17.73
1561	CHECK TCS. NM_OFF_TBL3_21CH	-18.73 -17.73
1562	CHECK TCS. NM_OFF_TBL3_22CH	6.41 7.41
1563	CHECK TCS. NM_OFF_TBL3_23CH	-13.6 -12.6
1564	CHECK TCS. NM_OFF_TBL3_24CH	69.51 70.51
1565	CHECK TCS. NM_OFF_TBL3_25CH	-18.73 -17.73
1566	CHECK TCS. NM_OFF_TBL3_26CH	-18.73 -17.73
1567	CHECK TCS. NM_OFF_TBL3_27CH	-18.73 -17.73
1568	CHECK TCS. NM_OFF_TBL3_28CH	-18.72 -17.72
1569	CHECK TCS. NM_OFF_TBL3_29CH	6.41 7.41
1570	# CHECK TCS. NM_OFF_TBL3_30CH	-50.42 -50.40
1571	# CHECK TCS. NM_OFF_TBL3_31CH	-50.42 -50.40
1572	# CHECK TCS. NM_OFF_TBL3_32CH	-50.42 -50.40
1573	#	
1574	# 0x22	
1575	CHECK TCS. NM_ATSF_TBL_01CH	=ON
1576	CHECK TCS. NM_ATSF_TBL_02CH	=ON
1577	CHECK TCS. NM_ATSF_TBL_03CH	=ON
1578	CHECK TCS. NM_ATSF_TBL_04CH	=ON
1579	CHECK TCS. NM_ATSF_TBL_05CH	=ON
1580	CHECK TCS. NM_ATSF_TBL_06CH	=ON
1581	CHECK TCS. NM_ATSF_TBL_07CH	=ON
1582	CHECK TCS. NM_ATSF_TBL_08CH	=ON
1583	CHECK TCS. NM_ATSF_TBL_09CH	=ON
1584	CHECK TCS. NM_ATSF_TBL_10CH	=ON
1585	CHECK TCS. NM_ATSF_TBL_11CH	=ON
1586	CHECK TCS. NM_ATSF_TBL_12CH	=ON
1587	CHECK TCS. NM_ATSF_TBL_13CH	=ON
1588	CHECK TCS. NM_ATSF_TBL_14CH	=OFF
1589	CHECK TCS. NM_ATSF_TBL_15CH	=OFF
1590	CHECK TCS. NM_ATSF_TBL_16CH	=OFF
1591	CHECK TCS. NM_ATSF_TBL_17CH	=ON
1592	CHECK TCS. NM_ATSF_TBL_18CH	=ON
1593	CHECK TCS. NM_ATSF_TBL_19CH	=ON
1594	CHECK TCS. NM_ATSF_TBL_20CH	=ON
1595	CHECK TCS. NM_ATSF_TBL_21CH	=ON
1596	CHECK TCS. NM_ATSF_TBL_22CH	=ON
1597	CHECK TCS. NM_ATSF_TBL_23CH	=ON
1598	CHECK TCS. NM_ATSF_TBL_24CH	=ON
1599	CHECK TCS. NM_ATSF_TBL_25CH	=ON
1600	CHECK TCS. NM_ATSF_TBL_26CH	=ON
1601	CHECK TCS. NM_ATSF_TBL_27CH	=ON
1602	CHECK TCS. NM_ATSF_TBL_28CH	=ON
1603	CHECK TCS. NM_ATSF_TBL_29CH	=ON
1604	CHECK TCS. NM_ATSF_TBL_30CH	=OFF
1605	CHECK TCS. NM_ATSF_TBL_31CH	=OFF
1606	CHECK TCS. NM_ATSF_TBL_32CH	=OFF
1607	#	
1608	# 0x23	

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1609	CHECK TCS. NM_MASF_TBL_01CH	=ON
1610	CHECK TCS. NM_MASF_TBL_02CH	=ON
1611	CHECK TCS. NM_MASF_TBL_03CH	=ON
1612	CHECK TCS. NM_MASF_TBL_04CH	=ON
1613	CHECK TCS. NM_MASF_TBL_05CH	=ON
1614	CHECK TCS. NM_MASF_TBL_06CH	=ON
1615	CHECK TCS. NM_MASF_TBL_07CH	=ON
1616	CHECK TCS. NM_MASF_TBL_08CH	=ON
1617	CHECK TCS. NM_MASF_TBL_09CH	=ON
1618	CHECK TCS. NM_MASF_TBL_10CH	=ON
1619	CHECK TCS. NM_MASF_TBL_11CH	=ON
1620	CHECK TCS. NM_MASF_TBL_12CH	=ON
1621	CHECK TCS. NM_MASF_TBL_13CH	=ON
1622	CHECK TCS. NM_MASF_TBL_14CH	=OFF
1623	CHECK TCS. NM_MASF_TBL_15CH	=OFF
1624	CHECK TCS. NM_MASF_TBL_16CH	=OFF
1625	CHECK TCS. NM_MASF_TBL_17CH	=ON
1626	CHECK TCS. NM_MASF_TBL_18CH	=ON
1627	CHECK TCS. NM_MASF_TBL_19CH	=ON
1628	CHECK TCS. NM_MASF_TBL_20CH	=ON
1629	CHECK TCS. NM_MASF_TBL_21CH	=ON
1630	CHECK TCS. NM_MASF_TBL_22CH	=ON
1631	CHECK TCS. NM_MASF_TBL_23CH	=ON
1632	CHECK TCS. NM_MASF_TBL_24CH	=ON
1633	CHECK TCS. NM_MASF_TBL_25CH	=ON
1634	CHECK TCS. NM_MASF_TBL_26CH	=ON
1635	CHECK TCS. NM_MASF_TBL_27CH	=ON
1636	CHECK TCS. NM_MASF_TBL_28CH	=ON
1637	CHECK TCS. NM_MASF_TBL_29CH	=ON
1638	CHECK TCS. NM_MASF_TBL_30CH	=OFF
1639	CHECK TCS. NM_MASF_TBL_31CH	=OFF
1640	CHECK TCS. NM_MASF_TBL_32CH	=OFF
1641	#	
1642	# 0x24	
1643	CHECK TCS. HTR_PWR_01CH	21.86 21.88
1644	CHECK TCS. HTR_PWR_02CH	14.47 14.49
1645	CHECK TCS. HTR_PWR_03CH	14.5 14.52
1646	CHECK TCS. HTR_PWR_04CH	7.18 7.2
1647	CHECK TCS. HTR_PWR_05CH	7.38 7.4
1648	CHECK TCS. HTR_PWR_06CH	23.74 23.76
1649	CHECK TCS. HTR_PWR_07CH	7.38 7.4
1650	CHECK TCS. HTR_PWR_08CH	16.55 16.57
1651	CHECK TCS. HTR_PWR_09CH	21.67 21.69
1652	CHECK TCS. HTR_PWR_10CH	21.67 21.69
1653	CHECK TCS. HTR_PWR_11CH	14.45 14.47
1654	CHECK TCS. HTR_PWR_12CH	21.46 21.48
1655	CHECK TCS. HTR_PWR_13CH	23.96 23.98
1656	CHECK TCS. HTR_PWR_14CH	-0.01 0.01
1657	CHECK TCS. HTR_PWR_15CH	-0.01 0.01
1658	CHECK TCS. HTR_PWR_16CH	-0.01 0.01
1659	CHECK TCS. HTR_PWR_17CH	7.15 7.17
1660	CHECK TCS. HTR_PWR_18CH	14.46 14.48

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	48	1

1661	CHECK TCS. HTR_PWR_19CH	21. 6 21. 62
1662	CHECK TCS. HTR_PWR_20CH	14. 45 14. 47
1663	CHECK TCS. HTR_PWR_21CH	7. 15 7. 17
1664	CHECK TCS. HTR_PWR_22CH	24. 06 24. 08
1665	CHECK TCS. HTR_PWR_23CH	7. 38 7. 4
1666	CHECK TCS. HTR_PWR_24CH	6. 55 6. 57
1667	CHECK TCS. HTR_PWR_25CH	21. 69 21. 71
1668	CHECK TCS. HTR_PWR_26CH	21. 69 21. 71
1669	CHECK TCS. HTR_PWR_27CH	14. 33 14. 35
1670	CHECK TCS. HTR_PWR_28CH	21. 56 21. 58
1671	CHECK TCS. HTR_PWR_29CH	24 24. 02
1672	CHECK TCS. HTR_PWR_30CH	-0. 01 0. 01
1673	CHECK TCS. HTR_PWR_31CH	-0. 01 0. 01
1674	CHECK TCS. HTR_PWR_32CH	-0. 01 0. 01
1675	#	
1676	# 0x25	
1677	CHECK TCS. HTR_ENADIS_01CH	=ENA
1678	CHECK TCS. HTR_ENADIS_02CH	=ENA
1679	CHECK TCS. HTR_ENADIS_03CH	=ENA
1680	CHECK TCS. HTR_ENADIS_04CH	=ENA
1681	CHECK TCS. HTR_ENADIS_05CH	=ENA
1682	CHECK TCS. HTR_ENADIS_06CH	=ENA
1683	CHECK TCS. HTR_ENADIS_07CH	=ENA
1684	CHECK TCS. HTR_ENADIS_08CH	=ENA
1685	CHECK TCS. HTR_ENADIS_09CH	=ENA
1686	CHECK TCS. HTR_ENADIS_10CH	=ENA
1687	CHECK TCS. HTR_ENADIS_11CH	=ENA
1688	CHECK TCS. HTR_ENADIS_12CH	=ENA
1689	CHECK TCS. HTR_ENADIS_13CH	=ENA
1690	CHECK TCS. HTR_ENADIS_14CH	=DIS
1691	CHECK TCS. HTR_ENADIS_15CH	=DIS
1692	CHECK TCS. HTR_ENADIS_16CH	=DIS
1693	CHECK TCS. HTR_ENADIS_17CH	=ENA
1694	CHECK TCS. HTR_ENADIS_18CH	=ENA
1695	CHECK TCS. HTR_ENADIS_19CH	=ENA
1696	CHECK TCS. HTR_ENADIS_20CH	=ENA
1697	CHECK TCS. HTR_ENADIS_21CH	=ENA
1698	CHECK TCS. HTR_ENADIS_22CH	=ENA
1699	CHECK TCS. HTR_ENADIS_23CH	=ENA
1700	CHECK TCS. HTR_ENADIS_24CH	=ENA
1701	CHECK TCS. HTR_ENADIS_25CH	=ENA
1702	CHECK TCS. HTR_ENADIS_26CH	=ENA
1703	CHECK TCS. HTR_ENADIS_27CH	=ENA
1704	CHECK TCS. HTR_ENADIS_28CH	=ENA
1705	CHECK TCS. HTR_ENADIS_29CH	=ENA
1706	CHECK TCS. HTR_ENADIS_30CH	=DIS
1707	CHECK TCS. HTR_ENADIS_31CH	=DIS
1708	CHECK TCS. HTR_ENADIS_32CH	=DIS
1709	#	
1710	# 0x26	
1711	CHECK TCS. HTR_SENR_SEL_01	23 23
1712	CHECK TCS. HTR_SENR_SEL_02	13 13

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1713	CHECK TCS. HTR_SENR_SEL_03	13 13
1714	CHECK TCS. HTR_SENR_SEL_04	7 7
1715	CHECK TCS. HTR_SENR_SEL_05	16 16
1716	CHECK TCS. HTR_SENR_SEL_06	1 1
1717	CHECK TCS. HTR_SENR_SEL_07	18 18
1718	CHECK TCS. HTR_SENR_SEL_08	3 3
1719	CHECK TCS. HTR_SENR_SEL_09	19 19
1720	CHECK TCS. HTR_SENR_SEL_10	24 24
1721	CHECK TCS. HTR_SENR_SEL_11	8 8
1722	CHECK TCS. HTR_SENR_SEL_12	26 26
1723	CHECK TCS. HTR_SENR_SEL_13	10 10
1724	CHECK TCS. HTR_SENR_SEL_14	11 11
1725	CHECK TCS. HTR_SENR_SEL_15	13 13
1726	CHECK TCS. HTR_SENR_SEL_16	14 14
1727	CHECK TCS. HTR_SENR_SEL_17	6 6
1728	CHECK TCS. HTR_SENR_SEL_18	14 14
1729	CHECK TCS. HTR_SENR_SEL_19	14 14
1730	CHECK TCS. HTR_SENR_SEL_20	12 12
1731	CHECK TCS. HTR_SENR_SEL_21	15 15
1732	CHECK TCS. HTR_SENR_SEL_22	4 4
1733	CHECK TCS. HTR_SENR_SEL_23	20 20
1734	CHECK TCS. HTR_SENR_SEL_24	15 15
1735	CHECK TCS. HTR_SENR_SEL_25	17 17
1736	CHECK TCS. HTR_SENR_SEL_26	17 17
1737	CHECK TCS. HTR_SENR_SEL_27	17 17
1738	CHECK TCS. HTR_SENR_SEL_28	17 17
1739	CHECK TCS. HTR_SENR_SEL_29	9 9
1740	CHECK TCS. HTR_SENR_SEL_30	9 9
1741	CHECK TCS. HTR_SENR_SEL_31	5 5
1742	CHECK TCS. HTR_SENR_SEL_32	21 21
1743	CHECK TCS. HTR_SENR_SEL_33	22 22
1744	CHECK TCS. HTR_SENR_SEL_34	15 15
1745	CHECK TCS. HTR_SENR_SEL_35	29 29
1746	CHECK TCS. HTR_SENR_SEL_36	29 29
1747	CHECK TCS. HTR_SENR_SEL_37	29 29
1748	CHECK TCS. HTR_SENR_SEL_38	0 0
1749	CHECK TCS. HTR_SENR_SEL_39	2 2
1750	CHECK TCS. HTR_SENR_SEL_40	30 30
1751	CHECK TCS. HTR_SENR_SEL_41	25 25
1752	CHECK TCS. HTR_SENR_SEL_42	27 27
1753	CHECK TCS. HTR_SENR_SEL_43	28 28
1754	CHECK TCS. HTR_SENR_SEL_44	30 30
1755	CHECK TCS. HTR_SENR_SEL_45	30 30
1756	CHECK TCS. HTR_SENR_SEL_46	31 31
1757	CHECK TCS. HTR_SENR_SEL_47	31 31
1758	CHECK TCS. HTR_SENR_SEL_48	31 31
1759	#	
1760	# 0x27	
1761	CHECK TCS. SENR_LSB_SEL_01	0 0
1762	CHECK TCS. SENR_LSB_SEL_02	0 0
1763	CHECK TCS. SENR_LSB_SEL_03	1 1
1764	CHECK TCS. SENR_LSB_SEL_04	1 1

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1765	CHECK TCS. SENR_LSB_SEL_05	0 0
1766	CHECK TCS. SENR_LSB_SEL_06	0 0
1767	CHECK TCS. SENR_LSB_SEL_07	0 0
1768	CHECK TCS. SENR_LSB_SEL_08	0 0
1769	CHECK TCS. SENR_LSB_SEL_09	0 0
1770	CHECK TCS. SENR_LSB_SEL_10	0 0
1771	CHECK TCS. SENR_LSB_SEL_11	0 0
1772	CHECK TCS. SENR_LSB_SEL_12	0 0
1773	CHECK TCS. SENR_LSB_SEL_13	0 0
1774	CHECK TCS. SENR_LSB_SEL_14	0 0
1775	CHECK TCS. SENR_LSB_SEL_15	1 1
1776	CHECK TCS. SENR_LSB_SEL_16	1 1
1777	CHECK TCS. SENR_LSB_SEL_17	0 0
1778	CHECK TCS. SENR_LSB_SEL_18	0 0
1779	CHECK TCS. SENR_LSB_SEL_19	1 1
1780	CHECK TCS. SENR_LSB_SEL_20	0 0
1781	CHECK TCS. SENR_LSB_SEL_21	0 0
1782	CHECK TCS. SENR_LSB_SEL_22	0 0
1783	CHECK TCS. SENR_LSB_SEL_23	0 0
1784	CHECK TCS. SENR_LSB_SEL_24	0 0
1785	CHECK TCS. SENR_LSB_SEL_25	0 0
1786	CHECK TCS. SENR_LSB_SEL_26	0 0
1787	CHECK TCS. SENR_LSB_SEL_27	0 0
1788	CHECK TCS. SENR_LSB_SEL_28	0 0
1789	CHECK TCS. SENR_LSB_SEL_29	0 0
1790	CHECK TCS. SENR_LSB_SEL_30	0 0
1791	CHECK TCS. SENR_LSB_SEL_31	0 0
1792	CHECK TCS. SENR_LSB_SEL_32	0 0
1793	CHECK TCS. SENR_LSB_SEL_33	0 0
1794	CHECK TCS. SENR_LSB_SEL_34	2 2
1795	CHECK TCS. SENR_LSB_SEL_35	2 2
1796	CHECK TCS. SENR_LSB_SEL_36	2 2
1797	CHECK TCS. SENR_LSB_SEL_37	2 2
1798	CHECK TCS. SENR_LSB_SEL_38	0 0
1799	CHECK TCS. SENR_LSB_SEL_39	0 0
1800	CHECK TCS. SENR_LSB_SEL_40	2 2
1801	CHECK TCS. SENR_LSB_SEL_41	0 0
1802	CHECK TCS. SENR_LSB_SEL_42	0 0
1803	CHECK TCS. SENR_LSB_SEL_43	0 0
1804	CHECK TCS. SENR_LSB_SEL_44	2 2
1805	CHECK TCS. SENR_LSB_SEL_45	2 2
1806	CHECK TCS. SENR_LSB_SEL_46	2 2
1807	CHECK TCS. SENR_LSB_SEL_47	2 2
1808	CHECK TCS. SENR_LSB_SEL_48	0 0
1809	CHECK TCS. SENR_LSB1	0. 505 0. 515
1810	CHECK TCS. SENR_LSB2	1. 015 1. 025
1811	CHECK TCS. SENR_LSB3	2. 535 2. 545
1812	#	
1813	# 0x28	
1814	CHECK TCS. SENR_SEL_01	0 0
1815	CHECK TCS. SENR_SEL_02	1 1
1816	CHECK TCS. SENR_SEL_03	2 2

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1817	CHECK TCS. SENR_SEL_04	3 3
1818	CHECK TCS. SENR_SEL_05	4 4
1819	CHECK TCS. SENR_SEL_06	5 5
1820	CHECK TCS. SENR_SEL_07	6 6
1821	CHECK TCS. SENR_SEL_08	7 7
1822	CHECK TCS. SENR_SEL_09	8 8
1823	CHECK TCS. SENR_SEL_10	9 9
1824	CHECK TCS. SENR_SEL_11	10 10
1825	CHECK TCS. SENR_SEL_12	11 11
1826	CHECK TCS. SENR_SEL_13	12 12
1827	CHECK TCS. SENR_SEL_14	13 13
1828	CHECK TCS. SENR_SEL_15	14 14
1829	CHECK TCS. SENR_SEL_16	15 15
1830	CHECK TCS. SENR_SEL_17	16 16
1831	CHECK TCS. SENR_SEL_18	17 17
1832	CHECK TCS. SENR_SEL_19	18 18
1833	CHECK TCS. SENR_SEL_20	30 30
1834	CHECK TCS. SENR_SEL_21	20 20
1835	CHECK TCS. SENR_SEL_22	21 21
1836	CHECK TCS. SENR_SEL_23	22 22
1837	CHECK TCS. SENR_SEL_24	23 23
1838	CHECK TCS. SENR_SEL_25	24 24
1839	CHECK TCS. SENR_SEL_26	25 25
1840	CHECK TCS. SENR_SEL_27	26 26
1841	CHECK TCS. SENR_SEL_28	27 27
1842	CHECK TCS. SENR_SEL_29	28 28
1843	CHECK TCS. SENR_SEL_30	29 29
1844	CHECK TCS. SENR_SEL_31	30 30
1845	CHECK TCS. SENR_SEL_32	31 31
1846	CHECK TCS. SENR_SEL_33	32 32
1847	CHECK TCS. SENR_SEL_34	33 33
1848	CHECK TCS. SENR_SEL_35	34 34
1849	CHECK TCS. SENR_SEL_36	35 35
1850	CHECK TCS. SENR_SEL_37	36 36
1851	CHECK TCS. SENR_SEL_38	37 37
1852	CHECK TCS. SENR_SEL_39	38 38
1853	CHECK TCS. SENR_SEL_40	39 39
1854	CHECK TCS. SENR_SEL_41	40 40
1855	CHECK TCS. SENR_SEL_42	41 41
1856	CHECK TCS. SENR_SEL_43	31 31
1857	CHECK TCS. SENR_SEL_44	43 43
1858	CHECK TCS. SENR_SEL_45	44 44
1859	CHECK TCS. SENR_SEL_46	45 45
1860	CHECK TCS. SENR_SEL_47	46 46
1861	CHECK TCS. SENR_SEL_48	47 47
1862	#	
1863	#	
1864	# QL	Check-Item Spec Check
1865	#	-----+-----+-----
1866	# CMD 出力装置	Message STATUS CHECK 結果 OK G N
1867	#	
1868	#	=====

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1869 . # 4.3. TCS NMNL Mode Check
1870 # =====
1871 . #
1872 . TCS.SET_PRM_ENA
1873 #
1874 . # < CHECK >
1875 CHECK TCS.SET_PRM_ST =ENA
1876 . #
1877 . TCS.SET_PEAK_PWR 0x2328
1878 WAIT_SEC 2
1879 TCS.CTRL_PKPWR_EXE
1880 . #
1881 . # < CHECK >
1882 CHECK TCS.SET_POWER 90
1883 . #
1884 . TCS.CTRL_HTR_P1_ENA
1885 WAIT_SEC 2
1886 TCS.CTRL_HTR_P2_ENA
1887 WAIT_SEC 2
1888 TCS.CTRL_HTR_P3_ENA
1889 WAIT_SEC 2
1890 TCS.CTRL_HTR_P4_ENA
1891 #
1892 . # < CHECK >
1893 CHECK TCS.HTR01_P_VOL_ST =ON
1894 CHECK TCS.HTR09_P_VOL_ST =ON
1895 CHECK TCS.HTR17_P_VOL_ST =ON
1896 CHECK TCS.HTR25_P_VOL_ST =ON
1897 #
1898 . # < TABLE 選択 >
1899 . TCS.SET_NMNL_SEL_TBL_1
1900 WAIT_SEC 2
1901 TCS.SET_ATSF_SEL_TBL_1
1902 WAIT_SEC 2
1903 TCS.SET_BKUP_SEL_TBL_1
1904 #
1905 . # < CHECK >
1906 CHECK TCS.SET_NMNL_TBL =TBL1
1907 CHECK TCS.SET_SAFE_TBL1 =ENA
1908 CHECK TCS.SET_SAFE_TBL2 =DIS
1909 CHECK TCS.SET_SAFE_TBL3 =DIS
1910 CHECK TCS.SET_SAFE_TBL4 =DIS
1911 CHECK TCS.SET_BKUP_TBL1 =ENA
1912 CHECK TCS.SET_BKUP_TBL2 =DIS
1913 CHECK TCS.SET_BKUP_TBL3 =DIS
1914 CHECK TCS.SET_BKUP_TBL4 =DIS
1915 #
1916 . # < ピーク電力制御 ON >
1917 . TCS.CTRL_PKPWR_ON
1918 #
1919 . # < CHECK >
1920 CHECK TCS.PPWR_CTR =ON

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1921  #
1922  . # < ノミナル制御モード移行 >
1923  . TCS.MODE_NMNL
1924  #
1925  . # < CHECK >
1926  CHECK TCS.MODE_ST                =NMNL
1927  #
1928  . # QL                Check-Item                Spec                Check
1929  # -----+-----+-----+-----
1930  # TCS_MONITOR    TCS.HTR24_VOL_ST    32 秒間のどこかで ON    G    N
1931  #
1932  #
1933  . # QL                Check-Item                Spec                Check
1934  # -----+-----+-----+-----
1935  # CMD 出力装置    Message                STATUS CHECK 結果 OK    G    N
1936  #
1937  # =====
1938  . # 4.4. TCS STBY/DRU OFF
1939  # =====
1940  . #
1941  . TCS.MODE_STBY
1942  #
1943  . # < CHECK >
1944  CHECK TCS.MODE_ST                =STBY
1945  #
1946  . #
1947  . TCS.CTRL_HTR_P1_DIS
1948  WAIT_SEC 2
1949  TCS.CTRL_HTR_P2_DIS
1950  WAIT_SEC 2
1951  TCS.CTRL_HTR_P3_DIS
1952  WAIT_SEC 2
1953  TCS.CTRL_HTR_P4_DIS
1954  #
1955  . # < CHECK >
1956  CHECK TCS.HTR01_P_VOL_ST                =OFF
1957  CHECK TCS.HTR09_P_VOL_ST                =OFF
1958  CHECK TCS.HTR17_P_VOL_ST                =OFF
1959  CHECK TCS.HTR25_P_VOL_ST                =OFF
1960  #
1961  . #
1962  . ACS.DRU_SEL_NA
1963  #
1964  . # < CHECK >
1965  CHECK ACS_SYS.DRU_SEL_PT                =NA
1966  CHECK ACS.DRU_SEL_PT                =NA
1967  CHECK ACS.BIT_DRU_SEL_PT                =NA
1968  #
1969  . #
1970  . PCD.ON_OFF_ENA
1971  #
1972  . # < CHECK >

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```
1973 CHECK PCD.ON_OFF_ENA_DIS =ENA
1974 #
1975 #
1976 PCD.DRU_PSU_A_OFF
1977 WAIT_SEC 1
1978 PCD.ON_OFF_DIS
1979 #
1980 # < CHECK >
1981 CHECK PCD.DRU_PSU_A_ON_OFF =OFF
1982 CHECK PCD.ON_OFF_ENA_DIS =DIS
1983 #
1984 # QL Check-Item Spec Check
1985 # -----+-----+-----+-----
1986 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
1987 #
1988 #
1989 # *****
1990 # 5. MDP Load/Dump
1991 # *****
1992 # =====
1993 # 5.1. MDP 立ち上げ
1994 # =====
1995 #
1996 # MDP-DPU1,2(A系)のLINK Enableを確認
1997 # < CHECK >
1998 CHECK DH.SPW_POTMD_L1 =ENA
1999 CHECK DH.SPW_POTMD_L2 =ENA
2000 #
2001 # MDP-DPU1,2を電源ONする。(HW_STATE1は16秒以内にチェックすること。)
2002 #
2003 # <<< MDP-DPU1/2 Power ON >>>
2004 #
2005 # DH.MDP_DPU12_POWER_ON # DH.MC_EACH_EXE 0x099
2006 #: << MACRO >> 内容について以下に示す(DH.DUMMYは省略)
2007 #: 0x00 PCD.ON_OFF_ENA
2008 #: 0x01 PCD.PSU_MDP1_ON
2009 #: 0x02 PCD.PSU_MDP2_ON
2010 #: 0x03 PCD.ON_OFF_DIS
2011 #
2012 CHECK DH.CMD_EXE_CNT -updatewait 6
2013 WAIT_SEC 8
2014 CHECK MD1F.DPU_HW_STATE1 =DPU_RESET
2015 CHECK MD2F.DPU_HW_STATE1 =DPU_RESET
2016 CHECK PCD.ON_OFF_ENA_DIS =DIS
2017 CHECK PCD.PSU_MDP1_ON_OFF =ON
2018 CHECK PCD.PSU_MDP2_ON_OFF =ON
2019 CHECK DH.SPW_LNKST_L1 =RUN
2020 CHECK DH.SPW_LNKST_L2 =RUN
2021 #
2022 # *****
2023 # WAIT 16 sec (HW Initialization of DPU1 & DPU2)
2024 # *****
```

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```
2025 WAIT_SEC 13
2026 #
2027 # <<< MDP-DPU Initial HK check (2): 16 sec after PWR-ON >>>
2028 #
2029 CHECK MD1F.DPU_HW_STATE1 =DPU_RUN
2030 CHECK MD1F.DPU_HW_STATE2 =CPUOFF_DPUBUS
2031 CHECK MD1F.DMC_IF_RMAPERR =na
2032 CHECK MD1F.DMC_IF_2BITERR =na
2033 CHECK MD1F.DMC_IF_BTOERR =na
2034 CHECK MD1F.BTO_ERR =na
2035 CHECK MD1F.WDT_ERR =na
2036 CHECK MD1F.DPU_SELECT =DPU1
2037 CHECK MD2F.DPU_HW_STATE1 =DPU_RUN
2038 CHECK MD2F.DPU_HW_STATE2 =CPUOFF_DPUBUS
2039 CHECK MD2F.DMC_IF_RMAPERR =na
2040 CHECK MD2F.DMC_IF_2BITERR =na
2041 CHECK MD2F.DMC_IF_BTOERR =na
2042 CHECK MD2F.BTO_ERR =na
2043 CHECK MD2F.WDT_ERR =na
2044 CHECK MD2F.DPU_SELECT =DPU2
2045 . #
2046 . DH.SPW_RTEFDIR_SET 0x1E
2047 #
2048 . # < CHECK >
2049 CHECK DH.SPW_FDIR_ENA_MDP1 =ENA
2050 CHECK DH.SPW_FDIR_ENA_MDP2 =ENA
2051 CHECK DH.SPW_FDIR_ENA_PCD =ENA
2052 CHECK DH.SPW_FDIR_ENA_RMTTRR =ENA
2053 #
2054 . # QL Check-Item Spec Check
2055 # -----+-----+-----+-----
2056 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2057 #
2058 . # =====
2059 . # 5.2. MDP プログラム書き換え
2060 . # =====
2061 . # -----
2062 . # 5.2.1. DPU1
2063 . # -----
2064 #
2065 . # < DPU1 EEPROM 書き込み準備 >
2066 . MD1F.BUS_ACC_ENA
2067 #
2068 . # < CHECK >
2069 CHECK MD1F.DPU_HW_STATE2 =CPUOFF_DMCBUS
2070 #
2071 . # [Memory load to DPU1 RAM ID=DPU1-PRG-ROM]
2072 . # Load ADDRESS 1FC00000 - 1FCFFFFF
2073 #
2074 . # < DPU1 EEPROM 書き込み >
2075 . # 【注意】最適化を有効にして書き込みのこと
2076 . LOAD ram-041_NRM #Ver. 7.5
```

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2077  #
2078  . # 書き込み中は下記 STATUS をチェックする。
2079  . # QL          Check-Item          Spec          Check
2080  # -----+-----+-----+-----
2081  # MD1F_OBS_HK    << DPU1 H/W-HK >>
2082  #                DMC_IF_RMAPERR      na              G  N
2083  #                DMC_IF_2BITERR      na              G  N
2084  #                DMC_IF_BTOERR      na              G  N
2085  #                HWREJ_CNT            0               G  N
2086  #                ACC_REJ              na              G  N
2087  #                CODE_REJ            na              G  N
2088  #                STATE_REJ           na              G  N
2089  #                TID_REJ             na              G  N
2090  #
2091  . # [DPU1 DUMP Address=1FCFFFFF Length=1M]
2092  #
2093  . # < DPU1 EEPROM データ照合 >
2094  . DH.MEM_DUMP 0x00 0x03 0x1FC00000 0x100000
2095  #
2096  . # QL          Check-Item          Spec          Check
2097  # -----+-----+-----+-----
2098  #                照合結果              OK              G  N
2099  . #
2100  . MD1F.BUS_ACC_DIS
2101  #
2102  . # < CHECK >
2103  CHECK MD1F.DPU_HW_STATE2              =CPUOFF_DPUBUS
2104  #
2105  . # QL          Check-Item          Spec          Check
2106  # -----+-----+-----+-----
2107  # CMD 出力装置  Message              STATUS CHECK 結果 OK      G  N
2108  #
2109  . # -----
2110  . # 5.2.2. DPU2
2111  . # -----
2112  #
2113  . # < DPU2 EEPROM 書き込み準備 >
2114  . MD2F.BUS_ACC_ENA
2115  #
2116  . # < CHECK >
2117  CHECK MD2F.DPU_HW_STATE2              =CPUOFF_DMCBUS
2118  #
2119  . # [Memory load to DPU2 RAM ID=DPU2-PRG-ROM]
2120  . # Load ADDRESS 1FC00000 - 1FCFFFFF
2121  #
2122  . # < DPU2 EEPROM 書き込み >
2123  . # 【注意】最適化を有効にして書き込みのこと
2124  . LOAD ram-043_NRM                      #Ver. 7.5
2125  #
2126  . # 書き込み中は下記 STATUS をチェックする。
2127  . # QL          Check-Item          Spec          Check
2128  # -----+-----+-----+-----

```

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2129 # MD2F_OBS_HK << DPU2 H/W-HK >>
2130 # DMC_IF_RMAPERR na G N
2131 # DMC_IF_2BITERR na G N
2132 # DMC_IF_BTOERR na G N
2133 # HWREJ_CNT 0 G N
2134 # ACC_REJ na G N
2135 # CODE_REJ na G N
2136 # STATE_REJ na G N
2137 # TID_REJ na G N
2138 #
2139 . # [DPU2 DUMP Address=1FCFFFFFF Length=1M]
2140 #
2141 . # < DPU2 EEPROM データ照合 >
2142 . DH.MEM_DUMP 0x00 0x04 0x1FC00000 0x100000
2143 #
2144 . # QL Check-Item Spec Check
2145 # -----+-----+-----+-----
2146 # 照合結果 OK G N
2147 #
2148 . #
2149 . MD2F.BUS_ACC_DIS
2150 #
2151 . # < CHECK >
2152 CHECK MD2F.DPU_HW_STATE2 =CPUOFF_DPUBUS
2153 #
2154 . # QL Check-Item Spec Check
2155 # -----+-----+-----+-----
2156 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2157 #
2158 . # =====
2159 . # 5.3. MDP 立ち下げ
2160 . # =====
2161 #
2162 . # < MDP-DPU1/2 Power OFF >
2163 . DH.MDP_DPU12_POWER_OFF # DH.MC_EACH_EXE 0x0B0
2164 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
2165 #: 0x00 DH.ATMC_EACH_DIS 0x34
2166 #: 0x01 DH.SPW_RTEFDIR_SET 0x18
2167 #: 0x03 DH.ATMC_EACH_DIS 0x36
2168 #: 0x06 DH.ATMC_EACH_DIS 0x32
2169 #: 0x09 DH.ATMC_EACH_DIS 0x33
2170 #: 0x0A PCD.ON_OFF_ENA
2171 #: 0x0B PCD.PSU_MDP1_OFF
2172 #: 0x0C DH.ATMC_EACH_DIS 0x30
2173 #: 0x0D PCD.PSU_MDP2_OFF
2174 #: 0x0E PCD.ON_OFF_DIS
2175 #: 0x0F DH.ATMC_EACH_DIS 0x31
2176 #
2177 . # < CHECK >
2178 CHECK DH.ATMC_ENA_DIS30 =DIS
2179 CHECK DH.ATMC_ENA_DIS31 =DIS
2180 CHECK DH.ATMC_ENA_DIS32 =DIS

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2181 CHECK DH. ATMC_ENA_DIS33 =DIS
2182 CHECK DH. ATMC_ENA_DIS34 =DIS
2183 CHECK DH. ATMC_ENA_DIS36 =DIS
2184 CHECK DH. SPW_FDIR_ENA_MDP1 =DIS
2185 CHECK DH. SPW_FDIR_ENA_MDP2 =DIS
2186 CHECK DH. SPW_FDIR_ENA_PCD =ENA
2187 CHECK DH. SPW_FDIR_ENA_RMTRTR =ENA
2188 CHECK PCD. PSU_MDP1_ON_OFF =OFF
2189 CHECK PCD. PSU_MDP2_ON_OFF =OFF
2190 CHECK PCD. ON_OFF_ENA_DIS =DIS
2191 . #
2192 . #
2193 . # < CHECK >
2194 CHECK PCD. BAT_CV_ON_OFF -
2195 CHECK PCD. SA_V -
2196 CHECK PCD. SA_I -
2197 CHECK PCD. APR1_I -
2198 CHECK PCD. APR2_I -
2199 CHECK PCD. BUS_V -
2200 CHECK PCD. LOAD_I -
2201 CHECK PCD. CHG_I -
2202 CHECK PCD. DCHG_I -
2203 CHECK PCD. BAT_V -
2204 CHECK PCD. CEL1_V -
2205 CHECK PCD. CEL2_V -
2206 CHECK PCD. CEL3_V -
2207 CHECK PCD. CEL4_V -
2208 CHECK PCD. CEL5_V -
2209 CHECK PCD. CEL6_V -
2210 CHECK PCD. CEL7_V -
2211 CHECK PCD. CEL8_V -
2212 CHECK PCD. CEL9_V -
2213 CHECK PCD. CEL10_V -
2214 CHECK PCD. CEL11_V -
2215 CHECK PCD. CEL12_V -
2216 . #
2217 . # QL Check-Item Spec Check
2218 . # -----+-----+-----+-----
2219 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
2220 . #
2221 . #
2222 . # *****
2223 . # 6. MDP Check
2224 . # *****
2225 . # =====
2226 . # 6.1. MDP 立ち上げ
2227 . # =====
2228 . #
2229 . # # AT 機能を ENA にする
2230 . DH. ATMC_ENA
2231 . #
2232 . # < CHECK >

```

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2233 CHECK DH. ATMC_ENA_DIS =ENA
2234 . #
2235 . # # RQ waiting に設定する。
2236 . DH. RQMC_ENA
2237 . #
2238 . # < CHECK >
2239 CHECK DH. RQMC_ENA_DIS =ENA
2240 . #
2241 . # # システムタイマー機能を有効にする。
2242 . DH. SYSTMTR_ENA
2243 . #
2244 . # < CHECK >
2245 CHECK DH. SYSTMTR_ENA_DIS =ENA
2246 . #
2247 . # MDP-DPU1,2(A系) の LINK Enable を確認
2248 . #
2249 . # < CHECK >
2250 CHECK DH. SPW_POTMD_L1 =ENA
2251 CHECK DH. SPW_POTMD_L2 =ENA
2252 . #
2253 . # MDP-DPU1,2 を電源 ON する。(HW_STATE1 は 16 秒以内にチェックすること。)
2254 . #
2255 . # <<< MDP-DPU1/2 Power ON >>>
2256 . #
2257 . DH. MDP_DPU12_ON # DH. MC_EACH_EXE 0x099
2258 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
2259 #: 0x00 PCD. ON_OFF_ENA
2260 #: 0x01 PCD. PSU_MDP1_ON
2261 #: 0x02 PCD. PSU_MDP2_ON
2262 #: 0x03 PCD. ON_OFF_DIS
2263 . #
2264 CHECK DH. CMD_EXE_CNT -updatewait 6
2265 WAIT_SEC 8
2266 CHECK MD1F. DPU_HW_STATE1 =DPU_RESET
2267 CHECK MD2F. DPU_HW_STATE1 =DPU_RESET
2268 CHECK PCD. ON_OFF_ENA_DIS =DIS
2269 CHECK PCD. PSU_MDP1_ON_OFF =ON
2270 CHECK PCD. PSU_MDP2_ON_OFF =ON
2271 CHECK DH. SPW_LNKST_L1 =RUN
2272 CHECK DH. SPW_LNKST_L2 =RUN
2273 . #
2274 . # *****
2275 . # WAIT 16 sec (HW Initialization of DPU1 & DPU2)
2276 . # *****
2277 WAIT_SEC 13
2278 . #
2279 . # <<< MDP-DPU Initial HK check (2): 16 sec after PWR-ON >>>
2280 . #
2281 CHECK MD1F. DPU_HW_STATE1 =DPU_RUN
2282 CHECK MD1F. DPU_HW_STATE2 =CPUOFF_DPUBUS
2283 CHECK MD1F. DMC_IF_RMAPERR =na
2284 CHECK MD1F. DMC_IF_2BITERR =na

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2285 CHECK MD1F.DMC_IF_BTERR =na
2286 CHECK MD1F.BTO_ERR =na
2287 CHECK MD1F.WDT_ERR =na
2288 CHECK MD1F.DPU_SELECT =DPU1
2289 CHECK MD2F.DPU_HW_STATE1 =DPU_RUN
2290 CHECK MD2F.DPU_HW_STATE2 =CPUOFF_DPUBUS
2291 CHECK MD2F.DMC_IF_RMAPERR =na
2292 CHECK MD2F.DMC_IF_2BITERR =na
2293 CHECK MD2F.DMC_IF_BTERR =na
2294 CHECK MD2F.BTO_ERR =na
2295 CHECK MD2F.WDT_ERR =na
2296 CHECK MD2F.DPU_SELECT =DPU2
2297 #
2298 . # <<< MDP-DPU CPU_ON / RUN >>>
2299 . #
2300 . DH.MDP_DPU12_CPU_RUN # DH.MC_EACH_EXE 0x09A
2301 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
2302 #: 0x00 DH.ATMC_EACH_ENA 0x34
2303 #: 0x01 DH.SPW_RTEFDIR_SET 0x1E
2304 #: 0x05 DH.ATMC_EACH_ENA 0x36
2305 #: 0x06 MD1F.CPU_ON
2306 #: 0x07 MD2F.CPU_ON
2307 #: 0x08 MD1F.CPU_RUN
2308 #: 0x09 MD2F.CPU_RUN
2309 #: 0x0A DH.ATMC_EACH_ENA 0x30
2310 #: 0x0F DH.ATMC_EACH_ENA 0x31
2311 #
2312 CHECK DH.CMD_EXE_CNT -updatewait 30
2313 WAIT_SEC 5
2314 # < CHECK >
2315 CHECK DH.SPW_FDIR_ENA_MDP1 =ENA
2316 CHECK DH.SPW_FDIR_ENA_MDP2 =ENA
2317 CHECK DH.SPW_FDIR_ENA_PCD =ENA
2318 CHECK DH.SPW_FDIR_ENA_RMTRTR =ENA
2319 CHECK DH.ATMC_ENA_DIS30 =ENA
2320 CHECK DH.ATMC_ENA_DIS31 =ENA
2321 CHECK DH.ATMC_ENA_DIS34 =ENA
2322 CHECK DH.ATMC_ENA_DIS36 =ENA
2323 CHECK MD1F.DPU_HW_STATE2 =CPU_RUN
2324 CHECK MD1F.DPU_CMD_CODE 0x0701
2325 CHECK MD2F.DPU_HW_STATE2 =CPU_RUN
2326 CHECK MD2F.DPU_CMD_CODE 0x0701
2327 #
2328 #
2329 # < MDP1 SATELITE TIME CHECK (0x200/s) >
2330 #
2331 CHECK MD1F.SAT_TIME -updatewait 180
2332 LET LocalA0 = {MD1F.SAT_TIME} & 0xFFFFFFFF
2333 CHECK MD1F.SAT_TIME -updatewait 4
2334 LET LocalA1 = {MD1F.SAT_TIME} & 0xFFFFFFFF
2335 LET LocalA1 = LocalA1 - LocalA0
2336 SYSTEM test {LocalA1} = 512

```

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2337 #
2338 # < MDP1 SUNPULSE TIME CHECK (0x800-0xB00/4s) >
2339 #
2340 CHECK MD1F.SUNPULSE_TIME -updatewait 8
2341 LET LocalA0 = {MD1F.SUNPULSE_TIME} & 0xFFFFFFFF
2342 CHECK MD1F.SUNPULSE_TIME -updatewait 8
2343 LET LocalA1 = {MD1F.SUNPULSE_TIME} & 0xFFFFFFFF
2344 LET LocalA1 = LocalA1 - LocalA0
2345 SYSTEM test {LocalA1} -ge 2048 -a {LocalA1} -le 2816
2346 #
2347 # < MDP2 SATELITE TIME CHECK (0x200/s) >
2348 #
2349 CHECK MD2F.SAT_TIME -updatewait 8
2350 LET LocalA0 = {MD2F.SAT_TIME} & 0xFFFFFFFF
2351 CHECK MD2F.SAT_TIME -updatewait 4
2352 LET LocalA1 = {MD2F.SAT_TIME} & 0xFFFFFFFF
2353 LET LocalA1 = LocalA1 - LocalA0
2354 SYSTEM test {LocalA1} = 512
2355 #
2356 # < MDP2 SUNPULSE TIME CHECK (0x800-0xB00/4s) >
2357 #
2358 CHECK MD2F.SUNPULSE_TIME -updatewait 8
2359 LET LocalA0 = {MD2F.SUNPULSE_TIME} & 0xFFFFFFFF
2360 CHECK MD2F.SUNPULSE_TIME -updatewait 8
2361 LET LocalA1 = {MD2F.SUNPULSE_TIME} & 0xFFFFFFFF
2362 LET LocalA1 = LocalA1 - LocalA0
2363 SYSTEM test {LocalA1} -ge 2048 -a {LocalA1} -le 2816
2364 #
2365 # < CHECK >
2366 CHECK MD1F.DPU_HW_STATE1 =DPU_RUN
2367 CHECK MD1F.DPU_HW_STATE2 =CPU_RUN
2368 CHECK MD1F.DMC_IF_RMAPERR =na
2369 CHECK MD1F.DMC_IF_2BITERR =na
2370 CHECK MD1F.DMC_IF_BTERR =na
2371 CHECK MD1F.BTO_ERR =na
2372 CHECK MD1F.WDT_ERR =na
2373 CHECK MD1F.DPU_SELECT =DPU1
2374 CHECK MD1F.DPU_SW_STATE 2
2375 CHECK MD1F.SW_CMD_CODE 0
2376 CHECK MD1F.SW_CMD_CNT 0
2377 CHECK MD1F.SW_CMD_REJ_CNT 0
2378 CHECK MD1F.SW_CMD_REJ_CODE 0
2379 CHECK MD1F.MS_CMD_PRIO =DMC
2380 CHECK MD1F.RDN_MODE =OFF
2381 CHECK MD1F.SUNPULSE_MODE =NML
2382 CHECK MD1F.LOAD_DUMP_DIS =DIS
2383 CHECK MD1F.MEMLOAD_BUSY =OFF
2384 CHECK MD1F.MEMLOAD_NODE 0
2385 CHECK MD1F.MEMDUMP_BUSY =OFF
2386 CHECK MD1F.MEMDUMP_NODE 0
2387 CHECK MD1F.TASK_STAT_AP01 =OFF
2388 CHECK MD1F.TASK_STAT_AP02 =OFF

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2389	CHECK MD1F. TASK_STAT_AP03	=OFF
2390	CHECK MD1F. TASK_STAT_AP04	=OFF
2391	CHECK MD1F. TASK_STAT_AP05	=OFF
2392	CHECK MD1F. TASK_STAT_AP06	=OFF
2393	CHECK MD1F. TASK_STAT_AP07	=OFF
2394	CHECK MD1F. TASK_STAT_AP08	=OFF
2395	CHECK MD1F. TASK_STAT_AP09	=OFF
2396	CHECK MD1F. TASK_STAT_AP10	=OFF
2397	CHECK MD1F. TASK_STAT_AP11	=OFF
2398	CHECK MD1F. TASK_STAT_AP12	=OFF
2399	CHECK MD1F. TASK_STAT_AP13	=OFF
2400	CHECK MD1F. TASK_STAT_AP14	=OFF
2401	CHECK MD1F. TASK_STAT_AP15	=OFF
2402	CHECK MD1F. TASK_STAT_AP16	=OFF
2403	CHECK MD1F. UNEXPECT_CNT	0
2404	CHECK MD1F. UNEXPECT_CODE	0
2405	CHECK MD1F. DMC_IF_ERR	=na
2406	CHECK MD1F. TIME_ERR	=na
2407	CHECK MD1F. INST_CACHE_ERR	=na
2408	CHECK MD1F. DATA_CACHE_ERR	=na
2409	CHECK MD1F. SRAM_1BERR	=na
2410	CHECK MD1F. CMD_BUFF_1BERR	=na
2411	CHECK MD1F. CMD_BUFF_2BERR	=na
2412	CHECK MD1F. TLM_BUFF_1BERR	=na
2413	CHECK MD1F. TLM_BUFF_2BERR	=na
2414	CHECK MD1F. COMP_1BERR	=na
2415	CHECK MD1F. DS_1BERR	=na
2416	CHECK MD1F. DS_2BERR	=na
2417	CHECK MD1F. RDN_LINK_STAT	=ON
2418	CHECK MD1F. RIF_LINK_ERR	=na
2419	CHECK MD1F. RIF_ERR	=na
2420	CHECK MD1F. MS0_LINK_STAT	=OFF
2421	CHECK MD1F. MS1_LINK_STAT	=OFF
2422	CHECK MD1F. MS2_LINK_STAT	=OFF
2423	CHECK MD1F. MS3_LINK_STAT	=OFF
2424	CHECK MD1F. MS4_LINK_STAT	=OFF
2425	CHECK MD1F. MS5_LINK_STAT	=OFF
2426	CHECK MD1F. MS6_LINK_STAT	=OFF
2427	CHECK MD1F. MS7_LINK_STAT	=OFF
2428	CHECK MD1F. MS0_DAT_CLCT	=OFF
2429	CHECK MD1F. MS1_DAT_CLCT	=OFF
2430	CHECK MD1F. MS2_DAT_CLCT	=OFF
2431	CHECK MD1F. MS3_DAT_CLCT	=OFF
2432	CHECK MD1F. MS4_DAT_CLCT	=OFF
2433	CHECK MD1F. MS5_DAT_CLCT	=OFF
2434	CHECK MD1F. MS6_DAT_CLCT	=OFF
2435	CHECK MD1F. MS7_DAT_CLCT	=OFF
2436	CHECK MD1F. MS0_BUFF_SEL	=L
2437	CHECK MD1F. MS1_BUFF_SEL	=L
2438	CHECK MD1F. MS2_BUFF_SEL	=L
2439	CHECK MD1F. MS3_BUFF_SEL	=L
2440	CHECK MD1F. MS4_BUFF_SEL	=L

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2441	CHECK MD1F. MS5_BUFF_SEL	=L
2442	CHECK MD1F. MS6_BUFF_SEL	=L
2443	CHECK MD1F. MS7_BUFF_SEL	=L
2444	CHECK MD1F. MS0_SGL_CMD_ERR	=na
2445	CHECK MD1F. MS1_SGL_CMD_ERR	=na
2446	CHECK MD1F. MS2_SGL_CMD_ERR	=na
2447	CHECK MD1F. MS3_SGL_CMD_ERR	=na
2448	CHECK MD1F. MS4_SGL_CMD_ERR	=na
2449	CHECK MD1F. MS5_SGL_CMD_ERR	=na
2450	CHECK MD1F. MS6_SGL_CMD_ERR	=na
2451	CHECK MD1F. MS7_SGL_CMD_ERR	=na
2452	CHECK MD1F. MS0_MTI_CMD_ERR	=na
2453	CHECK MD1F. MS1_MTI_CMD_ERR	=na
2454	CHECK MD1F. MS2_MTI_CMD_ERR	=na
2455	CHECK MD1F. MS3_MTI_CMD_ERR	=na
2456	CHECK MD1F. MS4_MTI_CMD_ERR	=na
2457	CHECK MD1F. MS5_MTI_CMD_ERR	=na
2458	CHECK MD1F. MS6_MTI_CMD_ERR	=na
2459	CHECK MD1F. MS7_MTI_CMD_ERR	=na
2460	CHECK MD2F. DPU_HW_STATE1	=DPU_RUN
2461	CHECK MD2F. DPU_HW_STATE2	=CPU_RUN
2462	CHECK MD2F. DMC_IF_RMAPERR	=na
2463	CHECK MD2F. DMC_IF_2BITERR	=na
2464	CHECK MD2F. DMC_IF_BTOERR	=na
2465	CHECK MD2F. BTO_ERR	=na
2466	CHECK MD2F. WDT_ERR	=na
2467	CHECK MD2F. DPU_SELECT	=DPU2
2468	CHECK MD2F. DPU_SW_STATE	2
2469	CHECK MD2F. SW_CMD_CODE	0
2470	CHECK MD2F. SW_CMD_CNT	0
2471	CHECK MD2F. SW_CMD_REJ_CNT	0
2472	CHECK MD2F. SW_CMD_REJ_CODE	0
2473	CHECK MD2F. MS_CMD_PRIO	=DMC
2474	CHECK MD2F. RDN_MODE	=OFF
2475	CHECK MD2F. SUNPULSE_MODE	=NML
2476	CHECK MD2F. LOAD_DUMP_DIS	=DIS
2477	CHECK MD2F. MEMLOAD_BUSY	=OFF
2478	CHECK MD2F. MEMLOAD_NODE	0
2479	CHECK MD2F. MEMDUMP_BUSY	=OFF
2480	CHECK MD2F. MEMDUMP_NODE	0
2481	CHECK MD2F. TASK_STAT_AP01	=OFF
2482	CHECK MD2F. TASK_STAT_AP02	=OFF
2483	CHECK MD2F. TASK_STAT_AP03	=OFF
2484	CHECK MD2F. TASK_STAT_AP04	=OFF
2485	CHECK MD2F. TASK_STAT_AP05	=OFF
2486	CHECK MD2F. TASK_STAT_AP06	=OFF
2487	CHECK MD2F. TASK_STAT_AP07	=OFF
2488	CHECK MD2F. TASK_STAT_AP08	=OFF
2489	CHECK MD2F. TASK_STAT_AP09	=OFF
2490	CHECK MD2F. TASK_STAT_AP10	=OFF
2491	CHECK MD2F. TASK_STAT_AP11	=OFF
2492	CHECK MD2F. TASK_STAT_AP12	=OFF

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2493	CHECK MD2F. TASK_STAT_AP13	=OFF
2494	CHECK MD2F. TASK_STAT_AP14	=OFF
2495	CHECK MD2F. TASK_STAT_AP15	=OFF
2496	CHECK MD2F. TASK_STAT_AP16	=OFF
2497	CHECK MD2F. UNEXPECT_CNT	0
2498	CHECK MD2F. UNEXPECT_CODE	0
2499	CHECK MD2F. DMC_IF_ERR	=na
2500	CHECK MD2F. TIME_ERR	=na
2501	CHECK MD2F. INST_CACHE_ERR	=na
2502	CHECK MD2F. DATA_CACHE_ERR	=na
2503	CHECK MD2F. SRAM_1BERR	=na
2504	CHECK MD2F. CMD_BUFF_1BERR	=na
2505	CHECK MD2F. CMD_BUFF_2BERR	=na
2506	CHECK MD2F. TLM_BUFF_1BERR	=na
2507	CHECK MD2F. TLM_BUFF_2BERR	=na
2508	CHECK MD2F. COMP_1BERR	=na
2509	CHECK MD2F. DS_1BERR	=na
2510	CHECK MD2F. DS_2BERR	=na
2511	CHECK MD2F. RDN_LINK_STAT	=ON
2512	CHECK MD2F. RIF_LINK_ERR	=na
2513	CHECK MD2F. RIF_ERR	=na
2514	CHECK MD2F. MS0_LINK_STAT	=OFF
2515	CHECK MD2F. MS1_LINK_STAT	=OFF
2516	CHECK MD2F. MS2_LINK_STAT	=OFF
2517	CHECK MD2F. MS3_LINK_STAT	=OFF
2518	CHECK MD2F. MS4_LINK_STAT	=OFF
2519	CHECK MD2F. MS5_LINK_STAT	=OFF
2520	CHECK MD2F. MS6_LINK_STAT	=OFF
2521	CHECK MD2F. MS7_LINK_STAT	=OFF
2522	CHECK MD2F. MS0_DAT_CLCT	=OFF
2523	CHECK MD2F. MS1_DAT_CLCT	=OFF
2524	CHECK MD2F. MS2_DAT_CLCT	=OFF
2525	CHECK MD2F. MS3_DAT_CLCT	=OFF
2526	CHECK MD2F. MS4_DAT_CLCT	=OFF
2527	CHECK MD2F. MS5_DAT_CLCT	=OFF
2528	CHECK MD2F. MS6_DAT_CLCT	=OFF
2529	CHECK MD2F. MS7_DAT_CLCT	=OFF
2530	CHECK MD2F. MS0_BUFF_SEL	=L
2531	CHECK MD2F. MS1_BUFF_SEL	=L
2532	CHECK MD2F. MS2_BUFF_SEL	=L
2533	CHECK MD2F. MS3_BUFF_SEL	=L
2534	CHECK MD2F. MS4_BUFF_SEL	=L
2535	CHECK MD2F. MS5_BUFF_SEL	=L
2536	CHECK MD2F. MS6_BUFF_SEL	=L
2537	CHECK MD2F. MS7_BUFF_SEL	=L
2538	CHECK MD2F. MS0_SGL_CMD_ERR	=na
2539	CHECK MD2F. MS1_SGL_CMD_ERR	=na
2540	CHECK MD2F. MS2_SGL_CMD_ERR	=na
2541	CHECK MD2F. MS3_SGL_CMD_ERR	=na
2542	CHECK MD2F. MS4_SGL_CMD_ERR	=na
2543	CHECK MD2F. MS5_SGL_CMD_ERR	=na
2544	CHECK MD2F. MS6_SGL_CMD_ERR	=na

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2545 CHECK MD2F. MS7_SGL_CMD_ERR =na
2546 CHECK MD2F. MS0_MTI_CMD_ERR =na
2547 CHECK MD2F. MS1_MTI_CMD_ERR =na
2548 CHECK MD2F. MS2_MTI_CMD_ERR =na
2549 CHECK MD2F. MS3_MTI_CMD_ERR =na
2550 CHECK MD2F. MS4_MTI_CMD_ERR =na
2551 CHECK MD2F. MS5_MTI_CMD_ERR =na
2552 CHECK MD2F. MS6_MTI_CMD_ERR =na
2553 CHECK MD2F. MS7_MTI_CMD_ERR =na
2554 . #
2555 . MD1F. DPU_SW_ERR_CLR
2556 . #
2557 . # < CHECK >
2558 CHECK MD1F. SLOT_ERR =na
2559 . #
2560 . #
2561 . MD2F. DPU_SW_ERR_CLR
2562 . #
2563 . # < CHECK >
2564 CHECK MD2F. SLOT_ERR =na
2565 . #
2566 . # Telemetry Operation Mode 設定
2567 . # (Mission User HK Packet をリアルタイムに出力するように設定。
2568 . # リアルタイムは System HK 8 秒周期, DH, DR, Mission User HK 出力,
2569 . # 32kbps)
2570 . # DHFS
2571 . DH. OPE_MODE_32KBPS_CAT0x26 0x03 0x000000000000000000000000
2572 . #
2573 . # < CHECK >
2574 CHECK DH. CATEGORY_TBL_NO 0x26
2575 CHECK DH. DWNLNK_RATE =BPS32768
2576 WAIT_SEC 2
2577 LET MD1_SW_CNT = ( {MD1F. SW_CMD_CNT} + 5 ) & 0xFF
2578 LET MD2_SW_CNT = ( {MD2F. SW_CMD_CNT} + 5 ) & 0xFF
2579 LET MD1_REJ_CNT = ( {MD1F. SW_CMD_REJ_CNT} & 0xFF)
2580 LET MD2_REJ_CNT = ( {MD2F. SW_CMD_REJ_CNT} & 0xFF)
2581 . #
2582 . DH. MDP_DPU12_APL_START # DH. MC_EACH_EXE 0x09B
2583 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
2584 #: 0x00 DH. ATMC_EACH_ENA 0x32
2585 #: 0x02 MD1F. APP01_START
2586 #: 0x03 MD1F. APP02_START
2587 #: 0x04 MD1F. APP03_START
2588 #: 0x05 MD1F. APP04_START
2589 #: 0x06 MD1F. APP05_START
2590 #: 0x07 DH. ATMC_EACH_ENA 0x33
2591 #: 0x08 MD2F. APP01_START
2592 #: 0x09 MD2F. APP02_START
2593 #: 0x0A MD2F. APP13_START
2594 #: 0x0B MD2F. APP14_START
2595 #: 0x0C MD2F. APP05_START
2596 . #

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2597 . # < CHECK >
2598 CHECK DH. ATMC_ENA_DIS32 =ENA
2599 CHECK DH. ATMC_ENA_DIS33 =ENA
2600 #
2601 # < CHECK >
2602 SYSTEM test {MD1F. SW_CMD_CNT} = {MD1_SW_CNT}
2603 SYSTEM test {MD2F. SW_CMD_CNT} = {MD2_SW_CNT}
2604 SYSTEM test {MD1F. SW_CMD_REJ_CNT} = {MD1_REJ_CNT}
2605 SYSTEM test {MD2F. SW_CMD_REJ_CNT} = {MD2_REJ_CNT}
2606 #
2607 # < CHECK >
2608 CHECK MD1F. TASK_STAT_AP01 =ON
2609 CHECK MD1F. TASK_STAT_AP02 =ON
2610 CHECK MD1F. TASK_STAT_AP03 =ON
2611 CHECK MD1F. TASK_STAT_AP04 =ON
2612 CHECK MD1F. TASK_STAT_AP05 =ON
2613 CHECK MD1F. TASK_STAT_AP06 =OFF
2614 CHECK MD2F. TASK_STAT_AP01 =ON
2615 CHECK MD2F. TASK_STAT_AP02 =ON
2616 CHECK MD2F. TASK_STAT_AP13 =ON
2617 CHECK MD2F. TASK_STAT_AP14 =ON
2618 CHECK MD2F. TASK_STAT_AP05 =ON
2619 CHECK MD2F. TASK_STAT_AP06 =OFF
2620 #
2621 . # < MDP USER-TASK: Initial Sequence - MDP-DPU-INITSET>
2622 #
2623 . # < CHECK >
2624 LET MD1_GET = ({MD1U. U_CmdDmc_GetCNT} + 5 ) & 0x1F
2625 LET MD2_GET = ({MD2U. U_CmdDmc_GetCNT} + 5 ) & 0x1F
2626 LET MD1_EXE = ({MD1U. U_CmdDmc_ExecCNT} + 5 ) & 0x1F
2627 LET MD2_EXE = ({MD2U. U_CmdDmc_ExecCNT} + 5 ) & 0x1F
2628 . #
2629 . DH. MDP_U_DPU12_INIT_SET # DH. MC_EACH_EXE 0x080
2630 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
2631 #: 0x00 MD1U. HK_SET 0xFF 0x08
2632 #: 0x01 MD1U. HK_MODE_CHK
2633 #: 0x02 MD1U. TLM_CMP 0xFF 0x00 0x11
2634 #: 0x03 MD2U. HK_SET 0xFF 0x08
2635 #: 0x04 MD2U. HK_MODE_CHK
2636 #: 0x05 MD2U. TLM_CMP 0xFF 0x00 0x11
2637 #: 0x0E MD1U. WDT_ENA
2638 #: 0x0F MD2U. WDT_ENA
2639 #
2640 WAIT_SEC 5
2641 MD1U. HK_SET 0xFF 0x03
2642 MD2U. HK_SET 0xFF 0x03
2643 #
2644 . # < CHECK >
2645 SYSTEM test {MD1U. U_CmdDmc_GetCNT} = {MD1_GET}
2646 SYSTEM test {MD2U. U_CmdDmc_GetCNT} = {MD2_GET}
2647 SYSTEM test {MD1U. U_CmdDmc_ExecCNT} = {MD1_EXE}
2648 SYSTEM test {MD2U. U_CmdDmc_ExecCNT} = {MD2_EXE}

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2649	CHECK MD1U. U_WdtEna	=ENA	
2650	CHECK MD1U. U_HkMode	=CHK	
2651	CHECK MD1U. U_Hk_RepInt_DPU1	3	
2652	CHECK MD1U. U_Hk_RepInt_ENA	3	
2653	CHECK MD1U. U_Hk_RepInt_HEPE	3	
2654	CHECK MD1U. U_Hk_RepInt_HEPI	3	
2655	CHECK MD1U. U_Hk_RepInt_MEA1	3	
2656	CHECK MD1U. U_Hk_RepInt_MEA2	3	
2657	CHECK MD1U. U_Hk_RepInt_MGFO	3	
2658	CHECK MD1U. U_Hk_RepInt_MIA	3	
2659	CHECK MD1U. U_Hk_RepInt_MSA	3	
2660	CHECK MD1U. U_TImML_CMP_DPU1	=CMP	
2661	CHECK MD1U. U_TImML_CMP_ENA	=USR	
2662	CHECK MD1U. U_TImML_CMP_HEPE	=ARO	
2663	CHECK MD1U. U_TImML_CMP_HEPI	=ARO	
2664	CHECK MD1U. U_TImML_CMP_MEA1	=USR	
2665	CHECK MD1U. U_TImML_CMP_MEA2	=USR	
2666	CHECK MD1U. U_TImML_CMP_MGFO	=ARO	
2667	CHECK MD1U. U_TImML_CMP_MIA	=USR	
2668	CHECK MD1U. U_TImML_CMP_MSA	=USR	
2669	CHECK MD1U. U_TImH_CMP_DPU1	=CMP	
2670	CHECK MD1U. U_TImH_CMP_HEPE	=ARO	
2671	CHECK MD1U. U_TImH_CMP_HEPI	=ARO	
2672	CHECK MD1U. U_TImH_CMP_MEA1	=USR	
2673	CHECK MD1U. U_TImH_CMP_MEA2	=USR	
2674	CHECK MD1U. U_TImH_CMP_MGFO	=ARO	
2675	CHECK MD1U. U_TImH_CMP_MIA	=USR	
2676	CHECK MD1U. U_TImH_CMP_MSA	=USR	
2677	CHECK MD2U. U_WdtEna	=ENA	
2678	CHECK MD2U. U_HkMode	=CHK	
2679	CHECK MD2U. U_Hk_RepInt_DPU2	3	
2680	CHECK MD2U. U_Hk_RepInt_EWO	3	
2681	CHECK MD2U. U_Hk_RepInt_MEF	3	
2682	CHECK MD2U. U_Hk_RepInt_MGFI	3	
2683	CHECK MD2U. U_Hk_RepInt_MSAS	3	
2684	CHECK MD2U. U_Hk_RepInt_MWE	3	
2685	CHECK MD2U. U_TImML_CMP_DPU2	=CMP	
2686	CHECK MD2U. U_TImML_CMP_AM2P	=ARO	
2687	CHECK MD2U. U_TImML_CMP_EFD	=ARO	
2688	CHECK MD2U. U_TImML_CMP_EWOB	=ARO	
2689	CHECK MD2U. U_TImML_CMP_EWOE	=ARO	
2690	CHECK MD2U. U_TImML_CMP_MDM	=ARO	
2691	CHECK MD2U. U_TImML_CMP_MGFI	=ARO	
2692	CHECK MD2U. U_TImML_CMP_MSAS	=ARO	
2693	CHECK MD2U. U_TImML_CMP_SOR	=ARO	
2694	CHECK MD2U. U_TImH_CMP_DPU2	=CMP	
2695	CHECK MD2U. U_TImH_CMP_EFD	=ARO	
2696	CHECK MD2U. U_TImH_CMP_EWOB	=USR	
2697	CHECK MD2U. U_TImH_CMP_EWOE	=USR	
2698	CHECK MD2U. U_TImH_CMP_MGFI	=ARO	
2699	#		
2700	# QL	Check-Item	Spec
			Check

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2701  # -----+-----+-----+-----
2702  # CMD 出力装置      Message          STATUS CHECK 結果 OK      G  N
2703  #
2704  # =====
2705  # 6.2. SI 機器立ち上げ
2706  # =====
2707  # -----
2708  # 6.2.1. PME 立ち上げ／初期化
2709  # -----
2710  #
2711  # PCD.ON_OFF_ENA
2712  #
2713  # < CHECK >
2714  # CHECK PCD.ON_OFF_ENA_DIS          =ENA
2715  #
2716  # PCD.PSU_PME_ON
2717  #
2718  # < CHECK >
2719  # CHECK PCD.PSU_PME_ON_OFF          =ON
2720  #
2721  # QL          Check-Item          Spec          Check
2722  # -----+-----+-----+-----
2723  # CMD 出力装置      Message          STATUS CHECK 結果 OK      G  N
2724  #
2725  # -----
2726  # 6.2.2. MGF 立ち上げ／初期化
2727  # -----
2728  #
2729  # DH.MGF_ON          # DH.MC_EACH_EXE 0x095
2730  #:                  << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
2731  #:                  0x00 MD1F.PME_MGFO_ON
2732  #:                  0x01 PCD.ON_OFF_ENA
2733  #:                  0x02 PCD.PSU_MGF_I_ON
2734  #:                  0x03 PCD.ON_OFF_DIS
2735  #:                  0x0D MD1F.DPU_SW_ERR_CLR
2736  #:                  0x0E MD2F.DPU_SW_ERR_CLR
2737  #:                  0x0F MD2U.MGFI_HW_CMD 0xF0E8EE70
2738  #
2739  # < CHECK >
2740  # CHECK PCD.PSU_MGF_I_ON_OFF          =ON
2741  # CHECK MGFI.CRC_ERR_CNTR              0
2742  # CHECK MGFI.RESET_CNTR                0
2743  # CHECK MGFI.PHASE_VAL_X_A             -28.44 -28.35
2744  # CHECK MGFI.PHASE_VAL_Y_A             -43.64 -43.55
2745  # CHECK MGFI.PHASE_VAL_Z_A             -32.24 -32.15
2746  # CHECK MGFI.SYNC_DLY_VAL_A            976.55 976.64
2747  # CHECK MGFI.TLM_OUT_STAT_L            =OUTPUT
2748  # CHECK MGFI.TLM_OUT_RATE_L            =SPIN
2749  # CHECK MGFI.TLM_OUT_STAT_M            =OUTPUT
2750  # CHECK MGFI.TLM_OUT_RATE_M            =F8HZ
2751  # CHECK MGFI.TLM_OUT_STAT_H            =STOP
2752  # CHECK MGFI.TLM_OUT_RATE_H            =CHECKOUT

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2753	CHECK MD1F. PME_MGFO_STS	=ON	
2754	CHECK MGFO. PRAM_STAT	0	
2755	CHECK MGFO. MDATRAM_STAT	0	
2756	CHECK MGFO. EXTSYNC_OFF	0	
2757	CHECK MGFO. RELAY_ON_OFF	1	
2758	CHECK MGFO. OPEN_LOOP	0	
2759	CHECK MGFO. EXCIT_ON	1	
2760	CHECK MGFO. EEPROM_PAGE	0	
2761	CHECK MGFO. BOOT_OP	0	
2762	CHECK MGFO. BOOT_MAIN	1	
2763	CHECK MGFO. CS_OK	0	
2764	CHECK MGFO. RELAY1_STAT	1	
2765	CHECK MGFO. SYNC_PRESENT	1	
2766	CHECK MGFO. LINK_DISCONNECT	0	
2767	CHECK MGFO. LINK_EERR	0	
2768	CHECK MGFO. LINK_CERR	0	
2769	CHECK MGFO. LINK_PERR	0	
2770	CHECK MGFO. RMAP_ERR_FLAGS	0	
2771	CHECK MGFO. VALID_WCMDS	0	
2772	CHECK MGFO. INVALID_CMDS	0	
2773	CHECK MGFO. SYNC_OFFSET	0xa1fb 0xa1fb	
2774	CHECK MGFO. SOFTWARE_VER	0x0007	
2775	CHECK MGFO. EEPROM_CHECKSUM	0	
2776	CHECK MGFO. CALC_CHECKSUM	0	
2777	CHECK MGFO. ELECTR_TEMP	-30 70	
2778	CHECK MGFO. OS_TEMP	-60 180	
2779	CHECK MGFO. IS_TEMP	-60 180	
2780	CHECK MGFO. CURRENT_M12V	-40 -29	
2781	CHECK MGFO. CURRENT_3V3	32 39	
2782	CHECK MGFO. CURRENT_1V5	55 77	
2783	CHECK MGFO. VOLTAGE_PANA	7.3 7.9	
2784	CHECK MGFO. VOLTAGE_MANA	-7.9 -7.5	
2785	CHECK MGFO. VOLTAGE_P12V	11.3 12.5	
2786	CHECK MGFO. VOLTAGE_M12V	-12.5 -11.3	
2787	CHECK MGFO. VOLTAGE_3V3	3.1 3.5	
2788	CHECK MGFO. VOLTAGE_P5V	4.7 5.3	
2789	CHECK MGFO. VOLTAGE_M5V	-5.3 -4.7	
2790	CHECK MGFO. VOLTAGE_1V5	1.37 1.63	
2791	CHECK MGFO. TLM_OUT_STAT_L	=OUTPUT	
2792	CHECK MGFO. TLM_OUT_RATE_L	=SPIN	
2793	CHECK MGFO. TLM_OUT_STAT_M	=OUTPUT	
2794	CHECK MGFO. TLM_OUT_RATE_M	=F8HZ	
2795	CHECK MGFO. TLM_OUT_STAT_H	=STOP	
2796	CHECK MGFO. TLM_OUT_RATE_H	=CHECKOUT	
2797	CHECK MD1F. MS0_LINK_STAT	=ON	
2798	CHECK MD2F. MS5_LINK_STAT	=ON	
2799	CHECK MD1F. MS0_LINK_ERR	=na	
2800	CHECK MD1F. SLOT_ERR	=na	
2801	CHECK MD2F. MS5_LINK_ERR	=na	
2802	CHECK MD2F. SLOT_ERR	=na	
2803	#		
2804	# QL	Check-Item	Spec Check

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2805 # -----+-----+-----+-----
2806 # CMD 出力装置   Message           STATUS CHECK 結果 OK       G  N
2807 #
2808 # -----
2809 # 6.2.3. ENA 立ち上げ／初期化
2810 # -----
2811 #
2812 # PCD.ON_OFF_ENA
2813 #
2814 # < CHECK >
2815 CHECK PCD.ON_OFF_ENA_DIS           =ENA
2816 #
2817 # PCD.PSU_ENA_ON
2818 #
2819 # < CHECK >
2820 CHECK PCD.PSU_ENA_ON_OFF           =ON
2821 CHECK MD1F.MS3_LINK_STAT           =ON
2822 CHECK MD1F.MS3_DAT_CLCT            =ON
2823 #
2824 # QL          Check-Item           Spec           Check
2825 # -----+-----+-----+-----
2826 # CMD 出力装置   Message           STATUS CHECK 結果 OK       G  N
2827 #
2828 # -----
2829 # 6.2.4. MEA1 立ち上げ／初期化
2830 # -----
2831 #
2832 # N/A(確認手順で別途実施)
2833 #
2834 # -----
2835 # 6.2.5. MEA2 立ち上げ／初期化
2836 # -----
2837 #
2838 # N/A(確認手順で別途実施)
2839 #
2840 # -----
2841 # 6.2.6. MSA 立ち上げ／初期化
2842 # -----
2843 #
2844 # MD1U.MSA_DATA_MODE 0x00 0x01
2845 # PCD.PSU_MSA_ON
2846 #
2847 # < CHECK >
2848 CHECK PCD.PSU_MSA_ON_OFF           =ON
2849 CHECK MD1F.MS5_LINK_STAT           =ON
2850 CHECK MD1F.MS5_DAT_CLCT            =ON
2851 CHECK MSA.S_DATA_MODE_SE           =SCI
2852 CHECK MSA.S_DATA_MODE              01
2853 #
2854 # QL          Check-Item           Spec           Check
2855 # -----+-----+-----+-----
2856 # CMD 出力装置   Message           STATUS CHECK 結果 OK       G  N

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2857 #
2858 # -----
2859 . # 6.2.7. MIA 立ち上げ／初期化
2860 # -----
2861 . #
2862 . PCD.PSU_MIA_ON
2863 #
2864 . # < CHECK >
2865 CHECK PCD.PSU_MIA_ON_OFF =ON
2866 CHECK MD1F.MS4_LINK_STAT =ON
2867 CHECK MD1F.MS4_DAT_CLCT =ON
2868 #
2869 . # QL          Check-Item          Spec          Check
2870 # -----+-----+-----+-----+
2871 # CMD 出力装置  Message          STATUS CHECK 結果 OK      G  N
2872 #
2873 # -----
2874 . # 6.2.8. PWI 立ち上げ／初期化 [EWO, SORBET, AM2P, MEFISTO]
2875 # -----
2876 #
2877 . # <<<< Power on PWI >>>>
2878 #
2879 . DH.PWI_ON1_EWO_SOR_AM2P      # DH.MC_EACH_EXE 0x0096
2880 #:          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
2881 #:          0x00 MD2F.PME_EWO_ON
2882 #:          0x09 MD2F.PME_SORBET_ON
2883 #:          0x0A MD2F.PME_AM2P_ON
2884 #:          0x0B EWO.WPTP_ON
2885 #:          0x0C EWO.SCP_ON
2886 #:          0x0D MD2F.DPU_SW_ERR_CLR
2887 #:          0x0E MD2U.BUF_CLCT 0x10 0x00
2888 #:          0x0F MD2U.EWO_HW_INIT
2889 #
2890 . # < CHECK >
2891 CHECK MD2F.PME_EWO_STS      =ON
2892 CHECK MD2F.PME_SORBET_STS   =ON
2893 CHECK MD2F.PME_AM2P_STS     =ON
2894 CHECK MD2F.MS1_LINK_STAT    =ON
2895 CHECK MD2F.MS1_LINK_ERR     =na
2896 CHECK MD2F.MS1_DAT_CLCT     =ON
2897 CHECK MD2F.MS3_LINK_STAT    =ON
2898 CHECK MD2F.MS3_LINK_ERR     =na
2899 CHECK EWO.WPTP_ONOF         =ON
2900 CHECK EWO.WPTP_GAIN         =LOW
2901 CHECK EWO.WPTP_BIAS_ONOF    =ON
2902 CHECK EWO.WPTP_BIAS_CAL     =BIAS
2903 CHECK EWO.WPTP_CAL_ONOF     =OFF
2904 CHECK EWO.WPTP_CAL_IMP      =R10M
2905 CHECK EWO.SCP_ONOF          =ON
2906 CHECK EWO.SCP_CAL_STS       =OFF
2907 CHECK EWO.EFD_SYNC_STS      =EWO
2908 CHECK EWO.EFD_OBS_MODE      =STBY

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2909	CHECK EWO. EFD_FB_ONOF	=OFF		
2910	CHECK EWO. WPT1_BIAS	128		
2911	CHECK EWO. WPT2_BIAS	128		
2912	CHECK EWO. WPT1_LOOP_GAIN	0		
2913	CHECK EWO. WPT2_LOOP_GAIN	0		
2914	CHECK EWO. EFD_BIAS_SWP_1	0		
2915	CHECK EWO. EFD_BIAS_X1_CNT	1		
2916	CHECK EWO. WFC_E_AM2P_SEL	=STBY		
2917	CHECK EWO. WFC_E_INP_SEL	=DIFF		
2918	CHECK EWO. WFC_E_LPF_STS	=FC_20KHZ		
2919	CHECK EWO. WFC_E_PGA_STS	=LOW		
2920	CHECK EWO. WFC_B_OBS_STS	=STBY		
2921	CHECK EWO. WFC_B_LPF_STS	=FC_20KHZ		
2922	CHECK EWO. WFC_B_PGA_STS	=LOW		
2923	CHECK EWO. WFC_E_CAL_ATT	=OFF		
2924	CHECK EWO. WFC_E_EFD_ICAL	=OFF		
2925	CHECK EWO. WFC_B_ICAL	=OFF		
2926	CHECK EWO. TEMP_MON_ONOF	=ON		
2927	CHECK EWO. AM2P_TC_CNT	0		
2928	CHECK EWO. AM2P_TMTRG_CNT	0		
2929	CHECK EWO. AM2P_TC_ACK_CNT	0x01		
2930	CHECK EWO. AM2P_ACQ_CNT	0x01		
2931	CHECK EWO. OTR_WFCE	=NONE		
2932	CHECK EWO. OTR_WFCB	=NONE		
2933	CHECK EWO. MDP_PAC_EN_DIS	=DIS		
2934	CHECK EWO. EFD_SRAM_FL	=NOT_FULL		
2935	CHECK EWO. WFC_E_SRAM_FL	=NOT_FULL		
2936	CHECK EWO. WFC_B_SRAM_FL	=NOT_FULL		
2937	CHECK EWO. EFD_LU_STS	=NONE		
2938	CHECK EWO. WFC_E_LU_STS	=NONE		
2939	CHECK EWO. WFC_B_LU_STS	=NONE		
2940	CHECK EWO. EWO_CMD_ERR_CNT	0x00		
2941	CHECK EWO. RMAP_HDR_ERR	0x00		
2942	CHECK EWO. WFC_E_SRAM_ERR1	0x00		
2943	CHECK EWO. WFC_E_SRAM_ERR2	0x00		
2944	CHECK EWO. WFC_B_SRAM_ERR1	0x00		
2945	CHECK EWO. WFC_B_SRAM_ERR2	0x00		
2946	CHECK EWO. EFD_FIR_ERR	=NONE		
2947	CHECK EWO. WFC_B_FIR_ERR	=NONE		
2948	#			
2949	#	===== Record of the initial values =====		
2950	# QL	Check-Item	Spec	Data
2951	#			Check
2952	# EWO	TI	記録のみ	N/A
2953	#			
2954	# < CHECK >			
2955	CHECK SORB. CMD_CODE	0		
2956	CHECK SORB. WPT_GAIN	=LOW		
2957	CHECK SORB. WPT_BIAS_ON_OFF	=OFF		
2958	CHECK SORB. WPT_CAL_ON_OFF	=OFF		
2959	CHECK SORB. TNR1_ON_OFF	=OFF		
2960	CHECK SORB. TNR1_INP_SETUP	0		

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2961 CHECK SORB.TNR2_ON_OFF          =OFF
2962 CHECK SORB.TNR2_INP_SETUP        0
2963 CHECK SORB.HFR_ON_OFF            =OFF
2964 CHECK SORB.HFR_INP_SETUP          0
2965 CHECK SORB.VIA_TNR1               0
2966 CHECK SORB.VIA_TNR2               0
2967 CHECK SORB.HF1_RANGE               0
2968 CHECK SORB.HF2_RANGE               0
2969 CHECK SORB.HFR_FREQ_STEP           0
2970 CHECK SORB.SPW_LINK                1
2971 CHECK SORB.SPW_ERROR               =NORMAL
2972 CHECK SORB.CAL_SRC_SETUP           0
2973 CHECK SORB.CAL_BAND_A              0
2974 CHECK SORB.CAL_BAND_B              0
2975 CHECK SORB.CAL_BAND_C              0
2976 CHECK SORB.CAL_BAND_D              0
2977 CHECK SORB.CAL_BAND_HF1            0
2978 CHECK SORB.CAL_BAND_HF2            0
2979 #
2980 . # << PWI Observation Start 0x00D3 >>
2981 . DH.PWI_OBS_START                 # DH.MC_EACH_EXE 0x00D3
2982 #:                                << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
2983 #:                                0x00 EWO.WFCE_OBS_START
2984 #:                                0x01 EWO.WFCB_OBS_START
2985 #:                                0x02 EWO.EFD_OBS_START
2986 #:                                0x03 SORB.OBS_SEQ
2987 #:                                0x08 MD2U.BUF_CLCT 0x10 0x01
2988 #:                                0x0E EWO.RAM_FUL_CLR
2989 #
2990 . # < CHECK >
2991 CHECK EWO.EFD_OBS_MODE               =OBS
2992 CHECK EWO.WFC_E_AM2P_SEL             =WFC_OBS
2993 CHECK EWO.WFC_B_OBS_STS              =OBS
2994 CHECK EWO.MDP_PAC_EN_DIS             =ENA
2995 CHECK MD2F.MS3_DAT_CLCT              =ON
2996 CHECK SORB.TNR1_ON_OFF               =ON
2997 CHECK SORB.TNR1_INP_SETUP            0x1
2998 CHECK SORB.TNR2_ON_OFF               =ON
2999 CHECK SORB.VIA_TNR1                  0x0
3000 #
3001 . # QL          Check-Item          Spec          Check
3002 . # -----+-----+-----+-----+
3003 . # SORBET      COMMAND COUNTER      Increment      G  N
3004 . #             HFR FREQ. STEP        Increment      G  N
3005 . #
3006 . #             COMMAND CODE          0x518F または 0x53BF      G  N
3007 . #
3008 . MD2U.EWO_WFCE_FREQ_SEL 0x01
3009 . #
3010 . # # PWI 側で確認
3011 . # QL          Check-Item          Spec
3012 . # -----+-----+-----+

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3013 . # EWO          WFC(E) LPF FREQ          FC_120KHZ
3014 . #
3015 . MD2U.EWO_EFD_MODE 0xFF 0xFF
3016 . #
3017 . DH.PWI_ON2_MEF          # DH.MC_EACH_EXE 0x008A
3018 . #:                << MACRO >> 内容について以下に示す(DH.DUMMY は省略)
3019 . #:                0x00 MD2F.PME_MEFISTO_ON
3020 . #:                0x03 MD2F.DPU_SW_ERR_CLR
3021 . #
3022 . # < CHECK >
3023 . CHECK MD2F.PME_MEFISTO_STS          =ON
3024 . CHECK MD2F.MS4_LINK_STAT          =ON
3025 . CHECK MD2F.MS4_LINK_ERR          =na
3026 . #
3027 . # << PWI-MEFISTO Initialize 0x00D2 >>
3028 . #DH.PWI_MEF_INIT          # DH.MC_EACH_EXE 0x00D2
3029 . #:                << MACRO >> 内容について以下に示す(DH.DUMMY は省略)
3030 . #:                0x00 MEFI.INIT_A
3031 . #:                0x02 MEFI.INIT_A
3032 . #:                0x04 MEFI.INIT_B
3033 . #:                0x06 MEFI.INIT_C
3034 . #
3035 . # < CHECK >
3036 . #CHECK MEFI.BDAC1          0x8000
3037 . #CHECK MEFI.BDAC2          0x8000
3038 . #CHECK MEFI.SDAC1          0x8000
3039 . #CHECK MEFI.SDAC2          0x8000
3040 . #CHECK MEFI.ADC_HV_NEG          -145 145
3041 . #CHECK MEFI.ADC_HV_POS          -145 145
3042 . #
3043 . # QL          Check-Item          Spec          Check
3044 . # -----+-----+-----+-----
3045 . # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
3046 . #
3047 . # -----
3048 . # 6.2.9. MAST 立ち上げ／初期化
3049 . # -----
3050 . #
3051 . MD2F.PME_MAST_ON
3052 . #
3053 . # < CHECK >
3054 . CHECK MD2F.PME_MAST_STS          =ON
3055 . CHECK MD2F.MS0_LINK_STAT          =ON
3056 . #
3057 . # <初期チェック>
3058 . #
3059 . # < CHECK >
3060 . CHECK MWE.STAT_STBY          =OFF
3061 . CHECK MWE.STAT_MST_PSU          =MAIN
3062 . CHECK MWE.STAT_WPT_PSU          =MAIN
3063 . CHECK MWE.STAT_SAFE_MODE          =OFF
3064 . CHECK MWE.STAT_OVLD_CONT          =DIS

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3065 CHECK MWE. STAT_TEMP_CONT =DIS
3066 CHECK MWE. STAT_UNBL_CONT =DIS
3067 CHECK MWE. MST_MS_CONT =ENA
3068 CHECK MWE. MST_LEN_CONT =DIS
3069 CHECK MWE. WPT_MS_CONT =ENA
3070 CHECK MWE. WPT_LEN_CONT =DIS
3071 CHECK MWE. STAT_WPT_RATE =RST
3072 CHECK MWE. STAT_MST_STBY =OFF
3073 CHECK MWE. MST_SC_RET =OFF
3074 CHECK MWE. MST_SC_EXT =OFF
3075 CHECK MWE. MST_SC_RLSE =OFF
3076 CHECK MWE. MST_SC_DRV =OFF
3077 CHECK MWE. CONT_MTR2_OVHT =NON
3078 CHECK MWE. CONT_MTR2_OVLD =NON
3079 CHECK MWE. MST_SC_MS_RET =FULL
3080 CHECK MWE. CONT_LEN_DET_SC =NO
3081 CHECK MWE. CONT_MS_RL_SC =OFF
3082 CHECK MWE. MST_MGF_RET =OFF
3083 CHECK MWE. MST_MGF_EXT =OFF
3084 CHECK MWE. MST_MGF_RLSE =OFF
3085 CHECK MWE. MST_MGF_DRV =OFF
3086 CHECK MWE. CONT_MTR1_OVHT =NON
3087 CHECK MWE. CONT_MTR1_OVLD =NON
3088 CHECK MWE. MST_MGF_MS_RET =FULL
3089 CHECK MWE. CONT_LEN_DET_MGF =NO
3090 CHECK MWE. CONT_MS_RL_MGF =OFF
3091 CHECK MWE. STAT_WPT_STBY =OFF
3092 CHECK MWE. WPT_S1_RET =OFF
3093 CHECK MWE. WPT_S1_EXT =OFF
3094 CHECK MWE. WPT_S1_RELS =OFF
3095 CHECK MWE. WPT_S1_DRV =OFF
3096 CHECK MWE. WPT_S1_MS_HRM =CLS
3097 CHECK MWE. WPT_S1_MS_EXT =NON
3098 CHECK MWE. CONT_LEN_DET_S1 =NO
3099 CHECK MWE. CONT_MS_RL_S1 =OFF
3100 CHECK MWE. WPT_S2_RET =OFF
3101 CHECK MWE. WPT_S2_EXT =OFF
3102 CHECK MWE. WPT_S2_RELS =OFF
3103 CHECK MWE. WPT_S2_DRV =OFF
3104 CHECK MWE. WPT_S2_MS_HRM =CLS
3105 CHECK MWE. WPT_S2_MS_EXT =NON
3106 CHECK MWE. CONT_LEN_DET_S2 =NO
3107 CHECK MWE. CONT_MS_RL_S2 =OFF
3108 #
3109 #
3110 . # < MWE STBY ON >
3111 . MWE. MWE_STBY_ON
3112 . # STBY ON から 5 秒以上経過後、ステータスを QL にてチェックする。
3113 #
3114 . # < CHECK >
3115 CHECK MWE. STAT_STBY =ON
3116 CHECK MWE. MWE_CMND_ADRS 0x0A10

```

3117	CHECK MWE.MWE_CMND_CODE	0x0010			
3118	#				
3119	# QL	Check-Item	Spec	Data	Check
3120	#	-----+-----+-----+-----+-----			
3121	# MW-E	MST_SC_MTR_TEMP	取得のみ	_____°C	N/A
3122	#	MST_SC_DRV_TEMP	取得のみ	_____°C	N/A
3123	#	MST_MGF_MTR_TEMP	取得のみ	_____°C	N/A
3124	#	MST_MGF_DRV_TEMP	取得のみ	_____°C	N/A
3125	#	WPT_S1_MTR_TEMP	取得のみ	_____°C	N/A
3126	#	WPT_S1_CHAS_TEMP	取得のみ	_____°C	N/A
3127	#	WPT_S2_MTR_TEMP	取得のみ	_____°C	N/A
3128	#	WPT_S2_CHAS_TEMP	取得のみ	_____°C	N/A
3129	#	MWE_FPGA_TEMP	取得のみ	_____°C	N/A
3130	#	MWE_TR_TEMP	取得のみ	_____°C	N/A
3131	#				
3132	#	※エラークリアは後でまとめて実施。			
3133	#				
3134	# QL	Check-Item	Spec		Check
3135	#	-----+-----+-----+-----+-----			
3136	# CMD 出力装置	Message	STATUS CHECK 結果 OK		G N
3137	#				
3138	#	-----+-----+-----+-----+-----			
3139	#	6.2.10. MOTOR-PSU チェック			
3140	#	-----+-----+-----+-----+-----			
3141	#				
3142	PCD.ON_OFF_ENA				
3143	#				
3144	# < CHECK >				
3145	CHECK PCD.ON_OFF_ENA_DIS	=ENA			
3146	#				
3147	PCD.PSU_Motor_A_ON				
3148	#				
3149	# < CHECK >				
3150	CHECK PCD.PSU_MORTOR_A_ON_OFF	=ON			
3151	#				
3152	# QL	Check-Item	Spec	Data	Check
3153	#	-----+-----+-----+-----+-----			
3154	# PCS1	PCD.PSU_MOTOR_A_I	取得のみ	_____	N/A
3155	#				
3156	PCD.PSU_Motor_A_OFF				
3157	#				
3158	# < CHECK >				
3159	CHECK PCD.PSU_MORTOR_A_ON_OFF	=OFF			
3160	#				
3161	PCD.PSU_Motor_B_ON				
3162	#				
3163	# < CHECK >				
3164	CHECK PCD.PSU_MORTOR_B_ON_OFF	=ON			
3165	#				
3166	# QL	Check-Item	Spec	Data	Check
3167	#	-----+-----+-----+-----+-----			
3168	# PCS1	PCD.PSU_MOTOR_B_I	取得のみ	_____	N/A

```

3169 . #
3170 . PCD.PSU_Motor_B_OFF
3171 . #
3172 . # < CHECK >
3173 . CHECK PCD.PSU_MORTOR_B_ON_OFF =OFF
3174 . #
3175 . # QL          Check-Item          Spec          Check
3176 . # -----+-----+-----+-----
3177 . # CMD 出力装置  Message          STATUS CHECK 結果 OK      G  N
3178 . #
3179 . # -----
3180 . # 6.2.11. MDM 立ち上げ／初期化
3181 . # -----
3182 . #
3183 . PCD.MDM_E_ON
3184 . #
3185 . # < CHECK >
3186 . CHECK PCD.MDM_E_ON_OFF          =ON
3187 . CHECK MD2F.MS7_LINK_STAT        =ON
3188 . CHECK MD2F.MS7_DAT_CLCT         =ON
3189 . #
3190 . # QL          Check-Item          Spec          Check
3191 . # -----+-----+-----+-----
3192 . # CMD 出力装置  Message          STATUS CHECK 結果 OK      G  N
3193 . #
3194 . # -----
3195 . # 6.2.12. MSASI 立ち上げ／初期化
3196 . # -----
3197 . #
3198 . PCD.MSASI_ON
3199 . #
3200 . # < CHECK >
3201 . CHECK PCD.MSASI_ON_OFF          =ON
3202 . CHECK MD2F.MS6_LINK_STAT        =ON
3203 . CHECK MD2F.MS6_DAT_CLCT         =OFF
3204 . #
3205 . PCD.ON_OFF_DIS
3206 . #
3207 . # < CHECK >
3208 . CHECK PCD.ON_OFF_ENA_DIS        =DIS
3209 . #
3210 . MD1F.DPU_SW_ERR_CLR
3211 . MD2F.DPU_SW_ERR_CLR
3212 . #
3213 . # < CHECK >
3214 . CHECK MD1F.MS0_LINK_ERR          =na
3215 . CHECK MD1F.MS3_LINK_ERR          =na
3216 . CHECK MD1F.MS4_LINK_ERR          =na
3217 . CHECK MD1F.MS5_LINK_ERR          =na
3218 . CHECK MD1F.MS6_LINK_ERR          =na
3219 . CHECK MD1F.MS7_LINK_ERR          =na
3220 . CHECK MD1F.SLOT_ERR              =na

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3221 CHECK MD2F.MS0_LINK_ERR =na
3222 CHECK MD2F.MS1_LINK_ERR =na
3223 CHECK MD2F.MS2_LINK_ERR =na
3224 CHECK MD2F.MS3_LINK_ERR =na
3225 # CHECK MD2F.MS4_LINK_ERR =na
3226 CHECK MD2F.MS5_LINK_ERR =na
3227 CHECK MD2F.MS6_LINK_ERR =na
3228 CHECK MD2F.MS7_LINK_ERR =na
3229 CHECK MD2F.SLOT_ERR =na
3230 #
3231 . # QL Check-Item Spec Check
3232 # -----+-----+-----+-----
3233 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
3234 #
3235 . # =====
3236 . # 6.3. ENA MT テーブルアップデート後チェック
3237 . # =====
3238 #
3239 # ENA HK packet 生成間隔の設定 (16sec)
3240 . #
3241 . MD1U.HK_SET 0x0b 0x04
3242 WAIT_SEC 40
3243 # QL Check-Item Spec Judgment
3244 # -----+-----+-----+-----
3245 . # MD1U HU_Hk_RepInt_ENA 4(16sec) N/A
3246 #
3247 # < mission packet 生成 (HK) >
3248 # 16s
3249 . MD1U.ENA_PRIO_HK_SET 0x05
3250 #
3251 WAIT_SEC 40
3252 #
3253 # < CHECK >
3254 CHECK ENA.ENA_HK_INTVAL =sec_16
3255 #
3256 # < table read mode >
3257 . MD1U.ENA_PROCESS_MODE_SET 0x04 0x00 0x00 0x00 0x00 0xff 0xff 0x00
3258 & 0x00 0x44 0x00 0x00 0x20 0x00 0x22 0x00
3259 & 0x00 0x00 0x00
3260 WAIT_SEC 40
3261 # QL Check-Item Spec Check
3262 # -----+-----+-----+-----
3263 . # MPPE-ENA2 PROCMODE TBLRD N/A
3264 # PROC/SPN n_16 N/A
3265 # ADD/SPN sp_16 N/A
3266 #
3267 # REPRO 終了後 ミッション TLM を確認のこと (PI)
3268 #
3269 WAIT_SEC 60 # 1 分以上待つ
3270 #
3271 #
3272 # < dump mode >

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3273 . MD1U. ENA_PROCESS_MODE_SET 0x06 0x00 0x00 0x00 0x00 0x00 0x00 0x00
3274 &                                0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00
3275 &                                0x00 0x00 0x00
3276 WAIT_SEC 40
3277 #
3278 CHECK ENA. ENA_PROCESS_MODE                =DUMP
3279 #
3280 # < dump start >
3281 # lookup table, MDP EEPROM 上の ENA table 領域のダンプ
3282 #
3283 . MD1U. ENA_DUMP_MDPTBL 0x01 0xff 0xff 0x00 0x00 0x00 0x00
3284 WAIT_SEC 40
3285 #
3286 # 数分かかる
3287 #
3288 # QL          Check-Item          Spec          Check
3289 # -----+-----+-----+-----+
3290 . # MPPE-ENA2  DUMP                END                N/A
3291 #
3292 # REPRO 終了後 ミッション TLM を確認のこと (PI)
3293 #
3294 WAIT_SEC 60                # 1 分以上待つ
3295 #
3296 #
3297 #
3298 # < mass accumulation mode >
3299 . MD1U. ENA_PROCESS_MODE_SET 0x00 0x88 0x10 0x08 0x02 0xff 0xff 0x00
3300 &                                0x00 0x20 0x00 0x00 0x20 0x00 0x22 0x00
3301 &                                0x00 0x00 0x00
3302 #
3303 WAIT_SEC 40
3304 #
3305 # < CHECK >
3306 CHECK ENA. ENA_PROCESS_MODE                =MASS
3307 CHECK ENA. OBS_MODE                        =COINCI
3308 CHECK ENA. SV_WAVE1_TBL                    2
3309 CHECK ENA. SV_WAVE2A_TBL                    2
3310 CHECK ENA. SV_WAVE2B_TBL                    2
3311 CHECK ENA. SV_LENS_TBL                      2
3312 CHECK ENA. ENA_PHASE_NUMID                  =n_16
3313 CHECK ENA. ENA_ENGY_NUMID                    =n_8
3314 CHECK ENA. ENA_CH_NUMID                      =n_8
3315 CHECK ENA. ENA_MASS_NUMID                    =n_8
3316 CHECK ENA. ENA_ACCUM_NUMID                  =sp_4
3317 CHECK ENA. ENA_TRG_ENADIS                  =DIS
3318 #
3319 WAIT_SEC 20                # 1 分以上待つ (40+20 = 60s)
3320 #
3321 # < cal pulse 発生(1kHz) >
3322 . MD1U. ENA_S_CAL_MODE_SET 0x31 0x01 0x01 0x00 0x00 0x02
3323 #
3324 WAIT_SEC 40

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3325 #
3326 # < CHECK >
3327 CHECK ENA.CAL_FREQ =f_1k
3328 CHECK ENA.CAL_STARTR1_ON =ON
3329 CHECK ENA.CAL_STARTS1_ON =ON
3330 CHECK ENA.CAL_STOPS1_ON =ON
3331 CHECK ENA.CAL_TIMING_STARTR 0x00
3332 CHECK ENA.CAL_TIMING_STARTS 0x00
3333 CHECK ENA.CAL_TIMING_STOPS 0x02
3334 #
3335 WAIT_SEC 20 # 1 分以上待つ (40+20 = 60s)
3336 #
3337 # < cal pulse 発生(1kHz) >
3338 MD1U.ENA_S_CAL_MODE_SET 0x32 0x02 0x02 0x00 0x00 0x02
3339 #
3340 WAIT_SEC 40
3341 #
3342 # < CHECK >
3343 CHECK ENA.CAL_FREQ =f_1k
3344 CHECK ENA.CAL_STARTR2_ON =ON
3345 CHECK ENA.CAL_STARTS2_ON =ON
3346 CHECK ENA.CAL_STOPS2_ON =ON
3347 CHECK ENA.CAL_TIMING_STARTR 0x00
3348 CHECK ENA.CAL_TIMING_STARTS 0x00
3349 CHECK ENA.CAL_TIMING_STOPS 0x02
3350 #
3351 WAIT_SEC 20 # 1 分以上待つ (40+20 = 60s)
3352 #
3353 # < cal pulse 発生(1kHz) >
3354 MD1U.ENA_S_CAL_MODE_SET 0x34 0x04 0x04 0x00 0x00 0x02
3355 #
3356 WAIT_SEC 40
3357 #
3358 # < CHECK >
3359 CHECK ENA.CAL_FREQ =f_1k
3360 CHECK ENA.CAL_STARTR3_ON =ON
3361 CHECK ENA.CAL_STARTS3_ON =ON
3362 CHECK ENA.CAL_STOPS3_ON =ON
3363 CHECK ENA.CAL_TIMING_STARTR 0x00
3364 CHECK ENA.CAL_TIMING_STARTS 0x00
3365 CHECK ENA.CAL_TIMING_STOPS 0x02
3366 #
3367 WAIT_SEC 20 # 1 分以上待つ (40+20 = 60s)
3368 #
3369 # < cal pulse 発生(1kHz) >
3370 MD1U.ENA_S_CAL_MODE_SET 0x38 0x08 0x08 0x00 0x00 0x02
3371 #
3372 WAIT_SEC 40
3373 #
3374 # < CHECK >
3375 CHECK ENA.CAL_FREQ =f_1k
3376 CHECK ENA.CAL_STARTR4_ON =ON

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3377 CHECK ENA.CAL_STARTS4_ON          =ON
3378 CHECK ENA.CAL_STOPS4_ON          =ON
3379 CHECK ENA.CAL_TIMING_STARTR      0x00
3380 CHECK ENA.CAL_TIMING_STARTS      0x00
3381 CHECK ENA.CAL_TIMING_STOPS       0x02
3382 #
3383 WAIT_SEC 20                        # 1 分以上待つ (40+20 = 60s)
3384 #
3385 # < cal pulse 発生(1kHz) >
3386 MD1U.ENA_S_CAL_MODE_SET 0x31 0x10 0x10 0x00 0x00 0x02
3387 #
3388 WAIT_SEC 40
3389 #
3390 # < CHECK >
3391 CHECK ENA.CAL_FREQ                =f_1k
3392 CHECK ENA.CAL_STARTR1_ON          =ON
3393 CHECK ENA.CAL_STARTS5_ON          =ON
3394 CHECK ENA.CAL_STOPS5_ON           =ON
3395 CHECK ENA.CAL_TIMING_STARTR      0x00
3396 CHECK ENA.CAL_TIMING_STARTS      0x00
3397 CHECK ENA.CAL_TIMING_STOPS       0x02
3398 #
3399 WAIT_SEC 20                        # 1 分以上待つ (40+20 = 60s)
3400 #
3401 # < cal pulse 発生(1kHz) >
3402 MD1U.ENA_S_CAL_MODE_SET 0x32 0x20 0x20 0x00 0x00 0x02
3403 #
3404 WAIT_SEC 40
3405 #
3406 # < CHECK >
3407 CHECK ENA.CAL_FREQ                =f_1k
3408 CHECK ENA.CAL_STARTR2_ON          =ON
3409 CHECK ENA.CAL_STARTS6_ON          =ON
3410 CHECK ENA.CAL_STOPS6_ON           =ON
3411 CHECK ENA.CAL_TIMING_STARTR      0x00
3412 CHECK ENA.CAL_TIMING_STARTS      0x00
3413 CHECK ENA.CAL_TIMING_STOPS       0x02
3414 #
3415 WAIT_SEC 20                        # 1 分以上待つ (40+20 = 60s)
3416 #
3417 # < cal pulse 発生(1kHz) >
3418 MD1U.ENA_S_CAL_MODE_SET 0x34 0x40 0x40 0x00 0x00 0x02
3419 #
3420 WAIT_SEC 40
3421 #
3422 # < CHECK >
3423 CHECK ENA.CAL_FREQ                =f_1k
3424 CHECK ENA.CAL_STARTR3_ON          =ON
3425 CHECK ENA.CAL_STARTS7_ON          =ON
3426 CHECK ENA.CAL_STOPS7_ON           =ON
3427 CHECK ENA.CAL_TIMING_STARTR      0x00
3428 CHECK ENA.CAL_TIMING_STARTS      0x00

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3429 CHECK ENA.CAL_TIMING_STOPS 0x02
3430 #
3431 WAIT_SEC 20 # 1 分以上待つ (40+20 = 60s)
3432 #
3433 # < cal pulse 発生(1kHz) >
3434 . MD1U.ENA_S_CAL_MODE_SET 0x38 0x40 0x80 0x00 0x00 0x02
3435 #
3436 WAIT_SEC 40
3437 #
3438 # < CHECK >
3439 CHECK ENA.CAL_FREQ =f_1k
3440 CHECK ENA.CAL_STARTR1_ON =ON
3441 CHECK ENA.CAL_STARTS7_ON =ON
3442 CHECK ENA.CAL_STOPS8_ON =ON
3443 CHECK ENA.CAL_TIMING_STARTR 0x00
3444 CHECK ENA.CAL_TIMING_STARTS 0x00
3445 CHECK ENA.CAL_TIMING_STOPS 0x02
3446 #
3447 WAIT_SEC 20 # 1 分以上待つ (40+20 = 60s)
3448 #
3449 # < cal pulse 発生(1kHz) >
3450 . MD1U.ENA_S_CAL_MODE_SET 0x31 0x01 0x01 0x00 0x00 0x02
3451 #
3452 WAIT_SEC 40
3453 #
3454 # < CHECK >
3455 CHECK ENA.CAL_FREQ =f_1k
3456 CHECK ENA.CAL_STARTR1_ON =ON
3457 CHECK ENA.CAL_STARTS1_ON =ON
3458 CHECK ENA.CAL_STOPS1_ON =ON
3459 CHECK ENA.CAL_TIMING_STARTR 0x00
3460 CHECK ENA.CAL_TIMING_STARTS 0x00
3461 CHECK ENA.CAL_TIMING_STOPS 0x02
3462 #
3463 WAIT_SEC 20
3464 #
3465 # < TOF accumulation mode >
3466 . MD1U.ENA_PROCESS_MODE_SET 0x02 0x18 0x01 0x01 0x02 0xff 0xff 0x00
3467 & 0x00 0x20 0x00 0x00 0x20 0x00 0x22 0x00
3468 & 0x00 0x00 0x00
3469 WAIT_SEC 40
3470 # < CHECK >
3471 CHECK ENA.ENA_PROCESS_MODE =TOF
3472 CHECK ENA.OBS_MODE =COINCI
3473 CHECK ENA.SV_WAVE1_TBL 2
3474 CHECK ENA.SV_WAVE2A_TBL 2
3475 CHECK ENA.SV_WAVE2B_TBL 2
3476 CHECK ENA.SV_LENS_TBL 2
3477 CHECK ENA.ENA_PHASE_NUMID =n_1
3478 CHECK ENA.ENA_ENGY_NUMID =n_8
3479 CHECK ENA.ENA_CH_NUMID =n_1
3480 CHECK ENA.ENA_MASS_NUMID =n_1

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3533 . # -----
3534 . # 6.4.3. HEP-E L/M-Mode check
3535 . # -----
3536 . #
3537 . # HEP の健全性確認手順の間 REC を ON にしておく
3538 . #
3539 . DH.DR_REC_ON
3540 . #
3541 . # < CHECK >
3542 . CHECK DH.DR_REC_ON_OFF =ON
3543 . #
3544 . # <Power ON 初期ステータス確認>
3545 . #
3546 . # < CHECK >
3547 . CHECK HEPE.TEMP_MONITOR1 -0.459 43.209
3548 . CHECK HEPE.TEMP_MONITOR2 3.425 48.380
3549 . CHECK HEPE.ELE_ION_FL =ELE
3550 . CHECK HEPE.STBY_OBS_F =SBY
3551 . CHECK HEPE.HV_ELE_CTL =DIS
3552 . CHECK HEPE.HE_ELE_DAC =OFF
3553 . LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3554 . #
3555 . # QL Check-Item Spec Check
3556 . # -----+-----+-----+-----
3557 . # MPPE-HEPE1 HV_MONITOR1 0x0000 or 0x0100 G N
3558 . #
3559 . # ASIC 内部レジスタ 1~16 及び ASIC1~9 Disable ビット設定
3560 . MD1U.HEPE_ASIC_INIT
3561 . #
3562 . # < CHECK >
3563 . SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3564 . WAIT_SEC 2
3565 . LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3566 . #
3567 . # ASIC Gr1 レジスタ書き込みコマンド
3568 . HEPE.REG_WR_GR1
3569 . #
3570 . # < CHECK >
3571 . SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3572 . WAIT_SEC 2
3573 . LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3574 . #
3575 . #
3576 . # 4sec 待機 (処理時間)
3577 . #
3578 . #
3579 . # ASIC Gr2 レジスタ書き込みコマンド
3580 . HEPE.REG_WR_GR2
3581 . #
3582 . # < CHECK >
3583 . SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3584 . WAIT_SEC 2

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3585 LET HEPE_C_BUF_CNT = ( {HEPE.CMD_BUF_WR_CNT} + 4 ) & 0xF
3586 #
3587 . # 4sec 待機 (処理時間)
3588 #
3589 . # PEAK TIME 設定
3590 . HEPE.PEAK 0x0003
3591 #
3592 . # VFP_Gr1 設定
3593 . HEPE.ASIC_GR1_VFP 0x0009
3594 #
3595 . # VFP_Gr2 設定
3596 . HEPE.ASIC_GR2_VFP 0x0009
3597 #
3598 . # TH_BACK 設定
3599 . HEPE.TH_BACK 0x3FFE
3600 #
3601 . # < CHECK >
3602 SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3603 WAIT_SEC 2
3604 LET HEPE_C_BUF_CNT = ( {HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3605 CHECK HEPE.PEAK_TIME_VAL 0x0003
3606 CHECK HEPE.ASIC_GR1_VFP_VAL 0x0009
3607 CHECK HEPE.ASIC_GR2_VFP_VAL 0x0009
3608 CHECK HEPE.TH_BACK_VAL 0x3FFE
3609 #
3610 . # LOAD コマンド送信
3611 . HEPE.LOAD_START
3612 #
3613 . # < CHECK >
3614 SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3615 WAIT_SEC 2
3616 LET HEPE_C_BUF_CNT = ( {HEPE.CMD_BUF_WR_CNT} + 7 ) & 0xF
3617 #
3618 . # 温度補正テーブル初期化
3619 . MD1U.HEPE_TEMP_INIT
3620 #
3621 . # 設定終了まで 30 秒待ち
3622 . # SI にてステータス確認実施
3623 #
3624 . # 外部温度モニタ設定レジスタ設定
3625 . HEPE.TEMP_MONITOR_SEL 0x0000
3626 #
3627 . # < CHECK >
3628 SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3629 WAIT_SEC 2
3630 LET HEPE_C_BUF_CNT = ( {HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3631 #
3632 . # 温度補正_内部/外部コマンド送信 (外部切替)
3633 . HEPE.TEMP_CORRECT_EXT
3634 #
3635 . # < CHECK >
3636 SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}

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3637 WAIT_SEC 2
3638 LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3639 CHECK HEPE.TEMP_CORRECT =EXT
3640 #
3641 . # 4sec 待機 (処理時間)
3642 #
3643 . # 温度補正_内部/外部コマンド送信(内部切替)
3644 . HEPE.TEMP_CORRECT_INT
3645 #
3646 . # < CHECK >
3647 SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3648 CHECK HEPE.TEMP_CORRECT =INT
3649 #
3650 . # ELE-HV 電源 ON コマンド受信 ENA/DIS コマンド(HV-ELE-CTL_ENA/DIS=ENA)
3651 . HEPE.HV_ELE_CTL_ENA
3652 #
3653 . # < CHECK >
3654 CHECK HEPE.HV_ELE_CTL =ENA
3655 #
3656 . # ELE-HV REF 電源 ON/OFF スイッチ切替コマンド(ON/OFF=ON)
3657 . HEPE.HV_ELE_REF_ON
3658 #
3659 . # < CHECK >
3660 CHECK HEPE.HE_ELE_DAC =ON
3661 #
3662 . # ELE-HV 出力電圧設定(DAC 設定値=80h 設定)
3663 . HEPE.HV_ELE_DAC 0x0080
3664 #
3665 . # 35sec 後に以下を確認 (処理時間)
3666 #
3667 . # < CHECK >
3668 CHECK HEPE.HV_ELE_DAC_VAL 0x0080
3669 CHECK HEPE.HV_MONITOR1 95 105
3670 LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3671 #
3672 . # FIFO_RESET コマンド送信
3673 . HEPE.FIFO_RESET
3674 #
3675 . # < CHECK >
3676 SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3677 WAIT_SEC 2
3678 LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3679 #
3680 . # READ_BACK コマンド送信
3681 . HEPE.READ_BACK
3682 #
3683 . # < CHECK >
3684 SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3685 WAIT_SEC 2
3686 LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3687 #
3688 . # 4sec 待機(処理時間)

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3689  #
3690  . # FIFO_RESET コマンド送信
3691  . HEPE.FIFO_RESET
3692  #
3693  . # < CHECK >
3694  SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3695  #
3696  . # CAL_DAC 設定
3697  . HEPE.CAL_DAC 0x0010
3698  #
3699  . # < CHECK >
3700  CHECK HEPE.CAL_DAC_VAL 0x0010
3701  #
3702  . # CAL_ASIC_ID 設定 Gr1
3703  . HEPE.CAL_ASIC_ID 0x0002
3704  #
3705  . # < CHECK >
3706  CHECK HEPE.CAL_ASIC_ID_VAL 0x0002
3707  LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3708  #
3709  . # 60sec 待機 (処理時間)
3710  #
3711  . # FIFO_RESET コマンド送信
3712  . HEPE.FIFO_RESET
3713  #
3714  . # < CHECK >
3715  SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3716  #
3717  . # CAL_ASIC_ID 設定 Gr1
3718  . HEPE.CAL_ASIC_ID 0x0004
3719  #
3720  . # < CHECK >
3721  CHECK HEPE.CAL_ASIC_ID_VAL 0x0004
3722  LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3723  #
3724  . # 60sec 待機 (処理時間)
3725  #
3726  . # FIFO_RESET コマンド送信
3727  . HEPE.FIFO_RESET
3728  #
3729  . # < CHECK >
3730  SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE_C_BUF_CNT}
3731  #
3732  . # CAL_ASIC_ID 設定 Gr2
3733  . HEPE.CAL_ASIC_ID 0x0006
3734  #
3735  . # < CHECK >
3736  CHECK HEPE.CAL_ASIC_ID_VAL 0x0006
3737  LET HEPE_C_BUF_CNT = ({HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3738  #
3739  . # 60sec 待機 (処理時間)
3740  #

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3741 . # FIFO_RESET コマンド送信
3742 . HEPE.FIFO_RESET
3743 . #
3744 . # < CHECK >
3745 . SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE.C_BUF_CNT}
3746 . WAIT_SEC 2
3747 . LET HEPE.C_BUF_CNT = ( {HEPE.CMD_BUF_WR_CNT} + 1 ) & 0xF
3748 . #
3749 . # SSD モード切替コマンド送信 (SSD/OFF=SSD)
3750 . HEPE.SSD_ON
3751 . #
3752 . # < CHECK >
3753 . SYSTEM test {HEPE.CMD_BUF_WR_CNT} = {HEPE.C_BUF_CNT}
3754 . #
3755 . # 測定モード切替コマンド送信 (STBY/START=START)
3756 . HEPE.OBS_START
3757 . #
3758 . # < CHECK >
3759 . CHECK HEPE.SSD_OFF_FL =OBS
3760 . CHECK HEPE.STBY_OBS_F =OBS
3761 . #
3762 . #
3763 . # 600sec 待機 (処理時間)
3764 . #
3765 . #
3766 . HEPE.HV_ELE_REF_OFF
3767 . #
3768 . MD1U.TLMH_TRG_SET 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00
3769 . MD2U.TLMH_TRG_SET 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00
3770 . #
3771 . # QL Check-Item Spec Check
3772 . # -----
3773 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
3774 . #
3775 . # -----
3776 . # 6.4.4. HEP-I L/M-Mode check
3777 . # -----
3778 . #
3779 . # <初期ステータス確認>
3780 . #
3781 . # < CHECK >
3782 . CHECK HEPI.TEMP_MONITOR1 3.526 48.013
3783 . CHECK HEPI.TEMP_MONITOR2 1.060 45.286
3784 . CHECK HEPI.ELE_ION_FL =ION
3785 . CHECK HEPI.STBY_OBS_F =SBY
3786 . CHECK HEPI.HV_ION_H_CTL =DIS
3787 . CHECK HEPI.HV_ION_L_CTL =DIS
3788 . CHECK HEPI.HE_ION_H_DAC =OFF
3789 . CHECK HEPI.HE_ION_DAC =OFF
3790 . LET HEPI.C_BUF_CNT = ( {HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3791 . #
3792 . # QL Check-Item Spec Check

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3793	# -----+-----+-----+-----		
3794	# MPPE-HEPI1 HV_MONITOR1	0x0000 or 0x0100	G N
3795	# HV_MONITOR2	0x0000 or 0x0100	G N
3796	# HV_MONITOR3	0x0000 or 0x0100	G N
3797	#		
3798	. # ASIC 内部 Resister1~16 及び ASIC1~9 Disable-Bit 設定		
3799	. MD1U.HEPI_ASIC_INIT		
3800	#		
3801	. # < CHECK >		
3802	SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}		
3803	WAIT_SEC 2		
3804	LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1) & 0xF		
3805	#		
3806	. # ASIC Gr1 レジスタ書き込みコマンド		
3807	. HEPI.REG_WR_GR1		
3808	#		
3809	. # < CHECK >		
3810	SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}		
3811	WAIT_SEC 2		
3812	LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1) & 0xF		
3813	#		
3814	. # WAIT 4sec (Writing ASIC Gr1 register)		
3815	#		
3816	. # ASIC Gr2 レジスタ書き込みコマンド		
3817	. HEPI.REG_WR_GR2		
3818	#		
3819	. # < CHECK >		
3820	SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}		
3821	#		
3822	. # WAIT 4sec (Writing ASIC Gr2 register)		
3823	#		
3824	. # PEAK TIME Setting		
3825	. HEPI.PEAK 0x0003		
3826	#		
3827	. # VFP_Gr1 設定		
3828	. HEPI.ASIC_GR1_VFP 0x0009		
3829	#		
3830	. # VFP_Gr2 設定		
3831	. HEPI.ASIC_GR2_VFP 0x0009		
3832	#		
3833	. # TH_ANTI 設定		
3834	. HEPI.TH_ANTI 0x0001		
3835	#		
3836	. # TH_BACK 設定		
3837	. HEPI.TH_BACK 0x3FFE		
3838	#		
3839	. # < CHECK >		
3840	CHECK HEPI.PEAK_TIME_VAL	0x0003	
3841	CHECK HEPI.ASIC_GR1_VFP_VAL	0x0009	
3842	CHECK HEPI.ASIC_GR2_VFP_VAL	0x0009	
3843	CHECK HEPI.TH_ANTI_VAL	0x0001	
3844	CHECK HEPI.TH_BACK_VAL	0x3FFE	

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3845 LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3846 #
3847 . # LOAD コマンド送信
3848 . HEPI.LOAD_START
3849 #
3850 . # < CHECK >
3851 SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
3852 WAIT_SEC 2
3853 LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 7 ) & 0xF
3854 #
3855 . # 温度補正テーブル設定
3856 . MD1U.HEPI_TEMP_INIT
3857 #
3858 #
3859 . # 設定終了まで 30 秒待ち
3860 . # SI にてステータス確認実施
3861 #
3862 #
3863 . # 外部温度モニタ設定レジスタ設定
3864 . HEPI.TEMP_MONITOR_SEL 0x00BE
3865 #
3866 . # < CHECK >
3867 SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
3868 WAIT_SEC 2
3869 LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3870 #
3871 . # 温度補正_内部/外部コマンド送信(外部切替)
3872 . HEPI.TEMP_CORRECT_EXT
3873 #
3874 . # < CHECK >
3875 SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
3876 WAIT_SEC 2
3877 LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3878 CHECK HEPI.TEMP_CORRECT =EXT
3879 #
3880 . # WAIT 4sec (処理時間)
3881 #
3882 . # 温度補正_内部/外部コマンド送信(内部切替)
3883 . HEPI.TEMP_CORRECT_INT
3884 #
3885 . # < CHECK >
3886 SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
3887 WAIT_SEC 2
3888 LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3889 CHECK HEPI.TEMP_CORRECT =INT
3890 #
3891 . # ION-HV-LOW 電源 ON コマンド受信 ENA/DIS コマンド (HV-ION-L-CTL_ENA/DIS=ENA)
3892 . HEPI.HV_ION_L_CTL_ENA
3893 #
3894 . # < CHECK >
3895 CHECK HEPI.HV_ION_L_CTL =ENA
3896 #

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3897 . # ION-HV-LOW REF 電源 ON/OFF スイッチ切替コマンド (ON/OFF=ON)
3898 . HEPI.HV_ION_REF_ON
3899 . #
3900 . # < CHECK >
3901 . CHECK HEPI.HE_ION_DAC =ON
3902 . #
3903 . # ION-HV-ch1 出力電圧設定 (Ch1 DAC 設定値=80h 設定)
3904 . HEPI.HV_ION_DAC_CH1 0x0080
3905 . #
3906 . # 45sec 待機後に以下を確認
3907 . #
3908 . # < CHECK >
3909 . CHECK HEPI.HV_ION_DAC1_VAL 0x0080
3910 . CHECK HEPI.HV_MONITOR1 50 90
3911 . #
3912 . #
3913 . # ION-HV-HIGH 電源 ON コマンド受信 ENA/DIS コマンド (HV-ION-H-CTL_ENA/DIS=ENA)
3914 . HEPI.HV_ION_H_CTL_ENA
3915 . #
3916 . # < CHECK >
3917 . CHECK HEPI.HV_ION_H_CTL =ENA
3918 . #
3919 . # ION-HV-HIGH REF 電源 ON/OFF SW 切替コマンド (ON/OFF=ON)
3920 . HEPI.HV_ION_H_REF_ON
3921 . #
3922 . # < CHECK >
3923 . CHECK HEPI.HE_ION_H_DAC =ON
3924 . #
3925 . # ION-HV-ch2 出力電圧設定 (Ch2 DAC 設定値=01h 設定)
3926 . HEPI.HV_ION_DAC_CH2 0x0001
3927 . #
3928 . # < CHECK >
3929 . CHECK HEPI.HV_ION_DAC2_VAL 0x0001
3930 . #
3931 . # WAIT 4sec (処理時間)
3932 . #
3933 . # < CHECK >
3934 . CHECK HEPI.HV_MONITOR2 0 14
3935 . #
3936 . # ION-HV-ch3 出力電圧設定 (Ch3 DAC 設定値=01h 設定)
3937 . HEPI.HV_ION_DAC_CH3 0x0001
3938 . #
3939 . # < CHECK >
3940 . CHECK HEPI.HV_ION_DAC3_VAL 0x0001
3941 . LET HEPI.C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3942 . #
3943 . # WAIT 4sec (処理時間)
3944 . #
3945 . # < CHECK >
3946 . CHECK HEPI.HV_MONITOR3 0 35
3947 . #
3948 . # FIFO_RESET コマンド送信

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3949 . HEPI.FIFO_RESET
3950 #
3951 . # < CHECK >
3952 SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
3953 WAIT_SEC 2
3954 LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3955 . #
3956 . # READ_BACK コマンド送信
3957 . HEPI.READ_BACK
3958 #
3959 . # < CHECK >
3960 SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
3961 #
3962 . # CAL_DAC 設定
3963 . HEPI.CAL_DAC 0x0010
3964 . #
3965 . # < CHECK >
3966 CHECK HEPI.CAL_DAC_VAL 0x0010
3967 #
3968 . # CAL_ASIC_ID 設定 Gr1
3969 . HEPI.CAL_ASIC_ID 0x0001
3970 . #
3971 . # < CHECK >
3972 CHECK HEPI.CAL_ASIC_ID_VAL 0x0001
3973 LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3974 #
3975 . # WAIT 60sec (Calibration Gr1)
3976 #
3977 . # FIFO_RESET コマンド送信
3978 . HEPI.FIFO_RESET
3979 #
3980 . # < CHECK >
3981 SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
3982 WAIT_SEC 2
3983 LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3984 #
3985 . # CAL_ASIC_ID 設定 Gr2
3986 . HEPI.CAL_ASIC_ID 0x0003
3987 . #
3988 . # < CHECK >
3989 CHECK HEPI.CAL_ASIC_ID_VAL 0x0003
3990 LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
3991 #
3992 . # WAIT 60sec (Calibration Gr2)
3993 #
3994 . # FIFO_RESET コマンド送信
3995 . HEPI.FIFO_RESET
3996 #
3997 . # < CHECK >
3998 SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
3999 WAIT_SEC 2
4000 LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF

```

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```

4001  #
4002  . # SSD モード切替コマンド送信 (SSD/OFF=SSD)
4003  . HEPI.SSD_ON
4004  #
4005  . # < CHECK >
4006  . SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4007  #
4008  . # 測定モード切替コマンド送信 (STBY/START=START)
4009  . HEPI.OBS_START
4010  . #
4011  . # < CHECK >
4012  . CHECK HEPI.OBS_STBY_START_RCV                =OBS
4013  . CHECK HEPI.SSD_OFF_FL                        =OBS
4014  . CHECK HEPI.STBY_OBS_F                        =OBS
4015  #
4016  . # 600sec 待機 (処理時間)
4017  #
4018  . # SSD モード切替コマンド送信 (SSD/OFF=OFF)
4019  . HEPI.SSD_OFF
4020  #
4021  . # < CHECK >
4022  . CHECK HEPI.SSD_OFF_FL                        =OFF
4023  . CHECK HEPI.STBY_OBS_F                        =SBY
4024  #
4025  . # TOF モード切替コマンド送信 (TOF/OFF=TOF)
4026  . HEPI.TOF_ON
4027  #
4028  . # < CHECK >
4029  . CHECK HEPI.TOF_OFF_FL                        =OBS
4030  . CHECK HEPI.STBY_OBS_F                        =OBS
4031  . LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4032  #
4033  . # TOF_CODE, PAR 設定 1
4034  . HEPI.TOF_CMD 0x0008 0x0001 0x0000 0x0000
4035  #
4036  . # < CHECK >
4037  . SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4038  . WAIT_SEC 2
4039  . LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4040  #
4041  . # TOF_COM コマンド送信 1
4042  . HEPI.TOF_COM
4043  #
4044  . # < CHECK >
4045  . SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4046  . WAIT_SEC 2
4047  . LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4048  #
4049  . # TOF_CODE, PAR 設定 2
4050  . HEPI.TOF_CMD 0x0009 0x0001 0x0000 0x0000
4051  #
4052  . # < CHECK >

```

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```

4053  SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4054  WAIT_SEC 2
4055  LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4056  #
4057  . # TOF_COM コマンド送信 2
4058  . HEPI.TOF_COM
4059  #
4060  . # < CHECK >
4061  SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4062  WAIT_SEC 2
4063  LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4064  #
4065  . # TOF_CODE, PAR 設定 3
4066  . HEPI.TOF_CMD 0x000A 0x0001 0x0001 0x0001
4067  #
4068  . # < CHECK >
4069  SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4070  WAIT_SEC 2
4071  LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4072  #
4073  . # TOF_COM コマンド送信 3
4074  . HEPI.TOF_COM
4075  #
4076  . # < CHECK >
4077  SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4078  #
4079  . # 4sec 待機 (処理時間)
4080  #
4081  . # SSD モード切替コマンド送信 (SSD/OFF=OFF)
4082  . HEPI.SSD_OFF
4083  #
4084  . # < CHECK >
4085  CHECK HEPI.SSD_OFF_FL                                =OFF
4086  #
4087  . # TOF モード切替コマンド送信 (TOF/OFF=TOF)
4088  . HEPI.TOF_ON
4089  #
4090  . # < CHECK >
4091  CHECK HEPI.TOF_OFF_FL                                =OBS
4092  CHECK HEPI.STBY_OBS_F                                =OBS
4093  LET HEPI_C_BUF_CNT = ({HEPI.CMD_BUF_WR_CNT} + 1 ) & 0xF
4094  #
4095  . # FIFO_RESET コマンド送信
4096  . HEPI.FIFO_RESET
4097  #
4098  . # < CHECK >
4099  SYSTEM test {HEPI.CMD_BUF_WR_CNT} = {HEPI_C_BUF_CNT}
4100  #
4101  . # 120sec 待機 (処理時間)
4102  #
4103  . # SSD モード切替コマンド送信 (SSD/OFF=SSD)
4104  . HEPI.SSD_ON

```

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```

4105 . #
4106 . # < CHECK >
4107 . CHECK HEPI.SSD_OFF_FL                      =OBS
4108 . #
4109 . # 120sec 待機 (処理時間)
4110 . #
4111 . HEPI.HV_ION_REF_OFF
4112 . DH.DR_REC_OFF
4113 . #
4114 . # < CHECK >
4115 . CHECK DH.DR_REC_ON_OFF                      =OFF
4116 . #
4117 . # QL          Check-Item          Spec          Check
4118 . # -----+-----+-----+-----+
4119 . # CMD 出力装置  Message          STATUS CHECK 結果 OK      G    N
4120 . #
4121 . # -----
4122 . # 6.4.5. HEP-I 立ち下げ
4123 . # -----
4124 . #
4125 . MD1F.HEPI_CLCT_STOP
4126 . #
4127 . # < CHECK >
4128 . CHECK MD1F.MS7_DAT_CLCT                    =OFF
4129 . #
4130 . PCD.ON_OFF_ENA
4131 . #
4132 . # < CHECK >
4133 . CHECK PCD.ON_OFF_ENA_DIS                    =ENA
4134 . #
4135 . PCD.PSU_HEP_i_OFF
4136 . #
4137 . # < CHECK >
4138 . CHECK PCD.PSU_HEP_I_ON_OFF                  =OFF
4139 . CHECK MD1F.MS7_LINK_STAT                    =OFF
4140 . #
4141 . # QL          Check-Item          Spec          Check
4142 . # -----+-----+-----+-----+
4143 . # CMD 出力装置  Message          STATUS CHECK 結果 OK      G    N
4144 . #
4145 . # -----
4146 . # 6.4.6. HEP-E 立ち下げ
4147 . # -----
4148 . #
4149 . MD1F.HEPE_CLCT_STOP
4150 . #
4151 . # < CHECK >
4152 . CHECK MD1F.MS6_DAT_CLCT                    =OFF
4153 . #
4154 . PCD.PSU_HEP_e_OFF
4155 . #
4156 . # < CHECK >

```

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```

4157 CHECK PCD.PSU_HEP_E_ON_OFF =OFF
4158 CHECK MD1F.MS6_LINK_STAT =OFF
4159 . #
4160 . PCD.ON_OFF_DIS
4161 . #
4162 . # < CHECK >
4163 CHECK PCD.ON_OFF_ENA_DIS =DIS
4164 . #
4165 . # QL Check-Item Spec Check
4166 . # -----+-----+-----+-----
4167 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
4168 . #
4169 . #
4170 . # =====
4171 . # 6.5. MEA1 Check
4172 . # =====
4173 . # -----
4174 . # 6.5.1. MEA1 function check
4175 . # -----
4176 . # ::::::::::::::::::::::::::::::::::::::::::::::::::::::::::::
4177 . # 6.5.1-1. MEA1 Startup
4178 . # <<M15-1 Initial Power ON Duration:10 min Day:35>>
4179 . # ::::::::::::::::::::::::::::::::::::::::::::::::::::::::::::
4180 . #
4181 . # # MEA1 の電源を ON する。
4182 . # Power on MEA1 by switching on.
4183 . #
4184 . PCD.ON_OFF_ENA
4185 WAIT_SEC 40
4186 . #
4187 . # < CHECK >
4188 CHECK PCD.ON_OFF_ENA_DIS =ENA
4189 . #
4190 . PCD.PSU_MEA1_ON
4191 WAIT_SEC 40
4192 . #
4193 . #
4194 . # < CHECK >
4195 CHECK PCD.PSU_MEA1_ON_OFF =ON
4196 . #
4197 . PCD.ON_OFF_DIS
4198 WAIT_SEC 40
4199 . #
4200 . # < CHECK >
4201 CHECK PCD.ON_OFF_ENA_DIS =DIS
4202 CHECK MD1F.MS1_LINK_STAT =ON
4203 . #
4204 . # # SW Error clear
4205 . MD1F.DPU_SW_ERR_CLR
4206 WAIT_SEC 40
4207 . #
4208 . # < CHECK >

```

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```

4209 CHECK MD1F.MS1_LINK_ERR =na
4210 CHECK MD1F.SLOT_ERR =na
4211 #
4212 #
4213 #
4214 WAIT_SEC 300
4215 #
4216 #
4217 # # MEA1 テレメトリ表示の確認(LINK 確立から 8 秒以上経過後に実施)
4218 # Confirmation of the MEA1 telemetry indication
4219 # (More than 8 minutes after from LINK establishment)
4220 #
4221 # < CHECK >
4222 CHECK MEA1.HK_I_P5V <110 # mA
4223 CHECK MEA1.HK_I_P12V <17 # mA
4224 CHECK MEA1.HK_MCP 0 # V
4225 CHECK MEA1.I_MCP 0 # uA
4226 CHECK MEA1.HK_TMP_FPG <30 # deg.
4227 CHECK MEA1.HK_TMP_MCP <30 # deg.
4228 CHECK MEA1.Status_reg 0x80 # (initial value)
4229 CHECK MEA1.l1_cmd_exe =0xFFFF
4230 CHECK MEA1.l2_cmd_exe =0xFFFF
4231 #
4232 # .....
4233 # 6.5.1-2. SWITCH ON SCAN, 10 min
4234 # .....
4235 #
4236 # MEA1.Scan_ON
4237 WAIT_SEC 40
4238 #
4239 # <CHECK>
4240 CHECK MEA1.l1_cmd_exe =0x2500
4241 #
4242 # Select the sun pulse synchronization
4243 # MEA1.Md_S_Auto
4244 WAIT_SEC 40
4245 #
4246 # <CHECK>
4247 CHECK MEA1.l1_cmd_exe =0x3100
4248 #
4249 # Select energies mode : mode 32 energies/32 sweeps
4250 # -> HV table n° 2, 3eV-3KeV (~300V maxi on the analyzer)"
4251 #
4252 # MEA1.mode_ROM_3 0x02
4253 WAIT_SEC 40
4254 #
4255 # <CHECK>
4256 CHECK MEA1.l1_cmd_exe =0x0202
4257 #
4258 #
4259 WAIT_SEC 300
4260 #

```

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```

4261 . # <CHECK>
4262 . # Check HKP TmSStp = HKP TmSStp_dft
4263 . #
4264 . ## *****
4265 . # WAIT until (5 min maximum) HKP TmSStp = TmSStp_dft to indicate the synchronization
4266 . # between sun plus and MEA.
4267 . # If not, do not execute the next steps!!"
4268 . # Probably, sun pulse signal have a problem (cf previous test at ISAS)!!"
4269 . # If already executed, you can pass at the next step."
4270 . ## *****
4271 . #
4272 . #
4273 . # QL          Check-Item          Spec          Check
4274 . # -----+-----+-----+-----+
4275 . # CMD 出力装置  Message          STATUS CHECK 結果 OK      G  N
4276 . #
4277 . #
4278 . # .....
4279 . # 6.5.1-3. TEST MODE ON, 10 min
4280 . # .....
4281 . #
4282 . #
4283 . # Swtich ON the test mode for the Amplifier A111
4284 . # to verify the good working of this
4285 . MEA1.Md_Tst_ON
4286 . WAIT_SEC 300
4287 . #
4288 . # .....
4289 . # 6.5.1-4. TEST MODE OFF, 5 min
4290 . # .....
4291 . #
4292 . #
4293 . # Stop the A111 test Mode
4294 . MEA1.Md_Tst_OF
4295 . WAIT_SEC 600
4296 . #
4297 . # <CHECK>
4298 . CHECK MEA1.l1_cmd_exe =0x3800
4299 . CHECK MEA1.Status_reg 0x80 # (bit n° 0 = 0)
4300 . #
4301 . # - wait 5 min to verify the Science Data are return to zero
4302 . # Check the science Data and compare with the previous value
4303 . #
4304 . #
4305 . # QL          Check-Item          Spec          Check
4306 . # -----+-----+-----+-----+
4307 . # CMD 出力装置  Message          STATUS CHECK 結果 OK      G  N
4308 . #
4309 . #
4310 . #
4311 . # .....
4312 . # 6.5.1-5. SWITCH OFF the SCAN (5 minutes)

```

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```

4313 . # .....
4314 . #
4315 . #
4316 . # SWITCH OFF THE SCAN
4317 . MEA1.Scan_OF
4318 . WAIT_SEC 300
4319 . #
4320 . # <CHECK>
4321 . CHECK MEA1.l1_cmd_exe =0x2400
4322 . #
4323 . # QL          Check-Item          Spec          Check
4324 . # -----+-----+-----+-----
4325 . # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
4326 . #
4327 . #
4328 . # -----
4329 . # 6.5.2. MEA1 SHUTDOWN
4330 . # -----
4331 . #
4332 . PCD.ON_OFF_ENA
4333 . WAIT_SEC 40
4334 . #
4335 . # < CHECK >
4336 . CHECK PCD.ON_OFF_ENA_DIS =ENA
4337 . #
4338 . MD1F.MEA1_CLCT_STOP
4339 . WAIT_SEC 40
4340 . #
4341 . # < CHECK >
4342 . CHECK MD1F.MS1_DAT_CLCT =OFF
4343 . #
4344 . PCD.PSU_MEA1_OFF
4345 . WAIT_SEC 40
4346 . #
4347 . # < CHECK >
4348 . CHECK PCD.PSU_MEA1_ON_OFF =OFF
4349 . CHECK MD1F.MS1_LINK_STAT =OFF
4350 . #
4351 . PCD.ON_OFF_DIS
4352 . WAIT_SEC 40
4353 . #
4354 . # < CHECK >
4355 . CHECK PCD.ON_OFF_ENA_DIS =DIS
4356 . #
4357 . # QL          Check-Item          Spec          Check
4358 . # -----+-----+-----+-----
4359 . # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
4360 . #
4361 . #
4362 . #
4363 . # =====
4364 . # 6.6. MEA2 Check

```

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```

4365 . # =====
4366 . # -----
4367 . # 6.6.1. MEA2 function check
4368 . # -----
4369 . # .....
4370 . # 6.6.1-1. MEA2 Startup
4371 . # <<M16-1 Initial Power ON Duration:10 min Day:35>>
4372 . # .....
4373 . #
4374 . # # MEA2 の電源を ON する。
4375 . # Power on MEA2 by switching on.
4376 . #
4377 . PCD.ON_OFF_ENA
4378 . WAIT_SEC 40
4379 . #
4380 . # < CHECK >
4381 . CHECK PCD.ON_OFF_ENA_DIS =ENA
4382 . #
4383 . PCD.PSU_MEA2_ON
4384 . WAIT_SEC 40
4385 . #
4386 . #
4387 . # < CHECK >
4388 . CHECK PCD.PSU_MEA2_ON_OFF =ON
4389 . #
4390 . PCD.ON_OFF_DIS
4391 . WAIT_SEC 40
4392 . #
4393 . # < CHECK >
4394 . CHECK PCD.ON_OFF_ENA_DIS =DIS
4395 . CHECK MD1F.MS2_LINK_STAT =ON
4396 . #
4397 . # # SW Error clear
4398 . MD1F.DPU_SW_ERR_CLR
4399 . WAIT_SEC 40
4400 . #
4401 . # < CHECK >
4402 . CHECK MD1F.MS2_LINK_ERR =na
4403 . CHECK MD1F.SLOT_ERR =na
4404 . #
4405 . #
4406 . #
4407 . WAIT_SEC 560
4408 . #
4409 . #
4410 . # # MEA2 テレメトリ表示の確認 (LINK 確立から 8 分以上経過後に実施)
4411 . # Confirmation of the MEA2 telemetry indication
4412 . # (More than 8 minutes after from LINK establishment)
4413 . #
4414 . # < CHECK >
4415 . CHECK MEA2.HK_I_P5V <110 # mA
4416 . CHECK MEA2.HK_I_P12V <17 # mA

```

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```

4417 CHECK MEA2.HK_MCP 0 # V
4418 CHECK MEA2.I_MCP 0 # uA
4419 CHECK MEA2.HK_TMP_FPG <30 # deg. (depend on the environment,maximum 85° C)
4420 CHECK MEA2.HK_TMP_MCP <30 # deg. (depend on the environment,maximum 80° C)
4421 CHECK MEA2.Status_reg 0x80 # (initial value)
4422 CHECK MEA2.l1_cmd_exe =0xFFFF
4423 CHECK MEA2.l2_cmd_exe =0xFFFF
4424 #
4425 #
4426 #
4427 # .....
4428 . # 6.6.1-2. SWITCH ON SCAN, 10 min
4429 # .....
4430 #
4431 . MEA2.Scan_ON
4432 WAIT_SEC 40
4433 #
4434 . # <CHECK>
4435 CHECK MEA2.l1_cmd_exe =0x2500
4436 #
4437 # Select the sun pulse synchronization
4438 . MEA2.Md_S_Auto
4439 WAIT_SEC 40
4440 #
4441 # <CHECK>
4442 CHECK MEA2.l1_cmd_exe =0x3100
4443 #
4444 # Select energies mode : mode 32 energies/32 sweeps
4445 # -> HV table n° 2, 3eV-3KeV (~300V maxi on the analyzer)"
4446 #
4447 . MEA2.mode_ROM_3 0x02
4448 WAIT_SEC 40
4449 #
4450 # <CHECK>
4451 CHECK MEA2.l1_cmd_exe =0x0202
4452 #
4453 #
4454 WAIT_SEC 300
4455 #
4456 . # <CHECK>
4457 # Check HKP TmSStp = HKP TmSStp_dft
4458 #
4459 ## *****
4460 # WAIT until (5 min maximum) HKP TmSStp = TmSStp_dft to indicate the synchronization
4461 # between sun plus and MEA.
4462 # If not, do not execute the next steps!!"
4463 # Probably, sun pulse signal have a problem (cf previous test at ISAS)!!"
4464 # If already executed, you can pass at the next step."
4465 ## *****
4466 #
4467 #
4468 . # QL Check-Item Spec Check

```

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```

4469 # -----+-----+-----+-----
4470 # CMD 出力装置      Message                STATUS CHECK 結果 OK      G  N
4471 #
4472 #
4473 # .....
4474 . # 6.6.1-3. TEST MODE ON, 10 min
4475 # .....
4476 #
4477 #
4478 . MEA2.Md_Tst_ON
4479 WAIT_SEC 300
4480 #
4481 . # <CHECK>
4482 CHECK MEA2.l1_cmd_exe =0x3900
4483 CHECK MEA2.Status_reg 0x81 # (bit n° 0 = 1)
4484 #
4485 # - wait 5 min to verify the Science Data
4486 # Check the science Data and compare with the previous value
4487 #
4488 #
4489 #
4490 . # QL                Check-Item                Spec                Check
4491 # -----+-----+-----+-----
4492 # CMD 出力装置      Message                STATUS CHECK 結果 OK      G  N
4493 #
4494 #
4495 #
4496 # .....
4497 . # 6.6-1.4. TEST MODE OFF, 10 min
4498 # .....
4499 #
4500 #
4501 # Stop the A111 test Mode
4502 . MEA2.Md_Tst_OF
4503 WAIT_SEC 600
4504 #
4505 . # <CHECK>
4506 CHECK MEA2.l1_cmd_exe =0x3800
4507 CHECK MEA2.Status_reg 0x80 # (bit n° 0 = 0)
4508 #
4509 # - wait 10 min to verify the Science Data are return to zero
4510 # Check the science Data and compare with the previous value
4511 #
4512 #
4513 . # QL                Check-Item                Spec                Check
4514 # -----+-----+-----+-----
4515 # CMD 出力装置      Message                STATUS CHECK 結果 OK      G  N
4516 #
4517 #
4518 #
4519 # .....
4520 . # 6.6.1-5. SWITCH OFF the SCAN (5 minutes)

```

```

4521 # .....
4522 #
4523 #
4524 # SWITCH OFF THE SCAN
4525 . MEA2.Scan_OF
4526 WAIT_SEC 300
4527 #
4528 . # <CHECK>
4529 CHECK MEA2.l1_cmd_exe =0x2400
4530 #
4531 #
4532 . # QL          Check-Item          Spec          Check
4533 # -----+-----+-----+-----
4534 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
4535 #
4536 #
4537 #
4538 # -----
4539 . # 6.6.2. MEA2 SHUTDOWN
4540 # -----
4541 . #
4542 . PCD.ON_OFF_ENA
4543 WAIT_SEC 40
4544 #
4545 # < CHECK >
4546 CHECK PCD.ON_OFF_ENA_DIS =ENA
4547 #
4548 . MD1F.MEA2_CLCT_STOP
4549 WAIT_SEC 40
4550 #
4551 # < CHECK >
4552 CHECK MD1F.MS2_DAT_CLCT =OFF
4553 #
4554 . PCD.PSU_MEA2_OFF
4555 WAIT_SEC 40
4556 #
4557 . # < CHECK >
4558 CHECK PCD.PSU_MEA2_ON_OFF =OFF
4559 CHECK MD1F.MS2_LINK_STAT =OFF
4560 #
4561 . PCD.ON_OFF_DIS
4562 WAIT_SEC 40
4563 #
4564 # < CHECK >
4565 CHECK PCD.ON_OFF_ENA_DIS =DIS
4566 #
4567 #
4568 . # QL          Check-Item          Spec          Check
4569 # -----+-----+-----+-----
4570 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
4571 #
4572 #

```

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```

4573 . # =====
4574 . # 6.7 DPU2 伸展 HK ソフト動作確認
4575 . # =====
4576 . # -----
4577 . # 6.7.1. MDP 伸展 HK 生成ソフト起動 (extension mode)
4578 . # -----
4579 . #
4580 . #
4581 . # < MDP 伸展 HK 生成ソフト起動 >
4582 . MD2F.APP15_START
4583 . WAIT_SEC 5
4584 . #
4585 . # < CHECK >
4586 . CHECK MD2F.TASK_STAT_AP15 =ON
4587 . CHECK MD2F.DPU_SW_STATE 3
4588 . #
4589 . # QL Check-Item Spec Check
4590 . # -----+-----+-----+-----
4591 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
4592 . #
4593 . # =====
4594 . # 6.7.2. 伸展 HK ソフト停止 (exit extension mode)
4595 . # =====
4596 . #
4597 . # < MDP 伸展 HK 生成ソフト停止 >
4598 . MD2U.APP15_SELFSTOP
4599 . WAIT_SEC 5
4600 . #
4601 . # <CHECK>
4602 . CHECK MD2F.TASK_STAT_AP15 =OFF
4603 . CHECK MD2F.DPU_SW_STATE 2
4604 . #
4605 . #
4606 . # QL Check-Item Spec Check
4607 . # -----+-----+-----+-----
4608 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
4609 . #
4610 . # *****
4611 . # 7. 立ち下げ
4612 . # *****
4613 . # =====
4614 . # 7.1. SI 機器立ち下げ
4615 . # =====
4616 . # -----
4617 . # 7.1.1. MSASI 立ち下げ
4618 . # -----
4619 . #
4620 . MSAS.TO_POWER_OFF
4621 . #
4622 . # < CHECK >
4623 . CHECK MSAS.HV_ON_OFF =OFF
4624 . CHECK MSAS.HV_MONI_N4K =0.00

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4625 CHECK MSAS.HV_MONI_P6K =0.00
4626 CHECK MSAS.HT_CTRL_ON_OFF =OFF
4627 CHECK MSAS.HT_ON_OFF =OFF
4628 CHECK MSAS.G_ON_OFF =OFF
4629 . #
4630 . # QL          Check-Item          Spec          Data          Check
4631 . # -----+-----+-----+-----+-----
4632 . # MSASI
4633 . #          P15V_V_L          12-16V          _____V          G  N
4634 . #          P15V_I_L          0.01-0.3A       _____A          G  N
4635 . #          M15V_V_L          12-16V          _____V          G  N
4636 . #          M15V_I_L          0.01-0.3A       _____A          G  N
4637 . #          P5V_V_L          4.5-5.2V        _____V          G  N
4638 . #          P5V_I_L          0.1-1.0A        _____A          G  N
4639 . #
4640 . PCD.ON_OFF_ENA
4641 . #
4642 . # < CHECK >
4643 CHECK PCD.ON_OFF_ENA_DIS =ENA
4644 . #
4645 . PCD.MSASI_OFF
4646 . #
4647 . # < CHECK >
4648 CHECK PCD.MSASI_ON_OFF =OFF
4649 CHECK MD2F.MS6_LINK_STAT =OFF
4650 . #
4651 . # QL          Check-Item          Spec          Check
4652 . # -----+-----+-----+-----
4653 . # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
4654 . #
4655 . # -----
4656 . # 7.1.2. MDM 立ち下げ
4657 . # -----
4658 . #
4659 . MD2F.MDM_CLCT_STOP
4660 . #
4661 . # < CHECK >
4662 CHECK MD2F.MS7_DAT_CLCT =OFF
4663 . #
4664 . PCD.MDM_E_OFF
4665 . #
4666 . # < CHECK >
4667 CHECK PCD.MDM_E_ON_OFF =OFF
4668 CHECK MD2F.MS7_LINK_STAT =OFF
4669 . #
4670 . # QL          Check-Item          Spec          Check
4671 . # -----+-----+-----+-----
4672 . # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
4673 . #
4674 . # *****
4675 . # ===== ここから PWE 内機器の OFF =====
4676 . # *****

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4677  #
4678  . # -----
4679  . # 7.1.3. MGF 立ち下げ
4680  . # -----
4681  . #
4682  . DH.MGF_OFF                # DH.MC_EACH_EXE 0x00AC
4683  #:                          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
4684  #:                          0x00 MD1F.MGFO_CLCT_STOP
4685  #:                          0x01 MD2F.MGFI_CLCT_STOP
4686  #:                          0x0C MD1F.PME_MGFO_OFF
4687  #:                          0x0D PCD.ON_OFF_ENA
4688  #:                          0x0E PCD.PSU_MGF_I_OFF
4689  #:                          0x0F PCD.ON_OFF_DIS
4690  #
4691  . # < CHECK >
4692  CHECK MD1F.MS0_DAT_CLCT                =OFF
4693  CHECK MD2F.MS5_DAT_CLCT                =OFF
4694  CHECK MD1F.PME_MGFO_STS                =OFF
4695  CHECK PCD.PSU_MGF_I_ON_OFF              =OFF
4696  CHECK MD1F.MS0_LINK_STAT                =OFF
4697  CHECK MD2F.MS5_LINK_STAT                =OFF
4698  #
4699  . # QL          Check-Item          Spec          Check
4700  # -----+-----+-----+-----
4701  # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
4702  #
4703  . # -----
4704  . # 7.1.4. MAST 立ち下げ
4705  . # -----
4706  . #
4707  . # << MWE シャットダウン >>
4708  . #
4709  . MWE.MST_STBY_OFF
4710  . #
4711  . # QL          Check-Item          Spec          Check
4712  . # -----+-----+-----+-----
4713  . # MWE          CMND_REPLY_ADRS      0x0A12          G  N
4714  . # MWE          CMND_REPLY_CODE      0x0013          G  N
4715  . #
4716  . # QL          Check-Item          Spec          Check
4717  . # -----+-----+-----+-----
4718  . # MWE          MWE_STBY              ON              G  N
4719  . # MWE          MST_PSU               MAIN            G  N
4720  . # MWE          WPT_PSU               MAIN            G  N
4721  . # MWE          SAFE_MODE             OFF              G  N
4722  . # -----+-----+-----+-----
4723  . # MWE          MST_STBY              OFF              G  N
4724  . # MWE          WPT_STBY              OFF              G  N
4725  . #
4726  . # < MWE STBY OFF >
4727  . MWE.MWE_STBY_OFF
4728  . #

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4729 . # < CHECK >
4730 CHECK MWE. STAT_STBY =OFF
4731 #
4732 . # < Power off MWE >
4733 . #
4734 . MD2F. PME_MAST_OFF
4735 #
4736 . # < CHECK >
4737 CHECK MD2F. PME_MAST_STS =OFF
4738 CHECK MD2F. MS0_LINK_STAT =OFF
4739 #
4740 . # QL Check-Item Spec Check
4741 # -----+-----+-----+-----+
4742 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
4743 #
4744 . # -----+-----+-----+-----+
4745 . # 7.1.5. PWI 立ち下げ
4746 . # -----+-----+-----+-----+
4747 . #
4748 . DH. PWI1_OFF # DH. MC_EACH_EXE 0x008B
4749 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
4750 #: 0x00 MD2U. EWO_AM2P_INT_SET 0x0000 0x00
4751 #: 0x0D MD2F. PME_MEFISTO_OFF
4752 #: 0x0E EWO. SCP_OFF
4753 #: 0x0F EWO. WPTP_OFF
4754 #
4755 . #
4756 . # < CHECK >
4757 CHECK MD2F. MS4_LINK_STAT =OFF
4758 CHECK MD2F. PME_MEFISTO_STS =OFF
4759 CHECK EWO. SCP_ONOF =OFF
4760 CHECK EWO. WPTP_ONOF =OFF
4761 #
4762 . DH. PWI_OFF2_AM2P_SOR_EWO # DH. MC_EACH_EXE 0x00AD
4763 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
4764 #: 0x00 MD2F. PME_AM2P_OFF
4765 #: 0x01 MD2F. SORB_CLCT_STOP
4766 #: 0x02 MD2F. PME_SORBET_OFF
4767 #: 0x03 MD2U. BUF_CLCT 0x10 0x00
4768 #: 0x0F MD2F. PME_EWO_OFF
4769 #
4770 . # < CHECK >
4771 CHECK MD2F. MS1_DAT_CLCT =OFF
4772 CHECK MD2F. MS3_DAT_CLCT =OFF
4773 CHECK MD2F. PME_SORBET_STS =OFF
4774 CHECK MD2F. PME_AM2P_STS =OFF
4775 CHECK MD2F. PME_EWO_STS =OFF
4776 CHECK MD2F. MS1_LINK_STAT =OFF
4777 CHECK MD2F. MS2_LINK_STAT =OFF
4778 CHECK MD2F. MS3_LINK_STAT =OFF
4779 #
4780 . # QL Check-Item Spec Check

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4781  # -----+-----+-----+-----
4782  # CMD 出力装置  Message                STATUS CHECK 結果 OK      G  N
4783  #
4784  # -----
4785  # 7.1.6. PME 立ち下げ
4786  # -----
4787  #
4788  # PCD.ON_OFF_ENA
4789  #
4790  # < CHECK >
4791  CHECK PCD.ON_OFF_ENA_DIS                =ENA
4792  #
4793  # PCD.PSU_PME_OFF
4794  #
4795  # < CHECK >
4796  CHECK PCD.PSU_PME_ON_OFF                =OFF
4797  #
4798  # QL          Check-Item                Spec                Check
4799  # -----+-----+-----+-----
4800  # CMD 出力装置  Message                STATUS CHECK 結果 OK      G  N
4801  #
4802  # -----
4803  # 7.1.7. MIA 立ち下げ
4804  # -----
4805  #
4806  # MD1F.MIA_CLCT_STOP
4807  #
4808  # < CHECK >
4809  CHECK MD1F.MS4_DAT_CLCT                =OFF
4810  #
4811  # PCD.PSU_MIA_OFF
4812  #
4813  # < CHECK >
4814  CHECK PCD.PSU_MIA_ON_OFF                =OFF
4815  CHECK MD1F.MS4_LINK_STAT                =OFF
4816  #
4817  # QL          Check-Item                Spec                Check
4818  # -----+-----+-----+-----
4819  # CMD 出力装置  Message                STATUS CHECK 結果 OK      G  N
4820  #
4821  # -----
4822  # 7.1.8. MSA 立ち下げ
4823  # -----
4824  #
4825  # MD1F.MSA_CLCT_STOP
4826  #
4827  # < CHECK >
4828  CHECK MD1F.MS5_DAT_CLCT                =OFF
4829  #
4830  # PCD.PSU_MSA_OFF
4831  #
4832  # < CHECK >

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4833	CHECK MD1F.MS5_LINK_STAT	=OFF	
4834	CHECK PCD.PSU_MSA_ON_OFF	=OFF	
4835	#		
4836	# QL	Check-Item	Spec
4837	#		Check
4838	# CMD 出力装置	Message	STATUS CHECK 結果 OK
4839	#		G N
4840	#		
4841	#	7.1.9. MEA2 立ち下げ	
4842	#		
4843	#		
4844	#	N/A(確認手順で別途実施)	
4845	#		
4846	#		
4847	#	7.1.10. MEA1 立ち下げ	
4848	#		
4849	#		
4850	#	N/A(確認手順で別途実施)	
4851	#		
4852	#		
4853	#	7.1.11. ENA 立ち下げ	
4854	#		
4855	#		
4856	ENA.SHUTDOWN 0x00		
4857	WAIT_SEC 200		
4858	# QL	Check-Item	Spec
4859	#		Check
4860	# MPPE-ENA2	HV MAIN SET	00
4861	#	HV STARTMCP SET	00
4862	#	HV STOPMCP SET	00
4863	#	HV DEF SET	00
4864	#	HVPS ONOFF	OFF
4865	#	HVPS SHUTDN MODE	NML
4866	#		N/A
4867	ENA.CLEAR_FLG 0x0F		
4868	WAIT_SEC 40		
4869	# QL	Check-Item	Spec
4870	#		Check
4871	# MPPE-ENA2	HVPS SHUTDN MODE	NON
4872	#		N/A
4873	MD1U. ENA_S_HV_SAFTY_ENA		
4874	WAIT_SEC 40		
4875	# QL	Check-Item	Spec
4876	#		Judgment
4877	# MPPE-ENA2	HVPS SAFTY	ENA
4878	#		N/A
4879	#		
4880	#		
4881	# < cal pulse 生成停止 >		
4882	MD1U. ENA_S_CAL_MODE_SET 0x00 0x00 0x00 0x00 0x00 0x00		
4883	WAIT_SEC 40		
4884	# QL	Check-Item	Spec
			Check

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4885 # -----+-----+-----+-----
4886 # MPPE-ENA2  CAL FREQ                OFF                N/A
4887 #
4888 #
4889 . PCD.ON_OFF_ENA
4890   WAIT_SEC 2
4891   PCD.PSU_ENA_OFF
4892   WAIT_SEC 2
4893   PCD.ON_OFF_DIS
4894   WAIT_SEC 40
4895   CHECK PCD.PSU_ENA_ON_OFF              =OFF
4896   CHECK PCD.ON_OFF_ENA_DIS              =DIS
4897 #
4898 . # QL                Check-Item          Spec                Check
4899 # -----+-----+-----+-----
4900 # CMD 出力装置  Message          STATUS CHECK 結果 OK    G    N
4901 #
4902 #
4903 . # =====
4904 . # 7.2. MDP 立ち下げ
4905 . # =====
4906 #
4907 . # <<< MDP-DPU RST/OFF >>>
4908 . DH.MDP_DPU12_CPU_OFF    # DH.MC_EACH_EXE 0x0B1
4909 #:                        << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
4910 #:                        0x00 DH.ATMC_EACH_DIS 0x32
4911 #:                        0x03 DH.ATMC_EACH_DIS 0x33
4912 #:                        0x06 DH.ATMC_EACH_DIS 0x30
4913 #:                        0x09 DH.ATMC_EACH_DIS 0x31
4914 #:                        0x0A MD1F.CPU_RESET_ENA
4915 #:                        0x0B MD1F.CPU_RESET
4916 #:                        0x0C MD1F.CPU_OFF
4917 #:                        0x0D MD2F.CPU_RESET_ENA
4918 #:                        0x0E MD2F.CPU_RESET
4919 #:                        0x0F MD2F.CPU_OFF
4920 #
4921 . # < CHECK >
4922   CHECK DH.ATMC_ENA_DIS30              =DIS
4923   CHECK DH.ATMC_ENA_DIS31              =DIS
4924   CHECK DH.ATMC_ENA_DIS32              =DIS
4925   CHECK DH.ATMC_ENA_DIS33              =DIS
4926 #
4927 . # QL                Check-Item          Spec                Check
4928 # -----+-----+-----+-----
4929 # MD1F_OBS_HK    << DPU1 H/W-HK >>
4930 #                HW_STATE2                CPU_RST_ENA (short)
4931 #                HWCMD_CODE                0x0801    (short)
4932 #                HW_STATE2                STANDBY    (short)
4933 #                HWCMD_CODE                0x0702    (short)
4934 #
4935 . # < CHECK >
4936   CHECK MD1F.DPU_HW_STATE2              =CPUOFF_DPUBUS

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4937 CHECK MD1F.DPU_CMD_CODE 0x0202
4938 #
4939 . # QL Check-Item Spec Check
4940 # -----+-----+-----+-----
4941 # MD2F_OBS_HK << DPU2 H/W-HK >>
4942 # HW_STATE2 CPU_RST_ENA (short)
4943 # HWCMD_CODE 0x0801 (short)
4944 # HW_STATE2 STANDBY (short)
4945 # HWCMD_CODE 0x0702 (short)
4946 #
4947 . # < CHECK >
4948 CHECK MD2F.DPU_HW_STATE2 =CPUOFF_DPUBUS
4949 CHECK MD2F.DPU_CMD_CODE 0x0202
4950 #
4951 . # Telemetry Operation Mode 設定
4952 . DH. OPE_MODE_32KBPS_CAT0x23 0x00 0x000000000000000000000000
4953 #
4954 . # <<< MDP-DPU1/2 Power OFF >>>
4955 . DH. MDP_DPU12_OFF # DH. MC_EACH_EXE 0x0B0
4956 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
4957 #: 0x00 DH. ATMC_EACH_DIS 0x34
4958 #: 0x01 DH. SPW_RTEFDIR_SET 0x18
4959 #: 0x03 DH. ATMC_EACH_DIS 0x36
4960 #: 0x06 DH. ATMC_EACH_DIS 0x32
4961 #: 0x09 DH. ATMC_EACH_DIS 0x33
4962 #: 0x0A PCD. ON_OFF_ENA
4963 #: 0x0B PCD. PSU_MDP1_OFF
4964 #: 0x0C DH. ATMC_EACH_DIS 0x30
4965 #: 0x0D PCD. PSU_MDP2_OFF
4966 #: 0x0E PCD. ON_OFF_DIS
4967 #: 0x0F DH. ATMC_EACH_DIS 0x31
4968 #
4969 . # < CHECK >
4970 CHECK DH. ATMC_ENA_DIS30 =DIS
4971 CHECK DH. ATMC_ENA_DIS31 =DIS
4972 CHECK DH. ATMC_ENA_DIS32 =DIS
4973 CHECK DH. ATMC_ENA_DIS33 =DIS
4974 CHECK DH. ATMC_ENA_DIS34 =DIS
4975 CHECK DH. ATMC_ENA_DIS36 =DIS
4976 CHECK DH. SPW_FDIR_ENA_MDP1 =DIS
4977 CHECK DH. SPW_FDIR_ENA_MDP2 =DIS
4978 CHECK DH. SPW_FDIR_ENA_PCD =ENA
4979 CHECK DH. SPW_FDIR_ENA_RMTRTR =ENA
4980 CHECK PCD. PSU_MDP1_ON_OFF =OFF
4981 CHECK PCD. PSU_MDP2_ON_OFF =OFF
4982 CHECK PCD. ON_OFF_ENA_DIS =DIS
4983 . #
4984 . # QL Check-Item Spec Check
4985 # -----+-----+-----+-----
4986 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
4987 #
4988 # =====

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4989 . # 7.3. 衛星立ち下げ
4990 # =====
4991 # -----
4992 . # 7.3.1. MC_DIS
4993 # -----
4994 . #
4995 . DH. ATMC_DIS
4996 #
4997 . # < CHECK >
4998 CHECK DH. ATMC_ENA_DIS =DIS
4999 . #
5000 . DH. RQMC_DIS
5001 #
5002 . # < CHECK >
5003 CHECK DH. RQMC_ENA_DIS =DIS
5004 #
5005 . DH. SYSTMTR_DIS
5006 #
5007 . # < CHECK >
5008 CHECK DH. SYSTMTR_ENA_DIS =DIS
5009 #
5010 . DH. ALL_MC_STOP
5011 . #
5012 . # < CHECK >
5013 CHECK DH. MC_ENA_DIS =DIS
5014 #
5015 . # QL          Check-Item          Spec          Check
5016 # -----+-----+-----+-----+
5017 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
5018 #
5019 # -----
5020 . # 7.3.2. 衛星立ち下げ
5021 # -----
5022 #
5023 . # QL          Check-Item          Spec          Check
5024 # -----+-----+-----+-----+
5025 # PCS1          PCD. CHG_CC_LO          ON          G  N
5026 #              PCD. BAT_CV_ON_OFF        OFF          G  N
5027 #              PCD. BAT_OT_ON_OFF        OFF          G  N
5028 #              PCD. BAT_OV_ON_OFF        OFF          G  N
5029 #              PCD. TMR_CNT              0          G  N
5030 #              DMC-PSU-A, -B, XTRP-A, B, SV-Heater 以外が OFF であること          G  N
5031 # TCIU          DH. XTRP_A_TX_ON_OFF      OFF          G  N
5032 #              DH. XTRP_B_TX_ON_OFF      OFF          G  N
5033 # 局管制        Uplink                  OFF          G  N
5034 #
5035 #
5036 . # 【注意】
5037 # CV_ON_OFF が ON になっている時は、必ず CV_OFF にすること。
5038 # CC_LO ON で無い場合は、CCCV LO モードへ移行すること。
5039 . #
5040 . PCD. CHG_LR1_ON

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5041  #
5042  . # < CHECK >
5043  CHECK PCD. CHG_LR1_ON_OFF          =ON
5044  CHECK PCD. CHG_I                    -0.1 0.1
5045  CHECK PCD. DCHG_I                   -0.2 0.2
5046  #
5047  . #
5048  . PCD. BUS_SW_ENA
5049  #
5050  . # < CHECK >
5051  CHECK PCD. BUS_SW_ENA_DIS            =ENA
5052  #
5053  . #
5054  . PCD. BUS_SW_OFF
5055  #
5056  . # < CHECK >
5057  CHECK PCD. BUS_SW_ENA_DIS            =DIS
5058  CHECK PCD. BUS_SW_ON_OFF            =OFF
5059  #
5060  . #
5061  . PCD. CHG_ENA
5062  . PCD. CHG_CC_L2
5063  #
5064  . # < CHECK >
5065  CHECK PCD. CHG_ENA_DIS                =ENA
5066  CHECK PCD. CHG_CC_L1_2                =L2
5067  CHECK PCD. CHG_CC_L0                  =OFF
5068  #
5069  . #
5070  . PCD. CHG_DIS
5071  #
5072  . # < CHECK >
5073  CHECK PCD. CHG_ENA_DIS                =DIS
5074  CHECK PCD. CHG_I                      0.1 0.3
5075  CHECK PCD. DCHG_I                    -0.2 0.2
5076  #
5077  . #
5078  . PCD. CHG_LR_OFF
5079  #
5080  . # < CHECK >
5081  CHECK PCD. CHG_LR1_ON_OFF            =OFF
5082  CHECK PCD. CHG_LR2_ON_OFF            =OFF
5083  CHECK PCD. CHG_I                      -0.1 0.1
5084  CHECK PCD. DCHG_I                    -0.2 0.2
5085  #
5086  . # 【確認】
5087  # BUS-V > BAT-V であることを確認する。
5088  . #
5089  . PCD. INT_EXT_ENA
5090  . PCD. BAT_EXT
5091  #
5092  . # < CHECK >

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Check
G N

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5093	CHECK PCD. INT_EXT_ENA_DIS	=DIS	
5094	CHECK PCD. BAT_INT_EXT	=EXT	
5095	. #		
5096	. PCD. CHG_BYPS_DIS		
5097	. #		
5098	. # < CHECK >		
5099	CHECK PCD. CHG_BYPS_ENA_DIS	255	
5100	CHECK PCD. SA_V	-	
5101	CHECK PCD. SA_I	-	
5102	CHECK PCD. APR1_I	-	
5103	CHECK PCD. APR2_I	-	
5104	CHECK PCD. BUS_V	-	
5105	CHECK PCD. LOAD_I	-	
5106	CHECK PCD. CHG_I	-	
5107	CHECK PCD. DCHG_I	-	
5108	CHECK PCD. BAT_V	-	
5109	CHECK PCD. CEL1_V	-	
5110	CHECK PCD. CEL2_V	-	
5111	CHECK PCD. CEL3_V	-	
5112	CHECK PCD. CEL4_V	-	
5113	CHECK PCD. CEL5_V	-	
5114	CHECK PCD. CEL6_V	-	
5115	CHECK PCD. CEL7_V	-	
5116	CHECK PCD. CEL8_V	-	
5117	CHECK PCD. CEL9_V	-	
5118	CHECK PCD. CEL10_V	-	
5119	CHECK PCD. CEL11_V	-	
5120	CHECK PCD. CEL12_V	-	
5121	CHECK PCD. BAT_CV	-	
5122	CHECK PCD. BAT_CV_ON_OFF	-	
5123	. #		
5124	. #		
5125	. PCD. CEL_V_MONI_OFF	# 【BAT 管理手順書 (TPP-7S8238-03K) へ記録】	
5126	. #		
5127	. # < CHECK >		
5128	CHECK PCD. CEL_V_MONI_ON_OFF	=OFF	
5129	CHECK PCD. BAT_CV	-	
5130	CHECK PCD. BAT_CV_ON_OFF	-	
5131	. #		
5132	. # 【操作】		
5133	. # RD LINK OFF		
5134	. # EXT-PS OFF		
5135	. # BAT_MEASURE STOP (BAT 温度計測停止)		
5136	. #		
5137	. # QL	Check-Item	Spec
5138	. #		Check
5139	. # CMD 出力装置	Message	STATUS CHECK 結果 OK G N
5140	. #		
5141	. #		
5142	. # 7.4. CNV-A/B SV-HTR 選択		
5143	. #		
5144	. #		

5145 . # 【操作】
5146 # 下記手順に従い、MIS にて “HPC_Command2 (MIS 出力 ch38)” 及び “HPC_Command3
5147 # (MIS 出力 ch40)” を出力し、CNV-A/B の MODE を “HTR” へ切り替える。
5148 #
5149 #
5150 # (1) SYSTEM Control 画面の DFE 画面を開く。
5151 # (2) IO Activation Control の IO Number に ch38 を入力し、Activate ボタンを押下する
5152 # ディスクリットコマンドのパルスが出力される。
5153 # (3) IO Activation Control の IO Number に ch40 を入力し、Activate ボタンを押下する
5154 # ディスクリットコマンドのパルスが出力される。
5155 # (4) 以下の流れでコマンド送信後のステータスを確認する。
5156 # (a) Main Menu: View -> TM/TC Data Monitor
5157 # (b) TM/TC Data Monitor 画面の、FE Data Filter で、“DFE TM Packet” をクリック
5158 # チェックマークがつく
5159 # (c) SYSTEM Control 画面の DFE をクリック
5160 # (d) Track Latest Entry → Datafield Decode → Acquisition Data で下記を確認
5161 #

MIS	Check-Item	Spec	Check
Relay	CNV-A MAIN (RLY6)	OPEN : 32768 (dec) 以上	G N
status	CNV-A HTR (RLY7)	SHORT : 32767 (dec) 以下	G N
	CNV-B MAIN (RLY13)	OPEN : 32768 (dec) 以上	G N
	CNV-B HTR (RLY14)	SHORT : 32767 (dec) 以下	G N

5168 #
5169 # =====
5170 . # 7.5. MIS 立ち下げ
5171 # =====
5172 . #
5173 . # 【MIS (MPO Interface Simulator)】
5174 #
5175 . # 【操作】
5176 # 下記手順により MIS 温度モニタを終了する。
5177 # (1) SYSTEM Control 画面の DFE をクリック
5178 # (2) Acquisition Control の STOP ボタンをクリック
5179 #
5180 . # 【操作】
5181 # 下記手順により MIS を立ち下げる。
5182 # (1) System Control → MFE → Bus profile
5183 # → Polling Sequence Table Control → stop
5184 # (2) System Control → PFE → Power Control Bus A → ON をクリック → OFF
5185 # (3) External Bus Power Supply の、DC VOLTS 表示=OFF を確認する
5186 # (4) PFE Platform の Under Voltage の LED=点灯を確認する
5187 # (5) Main Menu: Mode -> System Off-Line Mode を選択する
5188 # (6) Confirm → YES
5189 # (7) Main Menu: File -> Exit を選択する
5190 # (8) Exit System 画面において、以下を選択し、OK ボタンをクリック
5191 # Power FE : S/W Shutdown
5192 # MIL-1553B FE : Shutdown
5193 # Discrete FE : Shutdown
5194 # CMDVS Core Environment : S/W Shutdown
5195 # (9) MIS 本体の電源を以下の順に OFF する
5196 # 1. MIL-1553/Discrete Front-End

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表 4-1 ソフトウェアバージョン管理表

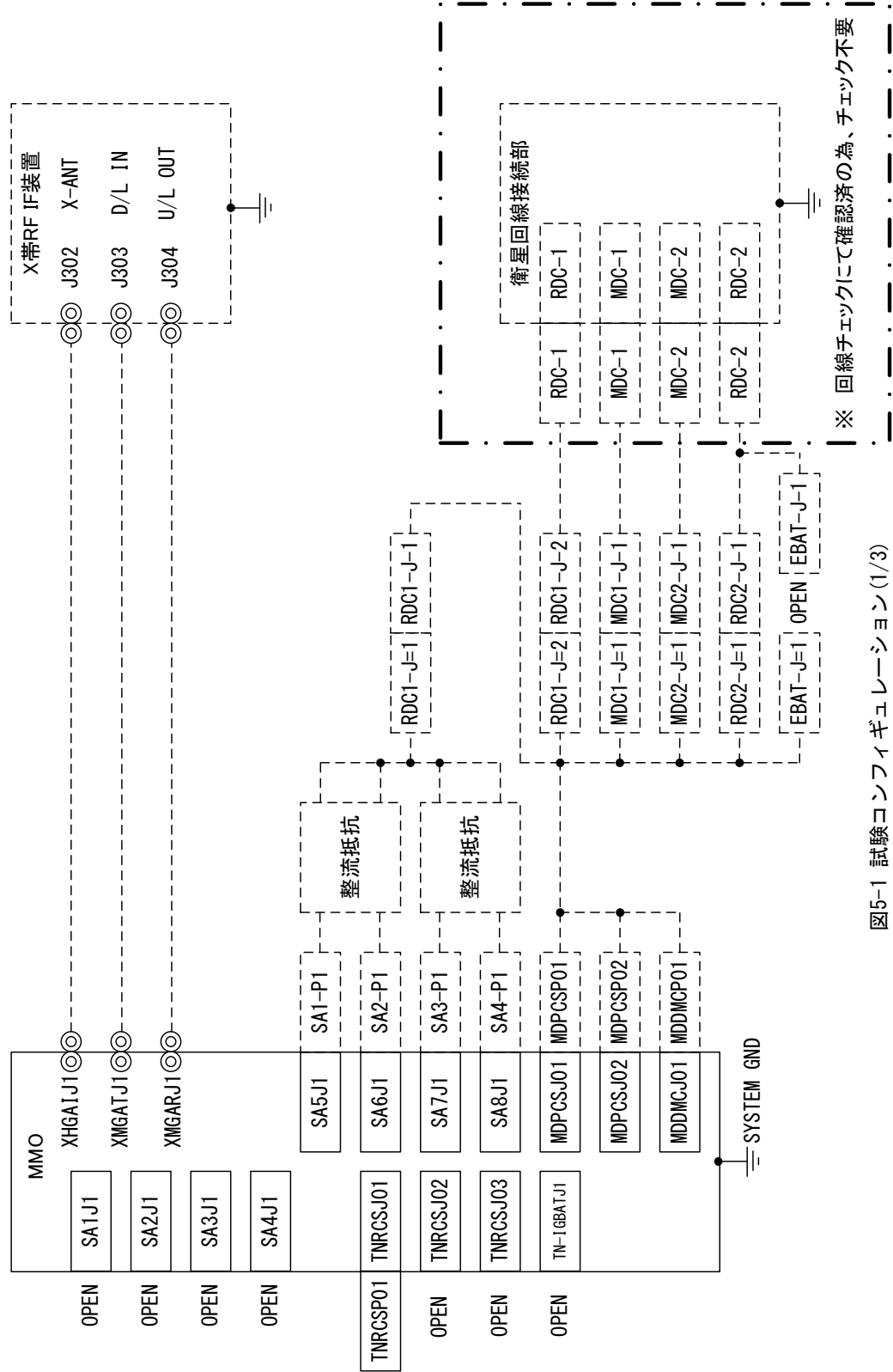
Component	Software(オブジェクト)/Table	Version	Program (ソース)	Revision
DMC	IPLソフトウェア	1.00.00	IPL	77
	DMCソフトウェア	1.01.23	PLTS	1850
			DHFS	1615
			ACFS	1553
			TCFS	1443
			T-Kernel	293
			T-Monitor	110
	MC-S_EEPROM	5.44	-	-
	MC-L_EEPROM	5.44	-	-
	ST_EEPROM	5.36	-	-
	AT_EEPROM	5.44	-	-
	RQ_EEPROM	5.13	-	-
	CATEGORY_TABLE_EEPROM	3.8	-	-
	PARTITION_TABLE_EEPROM	3.9	-	-
MDP (DPU1)	TM_DATA_BLOCK_TABLE_EEPROM	3.9	-	-
	TCS_TBL_ROM	2.2→2.3	-	-
MDP (DPU2)	Middleware	Ver. 7.4→Ver. 7.5	-	-
	Application software	Ver. 7.4→Ver. 7.5	-	-
MSA	Middleware	Ver. 7.4→Ver. 7.5	-	-
	Application software	Ver. 7.4→Ver. 7.5	-	-
MIA	-	2017/11/16(Ver. 40)	-	-
MEA1	-	2015/2/13	-	-
MEA2	-	2017/1/26	-	-

*供試体がない場合はN/Aとすること。

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5 コンフィギュレーション

コネクタの接続については、実施者と非実施者によるダブルチェックを実施し記録を残すこと



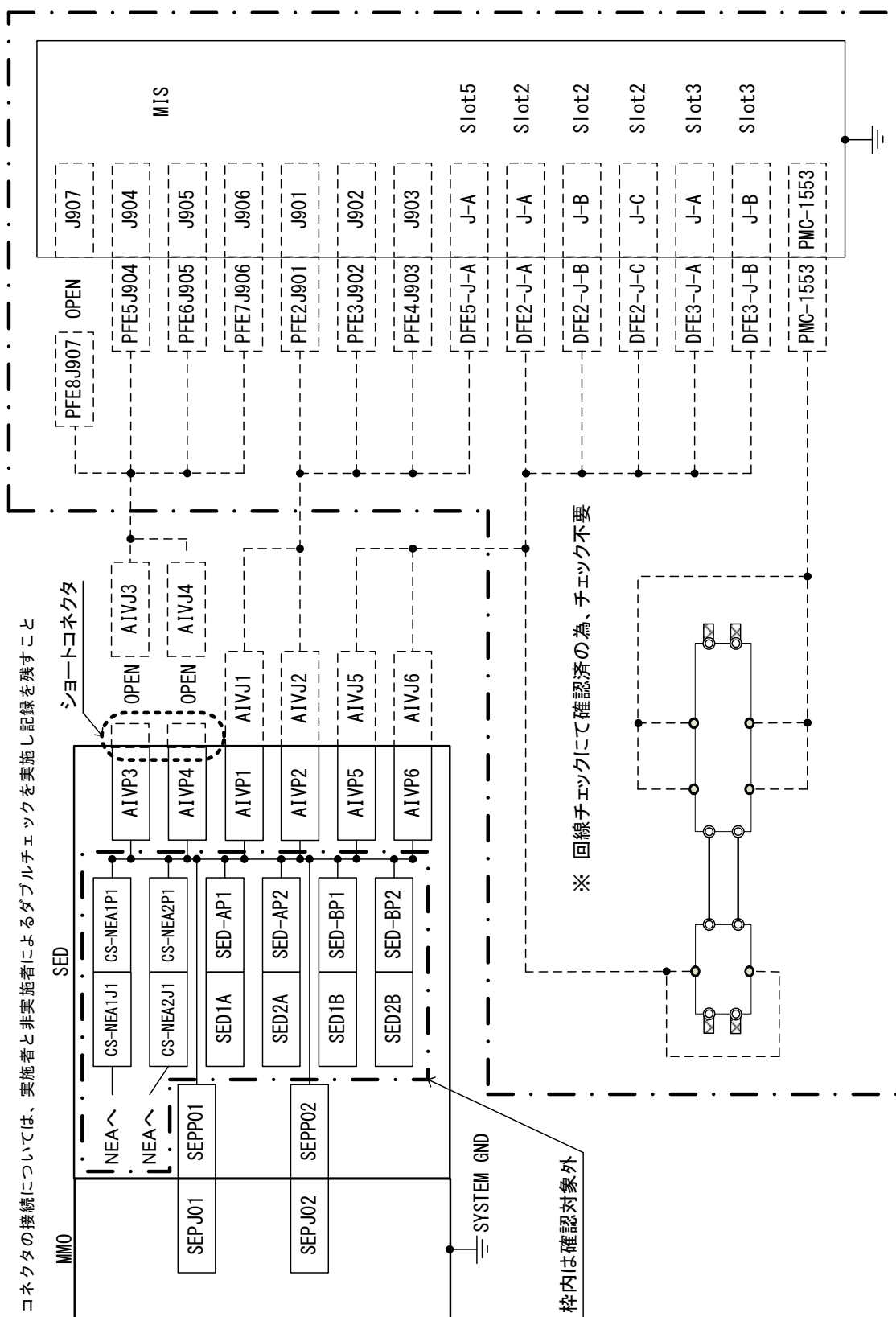


図5-1 試験コンフィギュレーション(2/3)

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コネクタの接続については、実施者と非実施者によるダブルチェックを実施し記録を残すこと

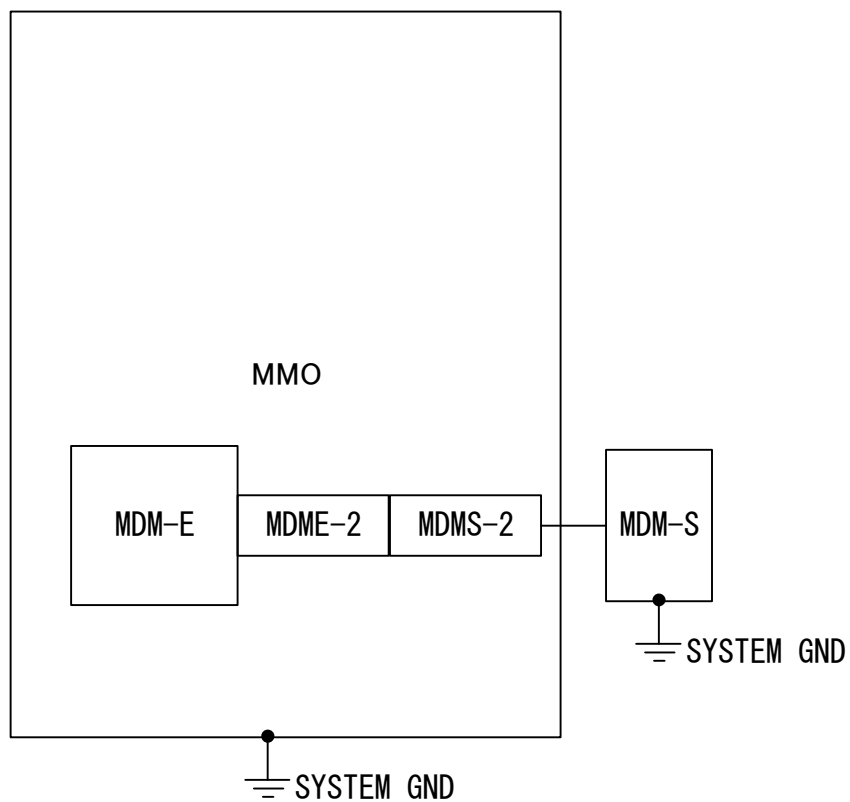


図5-1 試験コンフィギュレーション (3/3)

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表 5-1 コネクタ嵌合一覧

IFコネクタ名称	嵌合○/ 非嵌合×	用途	備考
TNRCSJ01	○	圧力センサモニタ HLV OPEN/CLOSEモニタ タンク温度モニタ	RCS圧力センサ確認のため嵌合
TNRCSJ02	×	HLVドライブ	誤動作リスク低減のためOPEN
TNRCSJ03	×	THVドライブ	誤動作リスク低減のためOPEN
TN-IGBATJ1	×	NEAドライブ(HGA/MGA)	NEA誤動作リスクを低減するためOPEN
SA1J1	×	SAP1電源入力	使用しないI/FのためOPEN
SA2J1	×	SAP2電源入力	使用しないI/FのためOPEN
SA3J1	×	SAP3電源入力	使用しないI/FのためOPEN
SA4J1	×	SAP4電源入力	使用しないI/FのためOPEN
SA5J1	○	SAP5電源入力	EXT-PS入力のため地上試験装置と接続
SA6J1	○	SAP6電源入力	EXT-PS入力のため地上試験装置と接続
SA7J1	○	SAP7電源入力	EXT-PS入力のため地上試験装置と接続
SA8J1	○	SAP8電源入力	EXT-PS入力のため地上試験装置と接続
XHGAJ1	○	HGA TX/RX	試験装置で終端のため接続
XMGATJ1	○	MGA-TX TX	試験装置で終端のため接続
XMGARJ1	○	MGA RX	試験装置で終端のため接続
MDPCSJ01	○	MD(電流/電圧値)	モニタ用のため地上試験装置と接続
MDPCSJ02	○	MD(電流/AGC値)	モニタ用のため地上試験装置と接続
MDDMCJ01	○	RD(CMD通信)	RDによる通信のため地上試験装置と接続
AIVP1	○	電源(28V/SEDヒーター)(主系)(CNV-A) CNV PWR SWDドライブ(主系)(CNV-A)	MIS電源使用のため嵌合
AIVP2	○	電源(28V/SEDヒーター)(主系)(CNV-B) CNV PWR SWDドライブ(主系)(CNV-B)	MIS電源使用のため嵌合
AIVP3	○	NEA/Pyroドライブ(CSM/SED)(主系)	安全のためショートコネクタを嵌合
AIVP4	○	NEA/Pyroドライブ(CSM/SED)(従系)	安全のためショートコネクタを嵌合
AIVP5	○	分離ステータス/温度モニタ(主系) 1553B通信(主系)	1553B I/F使用のため嵌合
AIVP6	○	分離ステータス/温度モニタ(従系) 1553B通信(従系)	1553B I/F使用のため嵌合

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6 データシート
N/A

表 7-1 不具合記録表

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