

文 書 番 号	シート No.	版 数
	14	1

3.7 試験/作業項目 (試験フロー)

MCS SVT-1c MMO試験は、以下の手順で実施する。

なお手順中、コマンド送信は全てESA側に実施を依頼し、作業完了の確認を以て次の手順に進むこと。

0. SETUP
 - 0.1. 試験系接続
 - 0.2. 試験系立ち上げ(I/F 変換装置/CCS)
 - 0.3. 衛星管制設定
1. MMO switch-ON and prepare MMO for 1553B monitoring
 - 1.1. MMO switch-ON (heater switch-on)
 - 1.2. MMO Switch to Cruise Initial mode on CNV-A – manually
 - 1.3. MMO TM mode configuration for 1553B monitoring
2. Preparation for MMO Test
 - 2.1. Prepare SSMM for test, i.e. change TM mode to NECP one (Cruise high system TM mode)
 - 2.2. Start OBCP: MMO Operational Heater Control Start with JZXK4242
 - 2.3. Set MMO HK rate to 1sec
3. Separation commands
 - 3.1. Set MMO ready for Separation
 - 3.2. Send MMO commands for separation
4. MMO System switch-on
 - 4.1. Switch on of bus systems
 - 4.1.1. MMO BAT_CEL_MONI_ON
 - 4.1.2. MMO Index Pulse Output
 - 4.2. Switch on MDP (SHT 28-37)
5. NECP Scenario
 - 5.1. PWI check (SHT 38-51)
 - 5.2. MWE check (SHT 52-56)
 - 5.3. MGF check (SHT 57-65)
 - 5.4. MPPE(1) check (SHT 66-82)
 - 5.5. MSASI check (SHT 83-94)
 - 5.6. MPPE(2) check (SHT 95-112)
 - 5.7. MDM check (SHT 113-116)
 - 5.8. MTL commanding of MMO (Heater health check) (SHT 117-121)
 - 5.9. Switch MMO dump route VC2 -> VC0 -> VC2
6. Prepare MMO for 1553B monitoring
 - 6.1. Switch OFF MDP (SHT 122-123)
 - 6.2. MMO Operational Heater shutdown
 - 6.3. BAT MONI_OFF
 - 6.4. MC STOP
 - 6.5. MMO TM mode configuration for 1553B monitoring
7. Suspending dumps of MMO data to simulate a ground station non visibility during NECP
 - 7.1. Stop dumps of MMO data to VC2
 - 7.2. Start dumps of MMO data to VC2
8. Switch off MMO
 - 8.1. Start OBCP: MMO Switch to Cruise Dormant Mode with JZXK4241
 - 8.2. MMO switch-OFF
 - 8.3. Stop dumps of MMO data to VC2

[illegible]

```

0050 # 1.1 Check MMO Temperatures
0051 # 1.2 Check MMO in dormant mode
0052 # 2. SWITCH on CNV-A or CNV-B? --> A
0053 # 3.1 Switch off both MMO converters
0054 # 3.2 Switch CNV relays to MAIN
0055 # 3.3 Switch on CNV-A
0056 # 3.4 Enable CNV-A trip-off monitoring
0057 # 3.5 Check MMO DMC status
0058 #
0059 #
0060 # < CHECK >
0061 CHECK DH.MC_ENA_DIS =ENA # DH1
0062 CHECK DH.MC_EXE_NO 0x001A # DH1
0063 CHECK DH.CATEGORY_TBL_NO 0x00 # DH2
0064 CHECK DH.TLM1553BFMTNO 0x00 # DH3
0065 #
0066 # System HK が 1 秒周期で出力されていること G N
0067 #
0068 # =====
0069 # 1.3. MMO TM mode configuration for 1553B monitoring
0070 # =====
0071 #
0072 DH.DUMMY
0073 #
0074 # QL Check-Item Spec Check
0075 # -----+-----+-----+-----
0076 # DH1 DH.CMD_EXE_CNT +1 増加 G N
0077 # DH1 DH.CMD_DLV_CNT +1 増加 G N
0078 #
0079 #
0080 DH.MC_EACH_EXE 0x002C # 1553B TLM mode 0 (SYS HK 1)
0081 #
0082 #: << MACRO >>
0083 #: 0x00 DH.DMP_PERIOD 0x04
0084 #: 0x04 ACS.ACS_PKT_FMT_1
0085 #: 0x05 DH.OPE_MODE 0x0C 0x0A 0x00 0x00 0x00000000000000000000000000000000
0086 #: 0x06 DH.TLMFMT1553B_CHG 0x00
0087 #
0088 # < CHECK >
0089 CHECK DH.DMP_PERIOD =SEC_16
0090 CHECK DH.CATEGORY_TBL_NO 0x00
0091 CHECK DH.DWNLNK_RATE =BPS16384
0092 CHECK DH.TLM1553BFMTNO 0x00
0093 #
0094 #
0095 #
0096 # *****
0097 # 2. Preparation for MMO Test
0098 # *****
0099 # =====
0100 # 2.1. Prepare SSMM for test, i.e. change TM mode to NECP one (Cruise high system TM mode)
0101 # =====

```

```

0102 #
0103 # (MPO Procedure)
0104 #
0105 #
0106 # =====
0107 . # 2.2. Start OBCP: MMO Operational Heater Control Start with ZJXK4242
0108 # =====
0109 #
0110 # (MPO Procedure JX-FCP-003)
0111 # 1.1 Check MMO in initial mode
0112 # 2 Start MMO Operational Heater Control
0113 #
0114 #
0115 . # < CHECK >
0116 CHECK PCD. DRU_PSU_A_ON_OFF =ON # PCS1
0117 CHECK TCS. MODE_ST =NMNL # TCS_MONITOR
0118 CHECK DH. MC_EXE_NO 0x001B # DH1
0119 CHECK DH. CATEGORY_TBL_NO 0x11 # DH2
0120 CHECK DH. TLM1553BFMTNO 0x00 # DH3
0121 #
0122 . # System HK が 64 秒周期で出力されていること G N
0123 #
0124 #
0125 # =====
0126 . # 2.3. Set MMO HK rate to 1sec
0127 # =====
0128 #
0129 # (MPO Procedure JX-SVT-003)
0130 #
0131 #
0132 . # < CHECK >
0133 CHECK DH. MC_EXE_NO 0x001A # DH1
0134 #
0135 . # System HK が 1 秒周期で出力されていること G N
0136 #
0137 #
0138 # *****
0139 . # 3. Separation commands
0140 # *****
0141 # =====
0142 . # 3.1. Set MMO ready for Separation
0143 # =====
0144 #
0145 . DH. MC_START
0146 . DH. ATMC_ENA
0147 . DH. SYSTMTR_ENA
0148 #
0149 . # QL Check-Item Spec Check
0150 # -----+-----+-----+-----
0151 # DH1 DH. CMD_EXE_CNT +3 増加 G N
0152 # DH1 DH. CMD_DLV_CNT +3 増加 G N
0153 #

```

```

0154 #
0155 . # < CHECK >
0156 CHECK DH.MC_ENA_DIS =ENA # DH1
0157 CHECK DH.ATMC_ENA_DIS =ENA # DH1
0158 CHECK DH.SYSTMR_ENA_DIS =ENA # DH1
0159 #
0160 #
0161 # =====
0162 . # 3.2. Send MMO commands for separation
0163 # =====
0164 #
0165 # (MPO Procedure JX-SVT-004)
0166 # Check that the MMO is in standby mode
0167 # MMO Separation Status Check request
0168 #
0169 #
0170 . # < CHECK >
0171 CHECK DH.ATMC_EXE_NO 0x07 # DH1
0172 CHECK DH.SEPSTS_ENADIS =ENA # DH1
0173 #
0174 . # QL Check-Item Spec Check
0175 # -----+-----+-----+-----
0176 # DH1 DH.MC_EXE_NO 0x000C or 0x0100 G N
0177 #
0178 #
0179 # (MPO Procedure JX-SVT-004)
0180 # MMO Post Separation Sequence disable
0181 #
0182 #
0183 #
0184 . # QL Check-Item Spec Check
0185 # -----+-----+-----+-----
0186 # DH1 DH.CMD_DLV_CNT +17count up G N
0187 #
0188 #
0189 . # < CHECK >
0190 CHECK DH.MC_EXE_NO 0x000F # DH1
0191 CHECK DH.SEPSTS_ENADIS =DIS # DH1
0192 #
0193 #
0194 # *****
0195 . # 4. MMO System switch-on
0196 # *****
0197 # =====
0198 . # 4.1. Switch on of bus systems
0199 # =====
0200 #
0201 . # <CAT 0x28>
0202 . DH.OPE_MODE 0x0D 0x00 0x28 0x02 0x01 0x01 0x01 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x00
0x00 0x00
0203 #
0204 . # < CHECK >

```

文 書 番 号	シート No.	版 数
	19	1

0205	CHECK DH. CATEGORY_TBL_NO	0x28		
0206	#			
0207	# QL	Check-Item	Spec	Check
0208	#	-----+-----+-----+-----		
0209	# DH1	DH. CMD_EXE_CNT	+1 増加	G N
0210	# DH1	DH. CMD_DLX_CNT	+1 増加	G N
0211	#			
0212	. DH. TLMFMT1553B_CHG	0x06		
0213	#			
0214	. # < CHECK >			
0215	CHECK DH. TLM1553BFMTNO	0x06		
0216	#			
0217	# QL	Check-Item	Spec	Check
0218	#	-----+-----+-----+-----		
0219	# DH1	DH. CMD_EXE_CNT	+1 増加	G N
0220	# DH1	DH. CMD_DLX_CNT	+1 増加	G N
0221	#			
0222	#			
0223	. DH. MC_START			
0224	. DH. ATMC_ENA			
0225	. DH. SYSTMTR_ENA			
0226	#			
0227	. # < CHECK >			
0228	CHECK DH. MC_ENA_DIS	=ENA		
0229	CHECK DH. ATMC_ENA_DIS	=ENA		
0230	CHECK DH. SYSTMTR_ENA_DIS	=ENA		
0231	#			
0232	. # QL	Check-Item	Spec	Check
0233	#	-----+-----+-----+-----		
0234	# DH1	DH. CMD_EXE_CNT	+3 増加	G N
0235	# DH1	DH. CMD_DLX_CNT	+3 増加	G N
0236	#			
0237	. DH. ATMC_EACH_DIS	0x09		
0238	. DH. TBL_ROMUPD_DIS			
0239	#			
0240	. # < CHECK >			
0241	CHECK DH. ATMC_ENA_DIS09	=DIS	# DH5	
0242	CHECK DH. TBL_ROMUPD_ED	=ENA	# DH7	
0243	#			
0244	#			
0245	#	-----		
0246	. # 4.1.1. MMO BAT_CEL_MONI_ON			
0247	#	-----		
0248	#			
0249	. PCD. CEL_V_MONI_ON	# 【ESTECBAT 管理手順書 (TPP-7S8238-03K) へ記録】		
0250	#			
0251	. # < CHECK >			
0252	CHECK PCD. CEL1_V	-		
0253	CHECK PCD. CEL2_V	-		
0254	CHECK PCD. CEL3_V	-		
0255	CHECK PCD. CEL4_V	-		
0256	CHECK PCD. CEL5_V	-		

文 書 番 号	シ-ト No.	版 数
	20	1

0257	CHECK PCD. CEL6_V	-
0258	CHECK PCD. CEL7_V	-
0259	CHECK PCD. CEL8_V	-
0260	CHECK PCD. CEL9_V	-
0261	CHECK PCD. CEL10_V	-
0262	CHECK PCD. CEL11_V	-
0263	CHECK PCD. CEL12_V	-
0264	CHECK PCD. BAT_INT_EXT	=EXT
0265	CHECK PCD. BUS_SW_ON_OFF	=OFF
0266	CHECK PCD. BAT_OT_ENA_DIS	=DIS
0267	CHECK PCD. CHG_I	-
0268	CHECK DH. BAT_TEMP1	-
0269	CHECK DH. BAT_TEMP2	-
0270	#	
0271	#	
0272	#	
0273	# -----	
0274	. # 4.1.2. MMO Index Pulse Output	
0275	# -----	
0276	#	
0277	. ACS. ACS_PKT_FMT_1	
0278	#	
0279	. # < CHECK >	
0280	CHECK ACS. F_ACS_PKT_FMT	=ACSHK1
0281	CHECK ACS_SYS. I_IPCHK	=RST
0282	CHECK ACS_SYS. I_IP_GNRT	=SELF
0283	CHECK ACS. STS_IP_SRC	=NULL
0284	CHECK ACS. STS_IP_OUT	=DIS
0285	CHECK ACS. STS_IP_GNRT	=DIS
0286	CHECK ACS. I_IPCHK	=RST
0287	CHECK ACS. I_IP_GNRT	=SELF
0288	CHECK ACS. BIT_I_IPCHK	=RST
0289	#	
0290	#	
0291	. ACS. SSAS_SEL_IMT	
0292	ACS. DUM_SP_ON 2047 4095 128 16 8475	
0293	#	
0294	. # < CHECK >	
0295	CHECK ACS_SYS. SSAS_ST	=IMT
0296	CHECK ACS_SYS. P_SPIN_SSAS_SEL	4.137 4.139
0297	CHECK ACS_SYS. THT_CRS_SEL	0x10 0x10
0298	CHECK ACS_SYS. SUN_ANG_SEL	-2 2
0299	CHECK ACS. SSAS_ST_PT	=IMT
0300	CHECK ACS. SSAS_ST	=IMT
0301	CHECK ACS. F_SPN_ERR_SEL	=OFF
0302	CHECK ACS. P_SPIN_SSAS_SEL	4.137 4.139
0303	CHECK ACS. THT_CRS_SEL	0x10 0x10
0304	CHECK ACS. SUN_ANG_COS	4.999 5.001
0305	CHECK ACS. SUN_ANG_SIN	2.499 2.501
0306	CHECK ACS. SUN_ANG_SEL	-2 2
0307	CHECK ACS. BIT_SSAS_ST	=IMT
0308	#	

文 書 番 号	シート No.	版 数
	21	1

```

0309  #
0310  . ACS. ACS_PKT_FMT_4
0311  #
0312  . # < CHECK >
0313  CHECK ACS. F_ACS_PKT_FMT           =ACSHK4
0314  CHECK ACS_SYS. F_ECL               =OFF
0315  CHECK ACS_SYS. I_ATT                =STBY
0316  CHECK ACS_SYS. P_B_AVE             4. 137 4. 139
0317  CHECK ACS. SSAS_IMT_SP_STS         =ON
0318  CHECK ACS. F_ECL                   =OFF
0319  CHECK ACS. I_ATT                    =STBY
0320  CHECK ACS. P_B_AVE                 4. 137 4. 139
0321  CHECK ACS. E_SSAS_SPIN              0. 0999 0. 1001
0322  CHECK ACS. N_SPINRATE_AVE          5 5
0323  #
0324  #
0325  . ACS. IP_K_DT 0x3C9BA5E3 0x3C9BA5E3
0326  #
0327  . # < CHECK >
0328  CHECK ACS. K_DT0                   0. 018999 0. 019001
0329  CHECK ACS. K_DT1                   0. 018999 0. 019001
0330  #
0331  #
0332  . ACS. IP_SRC_IP
0333  # WAIT_SEC 5
0334  ACS. IP_GNRT_ON
0335  # WAIT_SEC 5
0336  ACS. IP_OUT_ENA
0337  # WAIT_SEC 5
0338  ACS. ACS_PKT_FMT_1
0339  #
0340  . # < CHECK >
0341  CHECK ACS. F_ACS_PKT_FMT           =ACSHK1
0342  CHECK ACS_SYS. I_IP_GNRT           =SSAS
0343  CHECK ACS. STS_IP_SRC               =IP
0344  CHECK ACS. STS_IP_OUT               =ENA
0345  CHECK ACS. STS_IP_GNRT              =ENA
0346  CHECK ACS. I_IP_GNRT                =SSAS
0347  #
0348  #
0349  # =====
0350  . # 4.2. Switch on MDP
0351  # =====
0352  #
0353  . CALL dcsm-MDP_ON                  # MDP 立ち上げ
0354  #
0355  #
0356  #
0357  # *****
0358  . # 5. NECP Scenario
0359  # *****
0360  # =====

```


文 書 番 号	シ-ト No.	版 数
	22	1

```
0361 . # 5.1. PWI check
0362 # =====
0363 #
0364 . CALL dcsM-MD2U_OPE_CRUISE
0365 . CALL dcsM-PWI
0366 . CALL dcsM-MD2U_OPE_NORMAL
0367 #
0368 #
0369 # =====
0370 . # 5.2. MWE check
0371 # =====
0372 #
0373 . CALL dcsM-MWE
0374 #
0375 #
0376 # =====
0377 . # 5.3. MGF check
0378 # =====
0379 #
0380 . CALL dcsM-MGF
0381 #
0382 #
0383 #
0384 # =====
0385 . # 5.4. MPPE(1) check
0386 # =====
0387 #
0388 . CALL dcsM-MEA
0389 #
0390 . CALL dcsM-MIA
0391 #
0392 #
0393 # =====
0394 . # 5.5. MSASI check
0395 # =====
0396 #
0397 . CALL dcsM-SET_TLM-MODE11
0398 . CALL dcsM-MD2U_OPE_CRUISE
0399 . CALL dcsM-MSASI
0400 . CALL dcsM-SET_TLM-MODE11
0401 . CALL dcsM-MD2U_OPE_NORMAL
0402 . CALL dcsM-SET_TLM-MODE10
0403 #
0404 #
0405 # =====
0406 . # 5.6. MPPE(2) check
0407 # =====
0408 #
0409 . CALL dcsM-MSA
0410 #
0411 . CALL dcsM-ENA
0412 #
```

文 書 番 号	シート No.	版 数
	23	1

```

0413 . CALL dcsm-HEP_2
0414 #
0415 #
0416 # =====
0417 . # 5.7. MDM check
0418 # =====
0419 #
0420 . # CALL dcsm-MDM
0421 #
0422 #
0423 #
0424 # =====
0425 . # 5.8. MTL commanding of MMO (Heater health check)
0426 # =====
0427 #
0428 # # MTL (HTR_CHK) を事前にあげておき、指定した時間に実行
0429 #
0430 . CALL dcsm-HTR_CHK
0431 #
0432 #
0433 #
0434 # =====
0435 . # 5.9. Switch MMO dump route VC2 -> VC0 -> VC2
0436 # =====
0437 #
0438 # (MPO Procedure)
0439 # Stop dumps of MMO data to VC2
0440 # Start dumps of MMO data to VC0
0441 #
0442 # Keep dumps on VC0 running for 5 minutes
0443 #
0444 # Stop dumps of MMO data to VC0
0445 # Start dumps of MMO data to VC2
0446 #
0447 #
0448 #
0449 #
0450 #
0451 # *****
0452 . # 6. Prepare MMO for 1553B monitoring
0453 # *****
0454 # =====
0455 # 6.1. Switch OFF MDP
0456 # =====
0457 #
0458 . CALL dcsm-MDP_OFF
0459 #
0460 #
0461 # =====
0462 . # 6.2. MMO Operational Heater shutdown
0463 # =====
0464 #

```

文 書 番 号	シート No.	版 数
	24	1

```

0465  #
0466  . DH. ATMC_EACH_DIS 0x09
0467  . DH. TBL_ROMUPD_DIS
0468  #
0469  . # < CHECK >
0470  CHECK DH. ATMC_ENA_DIS09      =DIS      # DH5
0471  CHECK DH. TBL_ROMUPD_ED      =ENA      # DH7
0472  #
0473  #
0474  . TCS. MODE_STBY
0475  #
0476  . # < CHECK >
0477  CHECK TCS. MODE_ST          =STBY
0478  #
0479  . TCS. CTRL_HTR_P1_DIS
0480  WAIT_SEC 2
0481  TCS. CTRL_HTR_P2_DIS
0482  WAIT_SEC 2
0483  TCS. CTRL_HTR_P3_DIS
0484  WAIT_SEC 2
0485  TCS. CTRL_HTR_P4_DIS
0486  #
0487  . # < CHECK >
0488  CHECK TCS. HTR01_P_VOL_ST      =OFF
0489  CHECK TCS. HTR09_P_VOL_ST      =OFF
0490  CHECK TCS. HTR17_P_VOL_ST      =OFF
0491  CHECK TCS. HTR25_P_VOL_ST      =OFF
0492  #
0493  . ACS. DRU_SEL_NA
0494  #
0495  . # < CHECK >
0496  CHECK ACS_SYS. DRU_SEL_PT      =NA
0497  CHECK ACS. DRU_SEL_PT          =NA
0498  CHECK ACS. BIT_DRU_SEL_PT      =NA
0499  #
0500  . PCD. ON_OFF_ENA
0501  #
0502  . # < CHECK >
0503  CHECK PCD. ON_OFF_ENA_DIS      =ENA
0504  #
0505  #
0506  . PCD. DRU_PSU_A_OFF
0507  #
0508  . # < CHECK >
0509  . CHECK PCD. DRU_PSU_A_ON_OFF  =OFF
0510  #
0511  . PCD. ON_OFF_DIS
0512  #
0513  . # < CHECK >
0514  CHECK PCD. ON_OFF_ENA_DIS      =DIS
0515  #
0516  #

```

文 書 番 号	シ-ト No.	版 数
	25	1

```

0517 # =====
0518 # 6.3. BAT MONI_OFF
0519 # =====
0520 #
0521 # QL          Check-Item          Spec          Check
0522 # -----+-----+-----+-----+
0523 # PCS1        PCD. CHG_LR1_ON_OFF  OFF           G  N
0524 #             PCD. CHG_LR2_ON_OFF  OFF           G  N
0525 #             DMC-PSU-A, -B, XTRP-A, B, SV-Heater 以外が OFF であること  G  N
0526 # TCIU        DH. XTRP_A_TX_ON_OFF  OFF           G  N
0527 #             DH. XTRP_B_TX_ON_OFF  OFF           G  N
0528 #
0529 #
0530 # < CHECK >
0531 CHECK PCD. CHG_BYPS_ENA_DIS          255
0532 CHECK PCD. SA_V                      -
0533 CHECK PCD. SA_I                      -
0534 CHECK PCD. APR1_I                    -
0535 CHECK PCD. APR2_I                    -
0536 CHECK PCD. BUS_V                     -
0537 CHECK PCD. LOAD_I                    -
0538 CHECK PCD. CHG_I                     -
0539 CHECK PCD. DCHG_I                    -
0540 CHECK PCD. BAT_V                     -
0541 CHECK PCD. CEL1_V                    -
0542 CHECK PCD. CEL2_V                    -
0543 CHECK PCD. CEL3_V                    -
0544 CHECK PCD. CEL4_V                    -
0545 CHECK PCD. CEL5_V                    -
0546 CHECK PCD. CEL6_V                    -
0547 CHECK PCD. CEL7_V                    -
0548 CHECK PCD. CEL8_V                    -
0549 CHECK PCD. CEL9_V                    -
0550 CHECK PCD. CEL10_V                   -
0551 CHECK PCD. CEL11_V                   -
0552 CHECK PCD. CEL12_V                   -
0553 CHECK PCD. BAT_CV                    -
0554 CHECK PCD. BAT_CV_ON_OFF             -
0555 CHECK DH. BAT_TEMP1                  -
0556 CHECK DH. BAT_TEMP2                  -
0557 #
0558 PCD. CEL_V_MONI_OFF                  # 【BAT 管理手順書 (TPP-7S8238-03K) へ記録】
0559 #
0560 # < CHECK >
0561 CHECK PCD. CEL_V_MONI_ON_OFF          =OFF
0562 CHECK PCD. BAT_CV                     -
0563 CHECK PCD. BAT_CV_ON_OFF              -
0564 #
0565 #
0566 #
0567 # =====
0568 # 6.4. MC STOP

```

```

0569 # =====
0570 #
0571 . DH. ATMC_DIS
0572 DH. SYSTMR_DIS
0573 # DH. ALL_MC_STOP
0574 #
0575 . # < CHECK >
0576 CHECK DH. ATMC_ENA_DIS =DIS
0577 CHECK DH. SYSTMR_ENA_DIS =DIS
0578 # CHECK DH. MC_ENA_DIS =DIS
0579 #
0580 #
0581 # =====
0582 . # 6.5. MMO TM mode configuration for 1553B monitoring
0583 # =====
0584 #
0585 . DH. DUMMY
0586 #
0587 # QL          Check-Item          Spec          Check
0588 # -----+-----+-----+-----+
0589 # DH1          DH. CMD_EXE_CNT      +1 増加      G  N
0590 # DH1          DH. CMD_DLV_CNT      +1 増加      G  N
0591 #
0592 #
0593 . DH. MC_EACH_EXE 0x002C # 1553B TLM mode 0 (SYS HK 1)
0594 #
0595 #: << MACRO >>
0596 #: 0x00 DH. DMP_PERIOD 0x04
0597 #: 0x04 ACS. ACS_PKT_FMT_1
0598 #: 0x05 DH. OPE_MODE 0x0C 0x0A 0x00 0x00 0x000000000000000000000000
0599 #: 0x06 DH. TLMFMT1553B_CHG 0x00
0600 #
0601 . # < CHECK >
0602 CHECK DH. DMP_PERIOD =SEC_16
0603 CHECK DH. CATEGORY_TBL_NO 0x00
0604 CHECK DH. DWNLNK_RATE =BPS16384
0605 CHECK DH. TLM1553BFMTNO 0x00
0606 #
0607 #
0608 #
0609 # *****
0610 . # 7. Suspending dumps of MMO data to simulate a ground station non visibility during NECP
0611 # *****
0612 # =====
0613 . # 7.1. Stop dumps of MMO data to VG2
0614 # =====
0615 #
0616 # Stop MMO dumps during the second day for a period of 2 hours.
0617 # This simulates data dumps outside pass.
0618 #
0619 # 2 時間テレメがとまる（非可視を模擬）
0620 #

```

文 書 番 号	シート No.	版 数
	27	1

```

0621 # =====
0622 # 7.2. Start dumps of MMO data to VC2
0623 # =====
0624 #
0625 # Start the MMO PS dumps after two hours.
0626 # Dumping the stored data should take around 3 minutes.
0627 # Note, packet store will be dumped on VC2 with priority 8.
0628 #
0629 # テレメ再開 止まったところから再生される 約3分で追いつくみこみ
0630 #
0631 #
0632 # *****
0633 # 8. Switch off MMO
0634 # *****
0635 # =====
0636 # 8.1. Start OBCP: MMO Switch to Cruise Dormant Mode with JZXK4241
0637 # =====
0638 #
0639 # (MPO Procedure JX-FCP-002)
0640 #
0641 # HLVの確認のため、途中で DRU_A が ON になり、再度 OFF になる
0642 #
0643 # =====
0644 # 8.2. MMO switch-OFF
0645 # =====
0646 #
0647 # (MPO Procedure JX-SVT-002)
0648 # check that MMO is in dormant mode with both survival heaters switched-on.
0649 # Switch OFF CNV-A and CNV-B.
0650 # Switch OFF both survival (SED) heaters.
0651 #
0652 #
0653 # =====
0654 # 8.3. Stop dumps of MMO data to VC2
0655 # =====
0656 #
0657 # Dumps are stopped automatically after PFM handover back to AIT
0658 # at the end of SVT-1c day 4.
0659 #
0660 #

```

文 書 番 号	シート No.	版 数
TPP-7S8238-42K	28	1

dcsn(MDP 立ち上げ)

```

#!HEAD: dcsn-MDP_ON 2018-03-07 14:10:00 486 45 MMO DCSM
// SVT-1c MDP_ON
8001 #
8002 # <startTime>2018-03-16T00:00:00Z</startTime>
8003 # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004 . # =====
8005 . # 1. MDP 立ち上げ
8006 . # =====
8007 #
8008 . DH.MC_EACH_EXE 0x002C # 1553B TLM mode 0 (SYS HK 1)
8009 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
8010 #: 0x00 DH.DMP_PERIOD 0x04
8011 #: 0x04 ACS.ACS_PKT_FMT_1
8012 #: 0x05 DH.OPE_MODE 0x0C 0x0A 0x00 0x00 0x00000000000000000000000000000000
8013 #: 0x06 DH.TLMFMT1553B_CHG 0x00
8014 WAIT_SEC 40
8015 #
8016 # < CHECK >
8017 CHECK DH.DMP_PERIOD =SEC_16
8018 CHECK DH.CATEGORY_TBL_NO 0x00
8019 CHECK DH.DWNLNK_RATE =BPS16384
8020 CHECK DH.TLM1553BFMTNO 0x00
8021 #
8022 # MDP-DPU1,2(A 系) の LINK Enable を確認
8023 #
8024 # < CHECK >
8025 CHECK DH.SPW_POTMD_L1 =ENA
8026 CHECK DH.SPW_POTMD_L2 =ENA
8027 #
8028 . # MDP-DPU1,2 を電源 ON する。(HW_STATE1 は 16 秒以内にチェックすること。)
8029 #
8030 # <<< MDP-DPU1/2 Power ON >>>
8031 #
8032 . DH.MDP_DPU12_POWER_ON # DH.MC_EACH_EXE 0x099
8033 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
8034 #: 0x00 PCD.ON_OFF_ENA
8035 #: 0x01 PCD.PSU_MDP1_ON
8036 #: 0x02 PCD.PSU_MDP2_ON
8037 #: 0x03 PCD.ON_OFF_DIS
8038 #
8039 #
8040 . # < CHECK >
8041 CHECK MD1F.DPU_HW_STATE1 =DPU_RESET
8042 CHECK MD2F.DPU_HW_STATE1 =DPU_RESET
8043 CHECK PCD.ON_OFF_ENA_DIS =DIS
8044 CHECK PCD.PSU_MDP1_ON_OFF =ON
8045 CHECK PCD.PSU_MDP2_ON_OFF =ON
8046 CHECK DH.SPW_LNKST_L1 =RUN
8047 CHECK DH.SPW_LNKST_L2 =RUN
8048 #
8049 # *****

```

文 書 番 号	シート No.	版 数
	29	1

```

8050 # WAIT 16 sec (HW Initialization of DPU1 & DPU2)
8051 # *****
8052 WAIT_SEC 40
8053 #
8054 # <<< MDP-DPU Initial HK check (2): 16 sec after PWR-ON >>>
8055 #
8056 # < CHECK >
8057 CHECK MD1F.DPU_HW_STATE1      =DPU_RUN
8058 CHECK MD1F.DPU_HW_STATE2      =CPUOFF_DPUBUS
8059 CHECK MD1F.DMC_IF_RMAPERR     =na
8060 CHECK MD1F.DMC_IF_2BITERR     =na
8061 CHECK MD1F.DMC_IF_BTOERR     =na
8062 CHECK MD1F.BTO_ERR           =na
8063 CHECK MD1F.WDT_ERR           =na
8064 CHECK MD1F.DPU_SELECT        =DPU1
8065 CHECK MD2F.DPU_HW_STATE1      =DPU_RUN
8066 CHECK MD2F.DPU_HW_STATE2      =CPUOFF_DPUBUS
8067 CHECK MD2F.DMC_IF_RMAPERR     =na
8068 CHECK MD2F.DMC_IF_2BITERR     =na
8069 CHECK MD2F.DMC_IF_BTOERR     =na
8070 CHECK MD2F.BTO_ERR           =na
8071 CHECK MD2F.WDT_ERR           =na
8072 CHECK MD2F.DPU_SELECT        =DPU2
8073 #
8074 # <<< MDP-DPU CPU_ON / RUN >>>
8075 DH.MDP_DPU12_CPU_RUN # DH.MC_EACH_EXE 0x009A
8076 #:          << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
8077 #:          0x00 DH.ATMC_EACH_ENA 0x34
8078 #:          0x01 DH.SPW_RTEFDIR_SET 0x1E
8079 #:          0x05 DH.ATMC_EACH_ENA 0x36
8080 #:          0x06 MD1F.CPU_ON
8081 #:          0x07 MD2F.CPU_ON
8082 #:          0x08 MD1F.CPU_RUN
8083 #:          0x09 MD2F.CPU_RUN
8084 #:          0x0A DH.ATMC_EACH_ENA 0x30
8085 #:          0x0F DH.ATMC_EACH_ENA 0x31
8086 WAIT_SEC 180
8087 # ここで 180 秒待機 (MDP MiddleWare による初期化終了待ち)
8088 #
8089 # < CHECK >
8090 CHECK DH.SPW_FDIR_ENA_MDP1     =ENA
8091 CHECK DH.SPW_FDIR_ENA_MDP2     =ENA
8092 CHECK DH.SPW_FDIR_ENA_PCD      =ENA
8093 CHECK DH.SPW_FDIR_ENA_RMTRTR   =ENA
8094 CHECK MD1F.DPU_HW_STATE2      =CPU_RUN
8095 CHECK MD2F.DPU_HW_STATE2      =CPU_RUN
8096 #
8097 #
8098 # NECP で-updatewait は使用できないのでコメントアウト
8099 #
8100 # < MDP1 SATELITE TIME CHECK (0x200/s) >
8101 #

```


文 書 番 号	シート No.	版 数
	30	1

```

8102 # CHECK MD1F.SAT_TIME -updatewait 180
8103 # LET LocalA0 = {MD1F.SAT_TIME} & 0xFFFFFFFF
8104 # CHECK MD1F.SAT_TIME -updatewait 4
8105 # LET LocalA1 = {MD1F.SAT_TIME} & 0xFFFFFFFF
8106 # LET LocalA1 = LocalA1 - LocalA0
8107 # SYSTEM test {LocalA1} = 512
8108 #
8109 # < MDP1 SUNPULSE TIME CHECK (0x800-0xB00/4s) >
8110 #
8111 # CHECK MD1F.SUNPULSE_TIME -updatewait 8
8112 # LET LocalA0 = {MD1F.SUNPULSE_TIME} & 0xFFFFFFFF
8113 # CHECK MD1F.SUNPULSE_TIME -updatewait 8
8114 # LET LocalA1 = {MD1F.SUNPULSE_TIME} & 0xFFFFFFFF
8115 # LET LocalA1 = LocalA1 - LocalA0
8116 # SYSTEM test {LocalA1} -ge 2048 -a {LocalA1} -le 2816
8117 #
8118 # < MDP2 SATELITE TIME CHECK (0x200/s) >
8119 #
8120 # CHECK MD2F.SAT_TIME -updatewait 8
8121 # LET LocalA0 = {MD2F.SAT_TIME} & 0xFFFFFFFF
8122 # CHECK MD2F.SAT_TIME -updatewait 4
8123 # LET LocalA1 = {MD2F.SAT_TIME} & 0xFFFFFFFF
8124 # LET LocalA1 = LocalA1 - LocalA0
8125 # SYSTEM test {LocalA1} = 512
8126 #
8127 # < MDP2 SUNPULSE TIME CHECK (0x800-0xB00/4s) >
8128 #
8129 # CHECK MD2F.SUNPULSE_TIME -updatewait 8
8130 # LET LocalA0 = {MD2F.SUNPULSE_TIME} & 0xFFFFFFFF
8131 # CHECK MD2F.SUNPULSE_TIME -updatewait 8
8132 # LET LocalA1 = {MD2F.SUNPULSE_TIME} & 0xFFFFFFFF
8133 # LET LocalA1 = LocalA1 - LocalA0
8134 # SYSTEM test {LocalA1} -ge 2048 -a {LocalA1} -le 2816
8135 #
8136 #
8137 # <<< MDP-DPU Initial HK check (3): after the Initialization >>>
8138 # CPU_RUN 時、確率 7/8 で SLOT_ERR 起きるが問題ない(起動時の SLOT 番号齟齬)
8139 #
8140 # < CHECK >
8141 CHECK MD1F.DPU_HW_STATE1 =DPU_RUN
8142 CHECK MD1F.DPU_HW_STATE2 =CPU_RUN
8143 CHECK MD1F.DMC_IF_RMAPERR =na
8144 CHECK MD1F.DMC_IF_2BITERR =na
8145 CHECK MD1F.DMC_IF_BTOERR =na
8146 CHECK MD1F.BTO_ERR =na
8147 CHECK MD1F.WDT_ERR =na
8148 CHECK MD1F.DPU_SELECT =DPU1
8149 CHECK MD1F.DPU_SW_STATE 2
8150 CHECK MD1F.SW_CMD_CODE 0
8151 CHECK MD1F.SW_CMD_CNT 0
8152 CHECK MD1F.SW_CMD_REJ_CNT 0
8153 CHECK MD1F.SW_CMD_REJ_CODE 0

```

文 書 番 号	シート No.	版 数
	31	1

8154	CHECK MD1F. MS_CMD_PRI0	=DMC
8155	CHECK MD1F. RDN_MODE	=OFF
8156	CHECK MD1F. SUNPULSE_MODE	=NML
8157	CHECK MD1F. LOAD_DUMP_DIS	=DIS
8158	CHECK MD1F. MEMLOAD_BUSY	=OFF
8159	CHECK MD1F. MEMLOAD_NODE	0
8160	CHECK MD1F. MEMDUMP_BUSY	=OFF
8161	CHECK MD1F. MEMDUMP_NODE	0
8162	CHECK MD1F. TASK_STAT_AP01	=OFF
8163	CHECK MD1F. TASK_STAT_AP02	=OFF
8164	CHECK MD1F. TASK_STAT_AP03	=OFF
8165	CHECK MD1F. TASK_STAT_AP04	=OFF
8166	CHECK MD1F. TASK_STAT_AP05	=OFF
8167	CHECK MD1F. TASK_STAT_AP06	=OFF
8168	CHECK MD1F. TASK_STAT_AP07	=OFF
8169	CHECK MD1F. TASK_STAT_AP08	=OFF
8170	CHECK MD1F. TASK_STAT_AP09	=OFF
8171	CHECK MD1F. TASK_STAT_AP10	=OFF
8172	CHECK MD1F. TASK_STAT_AP11	=OFF
8173	CHECK MD1F. TASK_STAT_AP12	=OFF
8174	CHECK MD1F. TASK_STAT_AP13	=OFF
8175	CHECK MD1F. TASK_STAT_AP14	=OFF
8176	CHECK MD1F. TASK_STAT_AP15	=OFF
8177	CHECK MD1F. TASK_STAT_AP16	=OFF
8178	CHECK MD1F. UNEXPECT_CNT	0
8179	CHECK MD1F. UNEXPECT_CODE	0
8180	CHECK MD1F. DMC_IF_ERR	=na
8181	CHECK MD1F. TIME_ERR	=na
8182	CHECK MD1F. INST_CACHE_ERR	=na
8183	CHECK MD1F. DATA_CACHE_ERR	=na
8184	CHECK MD1F. SRAM_1BERR	=na
8185	CHECK MD1F. CMD_BUFF_1BERR	=na
8186	CHECK MD1F. CMD_BUFF_2BERR	=na
8187	CHECK MD1F. TLM_BUFF_1BERR	=na
8188	CHECK MD1F. TLM_BUFF_2BERR	=na
8189	CHECK MD1F. COMP_1BERR	=na
8190	CHECK MD1F. DS_1BERR	=na
8191	CHECK MD1F. DS_2BERR	=na
8192	CHECK MD1F. RDN_LINK_STAT	=ON
8193	CHECK MD1F. RIF_LINK_ERR	=na
8194	CHECK MD1F. RIF_ERR	=na
8195	CHECK MD1F. MS0_LINK_STAT	=OFF
8196	CHECK MD1F. MS1_LINK_STAT	=OFF
8197	CHECK MD1F. MS2_LINK_STAT	=OFF
8198	CHECK MD1F. MS3_LINK_STAT	=OFF
8199	CHECK MD1F. MS4_LINK_STAT	=OFF
8200	CHECK MD1F. MS5_LINK_STAT	=OFF
8201	CHECK MD1F. MS6_LINK_STAT	=OFF
8202	CHECK MD1F. MS7_LINK_STAT	=OFF
8203	CHECK MD1F. MS0_DAT_CLCT	=OFF
8204	CHECK MD1F. MS1_DAT_CLCT	=OFF
8205	CHECK MD1F. MS2_DAT_CLCT	=OFF

文 書 番 号	シート No.	版 数
	32	1

8206	CHECK MD1F. MS3_DAT_CLCT	=OFF
8207	CHECK MD1F. MS4_DAT_CLCT	=OFF
8208	CHECK MD1F. MS5_DAT_CLCT	=OFF
8209	CHECK MD1F. MS6_DAT_CLCT	=OFF
8210	CHECK MD1F. MS7_DAT_CLCT	=OFF
8211	CHECK MD1F. MS0_BUFF_SEL	=L
8212	CHECK MD1F. MS1_BUFF_SEL	=L
8213	CHECK MD1F. MS2_BUFF_SEL	=L
8214	CHECK MD1F. MS3_BUFF_SEL	=L
8215	CHECK MD1F. MS4_BUFF_SEL	=L
8216	CHECK MD1F. MS5_BUFF_SEL	=L
8217	CHECK MD1F. MS6_BUFF_SEL	=L
8218	CHECK MD1F. MS7_BUFF_SEL	=L
8219	CHECK MD1F. MS0_SGL_CMD_ERR	=na
8220	CHECK MD1F. MS1_SGL_CMD_ERR	=na
8221	CHECK MD1F. MS2_SGL_CMD_ERR	=na
8222	CHECK MD1F. MS3_SGL_CMD_ERR	=na
8223	CHECK MD1F. MS4_SGL_CMD_ERR	=na
8224	CHECK MD1F. MS5_SGL_CMD_ERR	=na
8225	CHECK MD1F. MS6_SGL_CMD_ERR	=na
8226	CHECK MD1F. MS7_SGL_CMD_ERR	=na
8227	CHECK MD1F. MS0_MTI_CMD_ERR	=na
8228	CHECK MD1F. MS1_MTI_CMD_ERR	=na
8229	CHECK MD1F. MS2_MTI_CMD_ERR	=na
8230	CHECK MD1F. MS3_MTI_CMD_ERR	=na
8231	CHECK MD1F. MS4_MTI_CMD_ERR	=na
8232	CHECK MD1F. MS5_MTI_CMD_ERR	=na
8233	CHECK MD1F. MS6_MTI_CMD_ERR	=na
8234	CHECK MD1F. MS7_MTI_CMD_ERR	=na
8235	CHECK MD2F. DPU_HW_STATE1	=DPU_RUN
8236	CHECK MD2F. DPU_HW_STATE2	=CPU_RUN
8237	CHECK MD2F. DMC_IF_RMAPERR	=na
8238	CHECK MD2F. DMC_IF_2BITERR	=na
8239	CHECK MD2F. DMC_IF_BTOERR	=na
8240	CHECK MD2F. BTO_ERR	=na
8241	CHECK MD2F. WDT_ERR	=na
8242	CHECK MD2F. DPU_SELECT	=DPU2
8243	CHECK MD2F. DPU_SW_STATE	2
8244	CHECK MD2F. SW_CMD_CODE	0
8245	CHECK MD2F. SW_CMD_CNT	0
8246	CHECK MD2F. SW_CMD_REJ_CNT	0
8247	CHECK MD2F. SW_CMD_REJ_CODE	0
8248	CHECK MD2F. MS_CMD_PRIO	=DMC
8249	CHECK MD2F. RDN_MODE	=OFF
8250	CHECK MD2F. SUNPULSE_MODE	=NML
8251	CHECK MD2F. LOAD_DUMP_DIS	=DIS
8252	CHECK MD2F. MEMLOAD_BUSY	=OFF
8253	CHECK MD2F. MEMLOAD_NODE	0
8254	CHECK MD2F. MEMDUMP_BUSY	=OFF
8255	CHECK MD2F. MEMDUMP_NODE	0
8256	CHECK MD2F. TASK_STAT_AP01	=OFF
8257	CHECK MD2F. TASK_STAT_AP02	=OFF

文 書 番 号	シート No.	版 数
	33	1

8258	CHECK MD2F.TASK_STAT_AP03	=OFF
8259	CHECK MD2F.TASK_STAT_AP04	=OFF
8260	CHECK MD2F.TASK_STAT_AP05	=OFF
8261	CHECK MD2F.TASK_STAT_AP06	=OFF
8262	CHECK MD2F.TASK_STAT_AP07	=OFF
8263	CHECK MD2F.TASK_STAT_AP08	=OFF
8264	CHECK MD2F.TASK_STAT_AP09	=OFF
8265	CHECK MD2F.TASK_STAT_AP10	=OFF
8266	CHECK MD2F.TASK_STAT_AP11	=OFF
8267	CHECK MD2F.TASK_STAT_AP12	=OFF
8268	CHECK MD2F.TASK_STAT_AP13	=OFF
8269	CHECK MD2F.TASK_STAT_AP14	=OFF
8270	CHECK MD2F.TASK_STAT_AP15	=OFF
8271	CHECK MD2F.TASK_STAT_AP16	=OFF
8272	CHECK MD2F.UNEXPECT_CNT	0
8273	CHECK MD2F.UNEXPECT_CODE	0
8274	CHECK MD2F.DMC_IF_ERR	=na
8275	CHECK MD2F.TIME_ERR	=na
8276	CHECK MD2F.INST_CACHE_ERR	=na
8277	CHECK MD2F.DATA_CACHE_ERR	=na
8278	CHECK MD2F.SRAM_1BERR	=na
8279	CHECK MD2F.CMD_BUFF_1BERR	=na
8280	CHECK MD2F.CMD_BUFF_2BERR	=na
8281	CHECK MD2F.TLM_BUFF_1BERR	=na
8282	CHECK MD2F.TLM_BUFF_2BERR	=na
8283	CHECK MD2F.COMP_1BERR	=na
8284	CHECK MD2F.DS_1BERR	=na
8285	CHECK MD2F.DS_2BERR	=na
8286	CHECK MD2F.RDN_LINK_STAT	=ON
8287	CHECK MD2F.RIF_LINK_ERR	=na
8288	CHECK MD2F.RIF_ERR	=na
8289	CHECK MD2F.MS0_LINK_STAT	=OFF
8290	CHECK MD2F.MS1_LINK_STAT	=OFF
8291	CHECK MD2F.MS2_LINK_STAT	=OFF
8292	CHECK MD2F.MS3_LINK_STAT	=OFF
8293	CHECK MD2F.MS4_LINK_STAT	=OFF
8294	CHECK MD2F.MS5_LINK_STAT	=OFF
8295	CHECK MD2F.MS6_LINK_STAT	=OFF
8296	CHECK MD2F.MS7_LINK_STAT	=OFF
8297	CHECK MD2F.MS1_DAT_CLCT	=OFF
8298	CHECK MD2F.MS3_DAT_CLCT	=OFF
8299	CHECK MD2F.MS5_DAT_CLCT	=OFF
8300	CHECK MD2F.MS6_DAT_CLCT	=OFF
8301	CHECK MD2F.MS7_DAT_CLCT	=OFF
8302	CHECK MD2F.MS1_BUFF_SEL	=L
8303	CHECK MD2F.MS3_BUFF_SEL	=L
8304	CHECK MD2F.MS5_BUFF_SEL	=L
8305	CHECK MD2F.MS6_BUFF_SEL	=L
8306	CHECK MD2F.MS7_BUFF_SEL	=L
8307	CHECK MD2F.MS0_SGL_CMD_ERR	=na
8308	CHECK MD2F.MS1_SGL_CMD_ERR	=na
8309	CHECK MD2F.MS2_SGL_CMD_ERR	=na

文 書 番 号	シート No.	版 数
	34	1

```

8310 CHECK MD2F.MS3_SGL_CMD_ERR      =na
8311 CHECK MD2F.MS4_SGL_CMD_ERR      =na
8312 CHECK MD2F.MS5_SGL_CMD_ERR      =na
8313 CHECK MD2F.MS6_SGL_CMD_ERR      =na
8314 CHECK MD2F.MS7_SGL_CMD_ERR      =na
8315 CHECK MD2F.MS0_MTI_CMD_ERR      =na
8316 CHECK MD2F.MS1_MTI_CMD_ERR      =na
8317 CHECK MD2F.MS2_MTI_CMD_ERR      =na
8318 CHECK MD2F.MS3_MTI_CMD_ERR      =na
8319 CHECK MD2F.MS4_MTI_CMD_ERR      =na
8320 CHECK MD2F.MS5_MTI_CMD_ERR      =na
8321 CHECK MD2F.MS6_MTI_CMD_ERR      =na
8322 CHECK MD2F.MS7_MTI_CMD_ERR      =na
8323 #
8324 #
8325 . # < MDP 立ち上げ後の SW エラークリア >
8326 . MD1F.DPU_SW_ERR_CLR
8327 WAIT_SEC 40
8328 MD2F.DPU_SW_ERR_CLR
8329 #
8330 . # # Mission Data を 1553B 通信の半分の帯域 (約 4kbps) で出力するモードとする
8331 . DH.MC_EACH_EXE 0x0036 # 1553B TLM mode 10 (SYS HK 1/16, BUS User HK 1/8, DUMP 1/16,
8332 # # MDP User HK 1/8, Mission User HK 1/8, Mission Data 1/2)
8333 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
8334 #: 0x00 DH.DMP_PERIOD 0x04
8335 #: 0x01 DH.DR_PTLOGCYC_SET 0x00
8336 #: 0x02 ACS.ACS_PKT_FMT_1
8337 #: 0x05 DH.OPE_MODE 0x0C 0x3E 0x2C 0x04 0x00000000000000000000000000000000
8338 #: 0x06 DH.TLMFMT1553B_CHG 0x0A
8339 #
8340 WAIT_SEC 40
8341 #
8342 CHECK DH.DMP_PERIOD                  =SEC_16
8343 CHECK DH.CATEGORY_TBL_NO            0x2C
8344 CHECK DH.DWNLNK_RATE                =BPS16384
8345 CHECK DH.TLM1553BFMTNO              0x0A
8346 CHECK ACS.F_ACS_PKT_FMT            =ACSHK1
8347 #
8348 #
8349 . # < MDP-DPU User-Task RUN : MDP-DPU1/2 APL START >
8350 . DH.MDP_DPU12_APL_START # DH.MC_EACH_EXE 0x009B
8351 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
8352 #: 0x00 DH.ATMC_EACH_ENA 0x32
8353 #: 0x02 MD1F.APP01_START
8354 #: 0x03 MD1F.APP02_START
8355 #: 0x04 MD1F.APP03_START
8356 #: 0x05 MD1F.APP04_START
8357 #: 0x06 MD1F.APP05_START
8358 #: 0x07 DH.ATMC_EACH_ENA 0x33
8359 #: 0x08 MD2F.APP01_START
8360 #: 0x09 MD2F.APP02_START
8361 #: 0x0A MD2F.APP13_START

```

```

8362 #: 0x0B MD2F. APP14_START
8363 #: 0x0C MD2F. APP05_START
8364 #
8365 WAIT_SEC 40
8366 #
8367 #
8368 # < CHECK >
8369 CHECK DH. ATMC_ENA_DIS32 =ENA
8370 CHECK DH. ATMC_ENA_DIS33 =ENA
8371 #
8372 # QL Check-Item Spec Check
8373 #-----+-----+-----+-----
8374 # MD1F << DPU1 S/W HK >>
8375 # SWCMD_CNT (+5 increment) N/A
8376 # SWREJ_CNT (no change) N/A
8377 # << APPLICATIONS >>
8378 #
8379 # < CHECK >
8380 CHECK MD1F. TASK_STAT_AP01 =ON
8381 CHECK MD1F. TASK_STAT_AP02 =ON
8382 CHECK MD1F. TASK_STAT_AP03 =ON
8383 CHECK MD1F. TASK_STAT_AP04 =ON
8384 CHECK MD1F. TASK_STAT_AP05 =ON
8385 #
8386 # QL Check-Item Spec Check
8387 #-----+-----+-----+-----
8388 # MD2F << DPU2 S/W HK >>
8389 # SWCMD_CNT (+5 increment) N/A
8390 # SWREJ_CNT (no change) N/A
8391 # << APPLICATIONS >>
8392 #
8393 # < CHECK >
8394 CHECK MD2F. TASK_STAT_AP01 =ON
8395 CHECK MD2F. TASK_STAT_AP02 =ON
8396 CHECK MD2F. TASK_STAT_AP13 =ON
8397 CHECK MD2F. TASK_STAT_AP14 =ON
8398 CHECK MD2F. TASK_STAT_AP05 =ON
8399 #
8400 #
8401 # < MDP USER-TASK: Initial Sequence - MDP-DPU-INITSET>
8402 #
8403 #
8404 DH. MDP_DPU12_INIT_SET # DH. MC_EACH_EXE 0x080
8405 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
8406 #: 0x00 MD1U. HK_SET 0xFF 0x08
8407 #: 0x01 MD1U. HK_MODE_CHK
8408 #: 0x02 MD1U. TLM_CMP 0xFF 0x00 0x11
8409 #: 0x03 MD2U. HK_SET 0xFF 0x08
8410 #: 0x04 MD2U. HK_MODE_CHK
8411 #: 0x05 MD2U. TLM_CMP 0xFF 0x00 0x11
8412 #: 0x0E MD1U. WDT_ENA
8413 #: 0x0F MD2U. WDT_ENA

```

文 書 番 号	シート No.	版 数
	36	1

8414	WAIT_SEC 5			
8415	MD1U. HK_SET 0xFF 0x04			
8416	WAIT_SEC 2			
8417	MD2U. HK_SET 0xFF 0x04			
8418	WAIT_SEC 40			
8419	#			
#	QL	Check-Item	Spec	Check
8421	#			
8422	# MD1U	U_CmdDmc_GetCNT	(increment: +5)	N/A
8423	#	U_CmdDmc_ExecCNT	(increment: +5)	N/A
8424	#	U_App03_Load	(checked by MDP)	
8425	#			
8426	# MD2U	U_CmdDmc_GetCNT	(increment: +5)	N/A
8427	#	U_CmdDmc_ExecCNT	(increment: +5)	N/A
8428	#	U_App13_Load	(checked by MDP)	
8429	#			
8430	# < CHECK >			
8431	CHECK MD1U. U_WdtEna		=ENA	
8432	CHECK MD1U. U_HkMode		=CHK	
8433	CHECK MD1U. U_Hk_RepInt_DPU1		4 4	
8434	CHECK MD1U. U_Hk_RepInt_ENA		4 4	
8435	CHECK MD1U. U_Hk_RepInt_HEPE		4 4	
8436	CHECK MD1U. U_Hk_RepInt_HEPI		4 4	
8437	CHECK MD1U. U_Hk_RepInt_MEA1		4 4	
8438	CHECK MD1U. U_Hk_RepInt_MEA2		4 4	
8439	CHECK MD1U. U_Hk_RepInt_MGFO		4 4	
8440	CHECK MD1U. U_Hk_RepInt_MIA		4 4	
8441	CHECK MD1U. U_Hk_RepInt_MSA		4 4	
8442	CHECK MD1U. U_TImML_CMP_DPU1		=CMP	
8443	CHECK MD1U. U_TImML_CMP_ENA		=USR	
8444	CHECK MD1U. U_TImML_CMP_HEPE		=ARO	
8445	CHECK MD1U. U_TImML_CMP_HEPI		=ARO	
8446	CHECK MD1U. U_TImML_CMP_MEA1		=USR	
8447	CHECK MD1U. U_TImML_CMP_MEA2		=USR	
8448	CHECK MD1U. U_TImML_CMP_MGFO		=ARO	
8449	CHECK MD1U. U_TImML_CMP_MIA		=USR	
8450	CHECK MD1U. U_TImML_CMP_MSA		=USR	
8451	CHECK MD1U. U_TImH_CMP_DPU1		=CMP	
8452	CHECK MD1U. U_TImH_CMP_HEPE		=ARO	
8453	CHECK MD1U. U_TImH_CMP_HEPI		=ARO	
8454	CHECK MD1U. U_TImH_CMP_MEA1		=USR	
8455	CHECK MD1U. U_TImH_CMP_MEA2		=USR	
8456	CHECK MD1U. U_TImH_CMP_MGFO		=ARO	
8457	CHECK MD1U. U_TImH_CMP_MIA		=USR	
8458	CHECK MD1U. U_TImH_CMP_MSA		=USR	
8459	CHECK MD2U. U_WdtEna		=ENA	
8460	CHECK MD2U. U_HkMode		=CHK	
8461	CHECK MD2U. U_Hk_RepInt_DPU2		4 4	
8462	CHECK MD2U. U_Hk_RepInt_EWO		4 4	
8463	CHECK MD2U. U_Hk_RepInt_MEF		4 4	
8464	CHECK MD2U. U_Hk_RepInt_MGFI		4 4	
8465	CHECK MD2U. U_Hk_RepInt_MSAS		4 4	

文 書 番 号	シ-ト No.	版 数
	37	1

8466	CHECK MD2U. U_Hk_RepInt_MWE	4 4	
8467	CHECK MD2U. U_TImML_CMP_DPU2	=CMP	
8468	CHECK MD2U. U_TImML_CMP_AM2P	=ARO	
8469	CHECK MD2U. U_TImML_CMP_EFD	=ARO	
8470	CHECK MD2U. U_TImML_CMP_EWOB	=ARO	
8471	CHECK MD2U. U_TImML_CMP_EWOE	=ARO	
8472	CHECK MD2U. U_TImML_CMP_MDM	=ARO	
8473	CHECK MD2U. U_TImML_CMP_MGFI	=ARO	
8474	CHECK MD2U. U_TImML_CMP_MSAS	=ARO	
8475	CHECK MD2U. U_TImML_CMP_SOR	=ARO	
8476	CHECK MD2U. U_TImH_CMP_DPU2	=CMP	
8477	CHECK MD2U. U_TImH_CMP_EFD	=ARO	
8478	CHECK MD2U. U_TImH_CMP_EWOB	=USR	
8479	CHECK MD2U. U_TImH_CMP_EWOE	=USR	
8480	CHECK MD2U. U_TImH_CMP_MGFI	=ARO	
8481	#		
8482	# QL	Check-Item	Spec
8483	#		Check
8484	# CMD 出力装置	Message	STATUS CHECK 結果 OK
8485	#		G N
8486	#		

文 書 番 号	シ-ト No.	版 数
	38	1

dcsM(MDP OPE_CRUISE 設定)

```
#!HEAD: dcsM-MD2U_OPE_CRUISE 2018-03-07 14:10:00 14 45 MMO DCSM
// SVT-1c MDDPU2_OPE_CRUISE
8001 #
8002 # <startTime>2018-03-16T00:00:00Z</startTime>
8003 # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004 # =====
8005 . # 1. SET MD2U Cruise mode 【interactive】
8006 # =====
8007 . #
8008 . MD2U.OPE_CRUISE
8009 #
8010 # QL          Check-Item          Spec          Judgment
8011 # -----+-----+-----+-----+
8012 # MD2U          *****CHECK by MDP-CORE*****
8013 #          U_TIm_OpeMode          CHK          N/A
8014 #
```

文 書 番 号	シート No.	版 数
	39	1

dcsn(PWI CHECK)

```

#!HEAD: dcsn-PWI 2018-03-07 14:12:00 582          45 MMO          DCSM          //
SVT-1c PWI_CHECK
8001  #
8002  # <startTime>2018-03-16T00:00:00Z</startTime>
8003  # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004  # *****
8005  # 1. PWI TEST
8006  # *****
8007  # =====
8008  # 1.1. SET MD2U Cruise mode 【interactive】
8009  # =====
8010  #
8011  # MD2U. OPE_CRUISE
8012  #
8013  # QL          Check-Item          Spec          Judgment
8014  # -----+-----+-----+-----
8015  # MD2U          *****CHECK by MDP-CORE*****
8016  #          U_Tlm_OpeMode          CHK          N/A
8017  #
8018  # <startTime>2018-03-16T00:00:00Z</startTime>
8019  # <stopTime>2018-03-16T20:00:00Z</stopTime>
8020  # =====
8021  # 1.2. PME PSU ON
8022  # =====
8023  #
8024  # PCD. ON_OFF_ENA
8025  #
8026  # ++++++
8027  #   Waiting for the update of the HK
8028  # ++++++
8029  #
8030  # QL          Check-Item          Spec          check
8031  # -----+-----+-----+-----
8032  # PCS1          <PCD> ON/OFF          ENA          N/A
8033  #
8034  # PCD. PSU_PME_ON
8035  #
8036  # ++++++
8037  #   Waiting for the update of the HK
8038  # ++++++
8039  #
8040  # QL          Check-Item          Spec          check
8041  # -----+-----+-----+-----
8042  # PCS1          <PCD> PME          ON          N/A
8043  #
8044  # PCD. ON_OFF_DIS
8045  #
8046  # QL          Check-Item          Spec          check
8047  # -----+-----+-----+-----
8048  # PCS1          <PCD> ON/OFF          DIS          N/A
8049  #

```

文 書 番 号	シート No.	版 数
TPP-7S8238-42K	40	1

8050	# =====			
8051	# 1.3. Initial status check			
8052	# =====			
8053	#			
8054	# MD2F.PME_EWO_ON			
8055	#			
8056	# ++++++			
8057	# Waiting for the update of the HK			
8058	# ++++++			
8059	#			
8060	# QL	Check-Item	Spec	判 定
8061	#			
8062	# MD2F	MD2F.PME_EWO_STS	ON	N/A
8063	#			
8064	# QL	Check-Item	Spec	判 定
8065	#			
8066	# EWO	PROM-READ	NONE	N/A
8067	# EWO	RELAY INIT	0x0	N/A
8068	# EWO	ECL X1 ANGL	0x9	N/A
8069	# EWO	WPT PREAMP POWER	OFF	N/A
8070	# EWO	WPT PREAMP GAIN	LOW	N/A
8071	# EWO	WPT PREAMP BIAS SW	ON	N/A
8072	# EWO	WPT PREAMP BIAS/CAL	BIAS	N/A
8073	# EWO	WPT PREAMP CAL SW	OFF	N/A
8074	# EWO	WPT PREAMP CAL/IMP	R1k	N/A
8075	# EWO	SEARCH COIL PREAMP POWER	OFF	N/A
8076	# EWO	SEARCH COIL PREAMP CAL SW	OFF	N/A
8077	# EWO	EFD SYNC	EWO	N/A
8078	# EWO	EFD OBS MODE	STBY	N/A
8079	# EWO	BIAS FEEDBACK	OFF	N/A
8080	# EWO	WPT1 BIAS	0x80	N/A
8081	# EWO	WPT2 BIAS	0x80	N/A
8082	# EWO	WPT1 LOOP GAIN	0x00	N/A
8083	# EWO	WPT2 LOOP GAIN	0x00	N/A
8084	# EWO	BIAS SWEEP TBL	0x40	N/A
8085	# EWO	BIAS SWEEP	0x00	N/A
8086	# EWO	BIAS SWEEP COUNT	0x00	N/A
8087	# EWO	SPB X1, 2	L: SN M: SS	N/A
8088	# EWO	SPB Y1, 2	L: SN M: SS	N/A
8089	# EWO	DPB WAVE X	L: S M: S	N/A
8090	# EWO	DPB WAVE Y	L: S M: S	N/A
8091	# EWO	DPB SPECT X	L: S M: S	N/A
8092	# EWO	DPB SPECT Y	L: N M: S	N/A
8093	# EWO	WFC(E) INT	0x2	N/A
8094	# EWO	WFC(E) OBS MODE	STBY	N/A
8095	# EWO	WFC(E) INP MODE	DIFF	N/A
8096	# EWO	WFC(E) LPF FREQ	FC_20kHz	N/A
8097	# EWO	WFC(E) GAIN	LOW	N/A
8098	# EWO	WFC(E) L-COMP0	E	N/A
8099	# EWO	WFC(E) M-COMP01	E1	N/A
8100	# EWO	WFC(E) M-COMP02	E2	N/A
8101	# EWO	WFC(E) VL COMP0	E1_E2	N/A

文 書 番 号	シート No.	版 数
	41	1

8102	# EWO	WFC(E) VL INT	0x0	N/A
8103	# EWO	WFC(E) VL COUNT	0x0	N/A
8104	# EWO	WFC(B) INT	0x2	N/A
8105	# EWO	WFC(B) OBS MODE	STBY	N/A
8106	# EWO	WFC(B) LPF FREQ	FC_20kHz	N/A
8107	# EWO	WFC(B) GAIN	LOW	N/A
8108	# EWO	WFC(B) L-COMPO	B	N/A
8109	# EWO	WFC(B) M-COMPO1	BX	N/A
8110	# EWO	WFC(B) M-COMPO2	BY	N/A
8111	# EWO	WFC(B) VL COMPO	BX_BY_BZ	N/A
8112	# EWO	WFC(B) VL COUNT	0x0	N/A
8113	# EWO	WFC(AGC) INT	0x7	N/A
8114	# EWO	WFC(AGC) E	DIS	N/A
8115	# EWO	WFC(AGC) B	DIS	N/A
8116	#			
8117	# EWO	WFC(AGC) MAX	E: 0x0fff B: 0x0fff	N/A
8118	# EWO	WFC(AGC) MIN	E: 0x0080 B: 0x0080	N/A
8119	#			
8120	# EWO	WFC(H-MODE) STAT	NOP	N/A
8121	# EWO	WFC(H-MODE) E	E1_E2	N/A
8122	# EWO	WFC(H-MODE) B	BX_BY_BZ	N/A
8123	# EWO	ISDM INT	0x0	N/A
8124	# EWO	ISDM COUNT	0x0	N/A
8125	# EWO	MDM	ENA	N/A
8126	# EWO	MDM COUNT	0x0	N/A
8127	# EWO	CALIBRATION STAGE	0x0	N/A
8128	# EWO	CALIBRATION EWO(E) ATT	OFF	N/A
8129	# EWO	CALIBRATION EWO(E) ICAL	OFF	N/A
8130	# EWO	CALIBRATION EWO(B) ICAL	OFF	N/A
8131	# EWO	DATA COMPRESSION LM	ZERO	N/A
8132	# EWO	DATA COMPRESSION WFC-H	CMP	N/A
8133	# EWO	DATA COMPRESSION PREC	0x8	N/A
8134	# EWO	AM2P STAGE	0x0	N/A
8135	# EWO	AM2P INT	0x0	N/A
8136	# EWO	AM2P COUNT	0x0	N/A
8137	# EWO	AM2P TC WORD COUNT	0x0	N/A
8138	# EWO	AM2P ACK COUNT	0x1	N/A
8139	# EWO	AM2P TM TRG COUNT	0x00	N/A
8140	# EWO	AM2P ACQUI COUNT	0x00	N/A
8141	# EWO	AM2P TC WORD	0xFFFF	N/A
8142	# EWO	AM2P TRG NUM	0x01	N/A
8143	# EWO	AM2P PRESET FREQ	MF	N/A
8144	# EWO	AM2P PRESET INP	NOT_SET	N/A
8145	# EWO	AM2P PRESET WPTP	LOW	N/A
8146	# EWO	AM2P PRESET EWOE	NOT_SET	N/A
8147	# EWO	AM2P PRESET MEF MODE	0000	N/A
8148	# EWO	AM2P PRESET MEF BIAS1	000000	N/A
8149	# EWO	AM2P PRESET MEF BIAS2	000000	N/A
8150	# EWO	TEMPERATURE MONITOR	ON	N/A
8151	#			
8152	# QL	Check-Item	Data	
8153	#	-----+-----+-----		

文 書 番 号	シート No.	版 数
	42	1

8154	# EWO	ERRORS CMD COUNT		
8155	# EWO	ERRORS RMAP CRC		
8156	# EWO	WFC(E) RANGE OVER		
8157	# EWO	WFC(B) RANGE OVER		
8158	# EWO	MDP TELEMETRY		
8159	# EWO	EWO SRAM OVERFLOW EFD		
8160	# EWO	EWO SRAM OVERFLOW WFC(E)		
8161	# EWO	EWO SRAM OVERFLOW WFC(B)		
8162	# EWO	LATCH UP EFD		
8163	# EWO	LATCH UP WFC(E)		
8164	# EWO	LATCH UP WFC(B)		
8165	# EWO	ERRORS SRAM WFC(E) 1BIT		
8166	# EWO	ERRORS SRAM WFC(E) 2BIT		
8167	# EWO	ERRORS SRAM WFC(B) 1BIT		
8168	# EWO	ERRORS SRAM WFC(B) 2BIT		
8169	# EWO	MDP RING BUF1		
8170	# EWO	MDP RING BUF2		
8171	# EWO	MDP RING BUF3		
8172	# EWO	MDP RING BUF4		
8173	# EWO	TI		
8174	# EWO	FIR COEF TABLE ERR: EFD		
8175	# EWO	FIR COEF TABLE ERR: WFC(B)		
8176	#			
8177	. MD1F.DPU_SW_ERR_CLR			
8178	WAIT_SEC 2			
8179	MD2F.DPU_SW_ERR_CLR			
8180	#			
8181	. MD2U.BUF_CLCT 0x10 0x00			
8182	#			
8183	. MD2U.EWO_HW_INIT			
8184	#			
8185	#			
8186	# ++++++			
8187	# Waiting for the update of the HK			
8188	# ++++++			
8189	#			
8190	. # QL	Check-Item	Spec	判 定
8191	#	-----+-----+-----+-----+-----		
8192	# EWO	PROM-READ	NONE->...->NONE	N/A
8193	#		START, EFD1, EFD2,	
8194	#		SWEEP, EWOB, ISDM may be	
8195	#		seen depending on timing	
8196	#			
8197	# EWO	WPT PREAMP CAL/IMP	R10M	N/A
8198	# EWO	BIAS SWEEP COUNT	0x1	N/A
8199	#			
8200	. MD2U.BUF_CLCT 0x10 0x01			
8201	#			
8202	#			
8203	. # QL	Check-Item	Spec	判 定
8204	#	-----+-----+-----+-----+-----		
8205	# EWO	DATA COMPRESSION LM	ZERO	N/A

文 書 番 号	シート No.	版 数
	43	1

```

8206 #
8207 #
8208 #
8209 # =====
8210 # 1.4 PWI ON except MEFISTO
8211 # =====
8212 #
8213 # EWO.WPTP_BIAS_OFF
8214 #
8215 WAIT_SEC 40
8216 #
8217 # QL          Check-item          spec          判定
8218 # -----
8219 # EWO          WPT PREAMP BIAS SW          OFF          N/A
8220 #
8221 EWO.EFD_OBS_START
8222 #
8223 WAIT_SEC 40
8224 #
8225 # QL          Check-item          spec          Check
8226 # -----
8227 # EWO          EFD OBS MODE          OBS          N/A
8228 #
8229 #
8230 EWO.WFCE_OBS_START
8231 #
8232 WAIT_SEC 40
8233 #
8234 # QL          Check-item          spec          判定
8235 # -----
8236 # EWO          WFC(E) OBS MODE          WFC_OBS          N/A
8237 #
8238 #
8239 EWO.WFCB_OBS_START
8240 #
8241 WAIT_SEC 40
8242 #
8243 # QL          Check-item          spec          判定
8244 # -----
8245 # EWO          WFC(B) OBS MODE          OBS          N/A
8246 #
8247 #
8248 # MD2U.EFD_FILTER_MODE 0x11
8249 #
8250 # CHECK EWO.EFD_FILTER_MODE 0x11
8251 #
8252 # <<SORBET Power ON>>
8253 # MD2F.PME_SORBET_ON
8254 #
8255 #
8256 # ++++++
8257 # Waiting for the update of the HK

```

文 書 番 号	シート No.	版 数
	44	1

8258	#	+++++
8259	#	
8260	# QL	Check-item spec Check
8261	#	-----
8262	# MDP2	PME_SORB_STS ON N/A
8263	#	
8264	#	<<AM2P Power ON>>
8265	#	MD2F. PME_AM2P_ON
8266	#	
8267	#	+++++
8268	#	Waiting for the update of the HK
8269	#	+++++
8270	#	
8271	#	
8272	# QL	Check-item spec Check
8273	#	-----
8274	# MDP2	PME_AM2P_STS ON N/A
8275	#	
8276	#	<< WPT-Pre ON>>
8277	#	EWO. WPTP_ON
8278	#	
8279	#	
8280	#	+++++
8281	#	Waiting for the update of the HK
8282	#	+++++
8283	#	
8284	# QL	Check-item spec Check
8285	#	-----
8286	# EWO	WPT PREAMP POWER ON N/A
8287	#	
8288	#	EWO. SCP_ON
8289	#	
8290	#	
8291	#	+++++
8292	#	Waiting for the update of the HK
8293	#	+++++
8294	#	
8295	# QL	Check-item spec Check
8296	#	-----
8297	# EWO	SEARCH COIL PREAMP POWER ON N/A
8298	#	
8299	#	MD1F. DPU_SW_ERR_CLR
8300	#	
8301	# QL	Check-Item Spec Check
8302	#	-----
8303	# MD1F OBS HK	MD1F. SLOT_ERR na N/A
8304	#	
8305	#	MD2F. DPU_SW_ERR_CLR
8306	#	
8307	# QL	Check-Item Spec Check
8308	#	-----
8309	# MD2F OBS HK	MD2F. SLOT_ERR na N/A

文 書 番 号	シート No.	版 数
	45	1

8310	#	MD2F. MS1_LINK_ERR	na	N/A
8311	#	MD2F. MS3_LINK_ERR	na	N/A
8312	#			
8313	#			
8314	#	<<Confirmation of the initial HK data>>		
8315	#			
8316	#	===== Record the initial status =====		
8317	#	(recorded by the PI)		
8318	#			
8319	#	QL	Check-item	spec Check
8320	#	-----		
8321	#	SRBT	COMMAND CODE	0x0000 N/A
8322	#		WPT GAIN	LOW N/A
8323	#		WPT BIAS	OFF N/A
8324	#		WPT CAL	OFF N/A
8325	#		AM2P OPERATION	OFF N/A
8326	#		TNR1 OPERATION	OFF N/A
8327	#		TNR1 INP SETUP	0 N/A
8328	#		TNR2 OPERATION	OFF N/A
8329	#		TNR2 INP SETUP	0 N/A
8330	#		HFR OPERATION	OFF N/A
8331	#		HFR INP SETUP	0 N/A
8332	#		HFR VIA TNR1	0 N/A
8333	#		HFR VIA TNR2	0 N/A
8334	#		HF1 RANGE	0 N/A
8335	#		HF2 RANGE	0 N/A
8336	#		HFR FREQ. STEP	0 N/A
8337	#		SPACEWIRE LINK RUNNING	1 N/A
8338	#		SPACEWIRE DISCON. ERROR	NORMAL N/A
8339	#		CALIBRATION SOURCE SETUP	0 N/A
8340	#		CALIBRATION BAND A	0 N/A
8341	#		CALIBRATION BAND B	0 N/A
8342	#		CALIBRATION BAND C	0 N/A
8343	#		CALIBRATION BAND D	0 N/A
8344	#		CALIBRATION HF1	0 N/A
8345	#		CALIBRATION HF2	0 N/A
8346	#			
8347	#	<<Confirmation of PWI status>>		
8348	#			
8349	#	SORB. OBS_SEQ		
8350	#			
8351	#	QL	Check-Item	Spec Check
8352	#	-----		
8353	#	SRBT	COMMAND CODE	0X518F or 0x53BF N/A
8354	#		COMMAND COUNTER	Increment N/A
8355	#			
8356	#	*****		
8357	#	1.5 MEFISTO test ON		
8358	#	*****		
8359	#			
8360	#	EWO. WFCE_HF_SEL		
8361	#			


```

8362 . # QL          Check-item          Spec          Check
8363 # -----+-----+-----+-----
8364 # EWO          WFC(E) LPF FREQ      FC_120KHZ      N/A
8365 #
8366 . MD2F.PME_MEFISTO_ON
8367 #
8368 #
8369 # ++++++
8370 . #      Waiting for the update of the HK
8371 # ++++++
8372 #
8373 . # QL          Check-Item          Spec          判 定
8374 # -----+-----+-----+-----
8375 # MD2F          MD2F.PME_EWO_STS      ON              N/A
8376 # EWO          WPT PREAMP POWER      ON              N/A
8377 # MD2F          MD2F.PME_MEFISTO_STS  ON              N/A
8378 #
8379 . MD1F.DPU_SW_ERR_CLR
8380 WAIT_SEC 2
8381 MD2F.DPU_SW_ERR_CLR
8382 #
8383 # Automatic shutdown when MDP finds |HV|>110V
8384 . MD2U.MEF_HV_CNTL_ENA 0x28 0xFF 0x00 0x90
8385 #
8386 . # QL          Check-Item          Spec          RESULT
8387 # -----+-----+-----+-----
8388 # MEFISTO       U_HV_CNTL_CHK          ENA              N/A
8389 # MEFISTO       U_HV_CNTL_DET          NON              N/A
8390 # MEFISTO       U_HV_CNTL_PARA_NL      0x28 (-110V)    N/A
8391 # MEFISTO       U_HV_CNTL_PARA_NU      0xFF             N/A
8392 # MEFISTO       U_HV_CNTL_PARA_PL      0x00             N/A
8393 # MEFISTO       U_HV_CNTL_PARA_PU      0x90 (+110V)    N/A
8394 #
8395 . MEFI.INIT_A
8396 #
8397 #
8398 # ++++++
8399 . #      Waiting for the update of the HK
8400 # ++++++
8401 #
8402 . # QL          Check-Item          Spec          RESULT
8403 # -----+-----+-----+-----
8404 # MEFISTO       COMMAND_TALLY          increment        N/A
8405 # MEFISTO       VALID_CMD              increment        N/A
8406 # MEFISTO       INVALID_CMD            no change        N/A
8407 # -----+-----+-----+-----
8408 # MEFISTO       INIT_COUNT              2               N/A
8409 # MEFISTO       U_HV_CNTL_DET          NON              N/A
8410 # -----+-----+-----+-----
8411 #
8412 . MEFI.INIT_A
8413 #

```

文 書 番 号	シート No.	版 数
TPP-7S8238-42K	47	1

8414	# QL	Check-Item	Spec	RESULT
8415	#	-----+-----+-----+-----		
8416	# MEFISTO	COMMAND_TALLY	increment	N/A
8417	# MEFISTO	VALID_CMD	increment	N/A
8418	# MEFISTO	INVALID_CMD	no change	N/A
8419	#	-----+-----+-----+-----		
8420	# MEFISTO	INIT_COUNT	2	N/A
8421	# MEFISTO	U_HV_CNTL_DET	NON	N/A
8422	#	-----+-----+-----+-----		
8423	#			
8424	#	Automatic shutdown when MDP finds HV >110V or HV <75V		
8425	MD2U.MEF_HV_CNTL_ENA	0x28 0x5F 0x61 0x90		
8426	#			
8427	# QL	Check-Item	Spec	RESULT
8428	#	-----+-----+-----+-----		
8429	# MEFISTO	U_HV_CNTL_CHK	ENA	N/A
8430	# MEFISTO	U_HV_CNTL_DET	NON	N/A
8431	# MEFISTO	U_HV_CNTL_PARA_NL	0x28 (-110V)	N/A
8432	# MEFISTO	U_HV_CNTL_PARA_NU	0x5F (-75V)	N/A
8433	# MEFISTO	U_HV_CNTL_PARA_PL	0x61 (+75)	N/A
8434	# MEFISTO	U_HV_CNTL_PARA_PU	0x90 (+110V)	N/A
8435	#			
8436	MEFI.INIT_B			
8437	#			
8438	#			
8439	#	+++++		
8440	#	Waiting for the update of the HK		
8441	#	+++++		
8442	#			
8443	# QL	Check-Item	Spec	RESULT
8444	#	-----+-----+-----+-----		
8445	# MEFISTO	COMMAND_TALLY	increment	N/A
8446	# MEFISTO	VALID_CMD	increment	N/A
8447	# MEFISTO	INVALID_CMD	no change	N/A
8448	#	-----+-----+-----+-----		
8449	# MEFISTO	INIT_COUNT	1	N/A
8450	#	-----+-----+-----+-----		
8451	#			
8452	MEFI.INIT_C			
8453	#			
8454	#			
8455	#	+++++		
8456	#	Waiting for the update of the HK		
8457	#	+++++		
8458	#			
8459	# QL	Check-Item	Spec	RESULT
8460	#	-----+-----+-----+-----		
8461	# MEFISTO	COMMAND_TALLY	increment	N/A
8462	# MEFISTO	VALID_CMD	increment	N/A
8463	# MEFISTO	INVALID_CMD	no change	N/A
8464	#	-----+-----+-----+-----		
8465	# MEFISTO	BDAC1	0x8000	N/A

文 書 番 号	シート No.	版 数
	48	1

```

8466 # MEFISTO BDAC2 0x8000 N/A
8467 # MEFISTO SDAC1 0x8000 N/A
8468 # MEFISTO SDAC2 0x8000 N/A
8469 # MEFISTO ADC_HV_NEG
8470 # MEFISTO ADC_HV_POS
8471 # -----+-----+-----+-----
8472 #
8473 # *****
8474 # 1.6 PWI test (ALL ON)
8475 # *****
8476 #
8477 #
8478 . MD2U.EWO_WFCE_GAIN_SET 0x01
8479 #
8480 # QL Check-item spec Check
8481 # -----
8482 # EWO WFC(E) GAIN LOW N/A
8483 #
8484 . EWO.WFCB_PGA_L_SEL
8485 #
8486 #
8487 . MD2U.EWO_WFCE_FREQ_SEL 0x00
8488 #
8489 # QL Check-item spec Check
8490 # -----
8491 # EWO WFC(E) LPF FREQ FC_20KHZ N/A
8492 #
8493 #
8494 . ##### AM2P Wire mode #####
8495 #
8496 . DH.PWI_AM2P_DOUB_WR_MODE_HF # DH.MC_EACH_EXE 0x00DB
8497 #: << MACRO >> the sequence is shown below(DH.DUMMYs are omitted)
8498 #: 0x00 MD2U.EWO_AM2P_FREQ_PRST 0x01
8499 #: 0x01 MD2U.EWO_AM2P_INP_PRST 0x01
8500 #: 0x02 MD2U.EWO_AM2P_MEF_MODE_PRST 0x212a
8501 #: 0x03 MD2U.EWO_AM2P_MEF_BIAS1_SET 0x800080
8502 #: 0x04 MD2U.EWO_AM2P_MEF_BIAS2_SET 0x800080
8503 #: 0x05 MD2U.EWO_AM2P_TC_SET 0x7004 0x07
8504 #
8505 . MD2U.EWO_AM2P_WPTP_GAIN_PRST 0x00
8506 WAIT_SEC 2
8507 MD2U.EWO_AM2P_EWOE_GAIN_PRST 0x01
8508 #
8509 . # First shot (commanded by PI)
8510 . MD2U.EWO_AM2P_INT_SET 0xFFFF 0x01
8511 #
8512 WAIT_SEC 60
8513 #
8514 . # Second shot (commanded by PI)
8515 . MD2U.EWO_AM2P_INT_SET 0xFFFF 0x01
8516 #
8517 WAIT_SEC 60

```

文 書 番 号	シ-ト No.	版 数
	49	1

```

8518 #
8519 # =====
8520 . # 1.7. PWI Shutdown (Interactive mode)
8521 # =====
8522 . #
8523 . DH. PWI_OFF1_AM2P_MEF_PRE # DH. MC_EACH_EXE 0x008B
8524 #: << MACRO >> the sequence is shown below(DH. DUMMYs are omitted)
8525 #: 0x00 MD2U. EWO_AM2P_INT_SET 0x0000 0x00
8526 #: 0x0D MD2F. PME_MEFISTO_OFF
8527 #: 0x0E EWO. SCP_OFF
8528 #: 0x0F EWO. WPTP_OFF
8529 #
8530 #
8531 # ++++++
8532 . # Waiting for the update of the HK
8533 # ++++++
8534 #
8535 . # < CHECK >
8536 CHECK MD2F. MS4_LINK_STAT =OFF
8537 CHECK MD2F. PME_MEFISTO_STS =OFF
8538 CHECK EWO. SCP_ONOF =OFF
8539 CHECK EWO. WPTP_ONOF =OFF
8540 . #
8541 . DH. PWI_OFF2_AM2P_SOR_EWO # DH. MC_EACH_EXE 0x00AD
8542 #: << MACRO >> the sequence is shown below(DH. DUMMYs are omitted)
8543 #: 0x00 MD2F. PME_AM2P_OFF
8544 #: 0x01 MD2F. SORB_CLCT_STOP
8545 #: 0x02 MD2F. PME_SORBET_OFF
8546 #: 0x03 MD2U. BUF_CLCT 0x10 0x00
8547 #: 0x0F MD2F. PME_EWO_OFF
8548 #
8549 #
8550 # ++++++
8551 . # Waiting for the update of the HK
8552 # ++++++
8553 #
8554 . # < CHECK >
8555 CHECK MD2F. MS1_DAT_CLCT =OFF
8556 CHECK MD2F. MS3_DAT_CLCT =OFF
8557 CHECK MD2F. PME_SORBET_STS =OFF
8558 CHECK MD2F. PME_AM2P_STS =OFF
8559 CHECK MD2F. PME_EWO_STS =OFF
8560 CHECK MD2F. MS1_LINK_STAT =OFF
8561 CHECK MD2F. MS2_LINK_STAT =OFF
8562 CHECK MD2F. MS3_LINK_STAT =OFF
8563 . #
8564 . PCD. ON_OFF_ENA
8565 #
8566 . # < CHECK >
8567 CHECK PCD. ON_OFF_ENA_DIS =ENA
8568 . #
8569 . PCD. PSU_PME_OFF

```

文 書 番 号	シート No.	版 数
	50	1

```
8570 . PCD.ON_OFF_DIS
8571 # ***** PWI all off *****
8572 . # < CHECK >
8573 CHECK PCD.PSU_PME_ON_OFF =OFF
8574 CHECK PCD.ON_OFF_ENA_DIS =DIS
8575 #
8576 . # QL          Check-Item          Spec          Check
8577 # -----+-----+-----+-----
8578 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
8579 #
8580 # <startTime>2018-03-16T00:00:00Z</startTime>
8581 # <stopTime>2018-03-16T20:00:00Z</stopTime>
8582 # =====
8583 . # 1.8. SET MD2U Normal mode 【interactive】
8584 # =====
8585 . #
8586 . MD2U.OPE_NRM
8587 #
8588 # QL          Check-Item          Spec          Judgment
8589 # -----+-----+-----+-----
8590 # MD2U          *****CHECK by MDP-CORE*****
8591 #          U_Tlm_OpeMode          nrm          N/A
8592 #
```

文 書 番 号	シ-ト No.	版 数
	51	1

dcsM(MDP OPE NRM 設定)

```
#!HEAD: dcsM-MD2U_OPE_NORMAL 2018-03-07 14:15:00 14 45 MMO DCSM
// SVT-1c MDDPU2 OPE NRM
8001 #
8002 # <startTime>2018-03-16T00:00:00Z</startTime>
8003 # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004 # =====
8005 . # 1. SET MD2U Normal mode 【interactive】
8006 # =====
8007 . #
8008 . MD2U_OPE_NRM
8009 #
8010 # QL          Check-Item          Spec          Judgment
8011 # -----+-----+-----+-----+
8012 # MD2U          *****CHECK by MDP-CORE*****
8013 #          U_TIm_OpeMode          nrm          N/A
8014 #
```

dcsn(MWE CHECK)

```

#!HEAD: dcsn-MWE 2018-03-07 14:21:00 221          45 MM0          DCSM          //
SVT-1c MWE_CHECK
8001  #
8002  # <startTime>2018-03-16T00:00:00Z</startTime>
8003  # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004  # *****
8005  # 1. MWE 機能チェック ((svt-1c))
8006  # *****
8007  # =====
8008  # 1.1. MWE Initial Checkout
8009  # =====
8010  # -----
8011  # 1.1.1. PME_ON
8012  # -----
8013  #
8014  # ステータス確認は MGF 側で実施するものとする。
8015  #
8016  # PCD.ON_OFF_ENA
8017  #
8018  # WAIT_SEC 40
8019  #
8020  # QL          Check-Item          Spec          Check
8021  # -----+-----+-----+-----
8022  # PCS1          PCD.ON_OFF_ENA_DIS          ENA          N/A
8023  #
8024  #
8025  # PCD.PSU_PME_ON
8026  #
8027  # WAIT_SEC 40
8028  #
8029  # QL          Check-Item          Spec          Check
8030  # -----+-----+-----+-----
8031  # PCS1          PCD.PSU_PME_ON_OFF          ON          N/A
8032  #
8033  # PCD.ON_OFF_DIS
8034  #
8035  # WAIT_SEC 40
8036  #
8037  # QL          Check-Item          Spec          Check
8038  # -----+-----+-----+-----
8039  # PCS1          PCD.ON_OFF_ENA_DIS          DIS          N/A
8040  #
8041  #
8042  # -----
8043  # 1.1.2. MWE ON and HK CHECK
8044  # -----
8045  #
8046  # MD2F.PME_MAST_ON
8047  #
8048  # WAIT_SEC 180
8049  #

```

8050	# QL	Check-Item	Spec	Check
8051	#			
8052	# MD2F	MD2F. PME_MAST_STS	ON	N/A
8053	#			
8054	#			
8055	#	2) POWER ON から 5 秒以上経過後、ステータスを QL にてチェックする。		
8056	#			
8057	# QL	Check-Item	Spec	Check
8058	#			
8059	# MAST/WPT-E	MWE. STAT_STBY	OFF	N/A
8060	# MAST/WPT-E	MWE. STAT_MST_STBY	OFF	N/A
8061	# MAST/WPT-E	MWE. STAT_WPT_STBY	OFF	N/A
8062	# MAST/WPT-E	MWE. STAT_MST_PSU	MAIN	N/A
8063	# MAST/WPT-E	MWE. STAT_WPT_PSU	MAIN	N/A
8064	# MAST/WPT-E	MWE. STAT_SAFE_MODE	OFF	N/A
8065	#			
8066	# MAST/WPT-E	MWE. STAT_OVLD_CONT	DIS	N/A
8067	# MAST/WPT-E	MWE. STAT_TEMP_CONT	DIS	N/A
8068	# MAST/WPT-E	MWE. STAT_UNBL_CONT	DIS	N/A
8069	# MAST/WPT-E	MWE. MST_MS_CONT	ENA	N/A
8070	# MAST/WPT-E	MWE. MST_LEN_CONT	DIS	N/A
8071	# MAST/WPT-E	MWE. WPT_MS_CONT	ENA	N/A
8072	# MAST/WPT-E	MWE. WPT_LEN_CONT	DIS	N/A
8073	# MAST/WPT-E	MWE. STAT_WPT_RATE	RST	N/A
8074	#			
8075	# MAST/WPT-E	MWE. MST_SC_RET	OFF	N/A
8076	# MAST/WPT-E	MWE. MST_SC_EXT	OFF	N/A
8077	# MAST/WPT-E	MWE. MST_SC_RLSE	OFF	N/A
8078	# MAST/WPT-E	MWE. MST_SC_DRV	OFF	N/A
8079	# MAST/WPT-E	MWE. CONT_MTR2_OVHT	NON	N/A
8080	# MAST/WPT-E	MWE. CONT_MTR2_OVLD	NON	N/A
8081	# MAST/WPT-E	MWE. MST_SC_MS_RET	FULL	N/A
8082	# MAST/WPT-E	MWE. MST_SC_MS_TNTS	NON	N/A
8083	# MAST/WPT-E	MWE. CONT_LEN_DET_SC	NO	N/A
8084	# MAST/WPT-E	MWE. CONT_MS_RL_SC	OFF	N/A
8085	#			
8086	# MAST/WPT-E	MWE. MST_MGF_RET	OFF	N/A
8087	# MAST/WPT-E	MWE. MST_MGF_EXT	OFF	N/A
8088	# MAST/WPT-E	MWE. MST_MGF_RLSE	OFF	N/A
8089	# MAST/WPT-E	MWE. MST_MGF_DRV	OFF	N/A
8090	# MAST/WPT-E	MWE. CONT_MTR1_OVHT	NON	N/A
8091	# MAST/WPT-E	MWE. CONT_MTR1_OVLD	NON	N/A
8092	# MAST/WPT-E	MWE. MST_MGF_MS_RET	FULL	N/A
8093	# MAST/WPT-E	MWE. MST_MGF_MS_TNTS	NON	N/A
8094	# MAST/WPT-E	MWE. CONT_LEN_DET_MGF	NO	N/A
8095	# MAST/WPT-E	MWE. CONT_MS_RL_MGF	OFF	N/A
8096	#			
8097	# MAST/WPT-E	MWE. WPT_S1_RET	OFF	N/A
8098	# MAST/WPT-E	MWE. WPT_S1_EXT	OFF	N/A
8099	# MAST/WPT-E	MWE. WPT_S1_RELS	OFF	N/A
8100	# MAST/WPT-E	MWE. WPT_S1_DRV	OFF	N/A
8101	# MAST/WPT-E	MWE. CONT_MTR1_OVHT	NON	N/A

文 書 番 号	シート No.	版 数
	54	1

8102	#	MAST/WPT-E	MWE. CONT_MTR1_OVLD	NON	N/A
8103	#	MAST/WPT-E	MWE. WPT_S1_MS_HRM	CLS	N/A
8104	#	MAST/WPT-E	MWE. WPT_S1_MS_EXT	NON	N/A
8105	#	MAST/WPT-E	MWE. CONT_LEN_DET_S1	NO	N/A
8106	#	MAST/WPT-E	MWE. CONT_MS_RL_S1	OFF	N/A
8107	#	-----+-----+-----+-----			
8108	#	MAST/WPT-E	MWE. WPT_S2_RET	OFF	N/A
8109	#	MAST/WPT-E	MWE. WPT_S2_EXT	OFF	N/A
8110	#	MAST/WPT-E	MWE. WPT_S2_RELS	OFF	N/A
8111	#	MAST/WPT-E	MWE. WPT_S2_DRV	OFF	N/A
8112	#	MAST/WPT-E	MWE. CONT_MTR2_OVHT	NON	N/A
8113	#	MAST/WPT-E	MWE. CONT_MTR2_OVLD	NON	N/A
8114	#	MAST/WPT-E	MWE. WPT_S2_MS_HRM	CLS	N/A
8115	#	MAST/WPT-E	MWE. WPT_S2_MS_EXT	NON	N/A
8116	#	MAST/WPT-E	MWE. CONT_LEN_DET_S2	NO	N/A
8117	#	MAST/WPT-E	MWE. CONT_MS_RL_S2	OFF	N/A
8118	#				
8119	#				
8120	#	-----			
8121	#	1. 1. 3. MOTOR-PSU_ON CHECK			
8122	#	-----			
8123	#	N/A (MOTOR PSU ON/OFF 省略)			
8124	#				
8125	#	-----			
8126	#	1. 1. 4. 制御コマンド確認			
8127	#	-----			
8128	#				
8129	#	<< MWE 制御コマンド確認 >>			
8130	#				
8131	#	MWE. MWE_STBY_ON			
8132	#				
8133	#	WAIT_SEC 180			
8134	#				
8135	#	QL	Check-Item	Spec	Check
8136	#	-----+-----+-----+-----			
8137	#	MAST/WPT-E	MWE. STAT_STBY	ON	N/A
8138	#	-----+-----+-----+-----			
8139	#	MAST/WPT-E	MWE. MWE_CMND_ADRS	0x0A10	M/A
8140	#	MAST/WPT-E	MWE. MWE_CMND_CODE	0c0010	N/A
8141	#				
8142	#	QL	Check-Item	Spec	Check
8143	#	-----+-----+-----+-----			
8144	#	MAST/WPT-E	MWE. MWE_FPGA_TEMP	取得のみ _____°C	N/A
8145	#	MAST/WPT-E	MWE. MWE_PTR_TEMP	取得のみ _____°C	N/A
8146	#	-----+-----+-----+-----			
8147	#	MAST/WPT-E	MWE. MSTSC_MTR_TEMP	取得のみ _____°C	N/A
8148	#	MAST/WPT-E	MWE. MSTSC_CAN_TEMP	取得のみ _____°C	N/A
8149	#	MAST/WPT-E	MWE. MSTMGF_MTR_TEMP	取得のみ _____°C	N/A
8150	#	MAST/WPT-E	MWE. MSTMGF_CAN_TEMP	取得のみ _____°C	N/A
8151	#	-----+-----+-----+-----			
8152	#	MAST/WPT-E	MWE. WPTS1_MTR_TEMP	取得のみ _____°C	N/A
8153	#	MAST/WPT-E	MWE. WPTS1_HRM_TEMP	取得のみ _____°C	N/A

8154	#	MAST/WPT-E	MWE. WPTS2_MTR_TEMP	取得のみ	_____°C	N/A
8155	#	MAST/WPT-E	MWE. WPTS2_HRM_TEMP	取得のみ	_____°C	N/A
8156	#					
8157	#					
8158	#					
8159	#					
8160	#	-----				
8161	#	1. 1. 5. MWE STBY OFF, POWER OFF				
8162	#	-----				
8163	#					
8164	#	<< スタンバイオフ >>				
8165	#	MWE. MWE_STBY_OFF				
8166	#					
8167		WAIT_SEC 40				
8168	#					
8169	#	QL	Check-Item	Spec		Check
8170	#	-----				
8171	#	MAST/WPT-E	MWE. STAT_STBY	OFF		N/A
8172	#	-----				
8173	#	MAST/WPT-E	MWE. MWE_CMND_ADRS	0x0A10		M/A
8174	#	MAST/WPT-E	MWE. MWE_CMND_CODE	0x0011		N/A
8175	#					
8176	#	QL	Check-Item	Spec		Check
8177	#	-----				
8178	#	PCS1	PCD. PSU_MOTOR_A_ON_OFF	OFF		
8179	#	PCS1	PCD. PSU_MOTOR_B_ON_OFF	OFF		
8180	#					
8181	#	<< パワーオフ >>				
8182	#	MD2F. PME_MAST_OFF				
8183	#					
8184		WAIT_SEC 40				
8185	#					
8186	#	QL	Check-Item	Spec		Check
8187	#	-----				
8188	#	MD2F	MD2F. PME_MAST_STS	OFF		N/A
8189	#					
8190	#					
8191	#	-----				
8192	#	1. 1. 6. PME_OFF				
8193	#	-----				
8194	#					
8195	#	PCD. ON_OFF_ENA				
8196	#					
8197		WAIT_SEC 40				
8198	#					
8199	#	QL	Check-Item	Spec		Check
8200	#	-----				
8201	#	PCS1	PCD. ON_OFF_ENA_DIS	ENA		N/A
8202	#					
8203	#					
8204	#	PCD. PSU_PME_OFF				
8205	#					

8206 WAIT_SEC 40

8207 #

8208 . # QL Check-Item Spec Check

8209 # -----+-----+-----+-----

8210 # PCS1 PCD. PSU_PME_ON_OFF OFF N/A

8211 #

8212 . PCD. ON_OFF_DIS

8213 #

8214 WAIT_SEC 40

8215 #

8216 . # QL Check-Item Spec Check

8217 # -----+-----+-----+-----

8218 # PCS1 PCD. ON_OFF_ENA_DIS DIS N/A

8219 #

8220 #

8221 #

dcsn(MGF CHECK)

```

#!HEAD: dcsn-MGF 2018-03-07 14:32:00 419          45 MMO          DCSM          //
SVT-1c MGF_CHECK
8001  #
8002  # <startTime>2018-03-16T00:00:00Z</startTime>
8003  # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004  # *****
8005  # 1. MGF commissioning ((SVT-1c))
8006  # *****
8007  # =====
8008  # 1.1. MGF commissioning in REAL TIME OPERATION
8009  # =====
8010  # -----
8011  # 1.1.1. MGF-I Initial power ON, HK check and parameter set
8012  # -----
8013  #
8014  # <MGF-I Power ON>
8015  #
8016  PCD.ON_OFF_ENA
8017  #
8018  WAIT_SEC 40
8019  #
8020  # QL          Check-Item          Spec          Check
8021  # -----+-----+-----+-----
8022  # PCS1        PCD.ON_OFF_ENA_DIS    ENA          N/A
8023  #
8024  PCD.PSU_MGF_I_ON
8025  #
8026  WAIT_SEC 40
8027  #
8028  # QL          Check-Item          Spec          Check
8029  # -----+-----+-----+-----
8030  # PCS1        PCD.PSU_MGF_I_ON_OFF  ON          N/A
8031  #
8032  #
8033  PCD.ON_OFF_DIS
8034  #
8035  WAIT_SEC 40
8036  #
8037  # QL          Check-Item          Spec          Check
8038  # -----+-----+-----+-----
8039  # PCS1        PCD.ON_OFF_ENA_DIS    DIS          N/A
8040  #
8041  #
8042  # <MGF-I HK check>
8043  #
8044  WAIT_SEC 100
8045  #
8046  # QL          Check-Item          Spec          Check
8047  # -----+-----+-----+-----
8048  # MGF-I       MGF1.CRC_ERR_CNTR      0          N/A
8049  #             MGF1.RESET_CNTR        0          N/A

```

文 書 番 号	シート No.	版 数
	58	1

8050	#	MGFI. CMD_CNTR	増加, FF の次は 0	N/A
8051	#	MGFI. PHASE_VAL_X_A	1.9 deg	N/A
8052	#	MGFI. PHASE_VAL_X_A	00 hex	N/A
8053	#	MGFI. PHASE_VAL_Y_A	1.9 deg	N/A
8054	#	MGFI. PHASE_VAL_Y_A	00 hex	N/A
8055	#	MGFI. PHASE_VAL_Z_A	1.9 deg	N/A
8056	#	MGFI. PHASE_VAL_Z_A	00 hex	N/A
8057	#	MGFI. SYNC_DLY_VAL_A	7812.5 us	N/A
8058	#	MGFI. SYNC_DLY_VAL_A	00 hex	N/A
8059	#	-----+-----+-----+-----		
8060	#	MGFI. TLM_OUT_STAT_L	OUTPUT	N/A
8061	#	MGFI. TLM_OUT_RATE_L	SPIN	N/A
8062	#	MGFI. TLM_OUT_STAT_M	OUTPUT	N/A
8063	#	MGFI. TLM_OUT_RATE_M	F8HZ	N/A
8064	#	MGFI. TLM_OUT_STAT_H	STOP	N/A
8065	#	MGFI. TLM_OUT_RATE_H	CHECKOUT	N/A
8066	#			
8067	#			
8068	#	<Spacewire LINK 確立の確認(MDP~MGF-I 間)>		
8069	#			
8070	#	QL	Check-Item	Spec
8071	#	-----+-----+-----+-----		
8072	#	MD2F OBS HK	MD2F. MS5_LINK_STAT	ON
8073	#			N/A
8074	#			
8075	#	<MDP エラークリア/ERR CLEAR MDP2 / MGF-I >		
8076	#			
8077		MD2F. DPU_SW_ERR_CLR		
8078	#			
8079		WAIT_SEC 40		
8080	#			
8081	#	QL	Check-Item	Spec
8082	#	-----+-----+-----+-----		
8083	#	MD2F OBS HK	MD2F. MS5_LINK_ERR	na
8084	#		MD2F. SLOT_ERR	na
8085	#			N/A
8086	#			
8087	#	<MGF-I Parameter set>		
8088	#			
8089	#	<<PHASE COMMAND>>		
8090	#			
8091		MGFI. PHASE_SET 0xF0 0xE8 0xEE		
8092	#			
8093		WAIT_SEC 100		
8094	#			
8095	#	QL	Check-Item	Spec
8096	#	-----+-----+-----+-----		
8097	#	MGF-I	MGFI. PHASE_VAL_X_A	-28.4 deg
8098	#		MGFI. PHASE_VAL_X_A	F0 hex
8099	#		MGFI. PHASE_VAL_Y_A	-43.6 deg
8100	#		MGFI. PHASE_VAL_Y_A	E8 hex
8101	#		MGFI. PHASE_VAL_Z_A	-32.2 deg

```

8102 # MGFI. PHASE_VAL_Z_A EE hex N/A
8103 # MGFI. DELTA_T_SAMPLE
8104 #
8105 # <<SYNC COMMAND>>
8106 #
8107 MGFI. SYNC_DLY_SET 0x70
8108 #
8109 WAIT_SEC 40
8110 #
8111 # QL Check-Item Spec Check
8112 # -----+-----+-----+-----
8113 # MGF-I MGFI. SYNC_DLY_VAL_A 976.6 us N/A
8114 # MGFI. SYNC_DLY_VAL_A 70 hex N/A
8115 #
8116 #
8117 #
8118 #
8119 # <startTime>2018-03-16T00:00:00Z</startTime>
8120 # <stopTime>2018-03-16T20:00:00Z</stopTime>
8121 # -----
8122 # 1.1.2. MGF-0 Initial power ON, HK check and parameter set
8123 # -----
8124 #
8125 # <MGF-0 Power ON>
8126 #
8127 PCD. ON_OFF_ENA
8128 #
8129 WAIT_SEC 40
8130 #
8131 # QL Check-Item Spec Check
8132 # -----+-----+-----+-----
8133 # PCS1 PCD. ON_OFF_ENA_DIS ENA N/A
8134 #
8135 PCD. PSU_PME_ON
8136 #
8137 WAIT_SEC 40
8138 #
8139 # QL Check-Item Spec Check
8140 # -----+-----+-----+-----
8141 # PCS1 PCD. PSU_PME_ON_OFF ON N/A
8142 #
8143 PCD. ON_OFF_DIS
8144 #
8145 WAIT_SEC 40
8146 #
8147 # QL Check-Item Spec Check
8148 # -----+-----+-----+-----
8149 # PCS1 PCD. ON_OFF_ENA_DIS DIS N/A
8150 #
8151 MD1F. PME_MGFO_ON
8152 #
8153 WAIT_SEC 40

```

8154	#			
8155	#	QL	Check-Item	Spec
8156	#			Check
8157	#	MD1F	MD1F. PME_MGFO_STS	=ON
8158	#			N/A
8159	#			
8160	#	<MGF-0 HK check>		
8161	#			
8162		WAIT_SEC 220		
8163	#			
8164	#	QL	Check-Item	Spec
8165	#			Check
8166	#	MGF-0	MGFO. PRAM_STAT	0
8167	#		MGFO. MDATRAM_STAT	0
8168	#		MGFO. EXTSYNC_OFF	0
8169	#		MGFO. RELAY_ON_OFF	1
8170	#		MGFO. OPEN_LOOP	0
8171	#		MGFO. EXCIT_ON	1
8172	#		MGFO. EEPROM_PAGE	0
8173	#		MGFO. BOOT_OP	0
8174	#		MGFO. BOOT_MAIN	1
8175	#		MGFO. CS_OK	0
8176	#		MGFO. RELAY1_STAT	1
8177	#		MGFO. SYNC_PRESENT	1
8178	#			
8179	#	MGF-0	MGFO. LINK_DISCONNECT	0
8180	#		MGFO. LINK_EERR	0
8181	#		MGFO. LINK_CERR	0
8182	#		MGFO. LINK_PERR	0
8183	#		MGFO. RMAP_ERR_FLAGS	0
8184	#			
8185	#	MGF-0	MGFO. VALID_WCMDS	0
8186	#		MGFO. VALID_CMDS	incrementing
8187	#		MGFO. INVALID_CMDS	0
8188	#		MGFO. VALID_REPLIES	incrementing
8189	#		MGFO. SYNC_OFFSET	0xA1FB
8190	#			
8191	#	MGF-0	MGFO. SOFTWARE_VER	0x0007
8192	#		MGFO. EEPROM_CHECKSUM	0x0000
8193	#		MGFO. CALC_CHECKSUM	0x0000
8194	#			
8195	#	MGF-0	MGFO. ELECTR_TEMP	>-30.0 < +70.0
8196	#		MGFO. OS_TEMP	>-60.0 < +180.0
8197	#		MGFO. IS_TEMP	>-60.0 < +180.0
8198	#		MGFO. CURRENT_P12V	>+65.0 < +78.0
8199	#		MGFO. CURRENT_M12V	>-40.0 < -29.0
8200	#		MGFO. CURRENT_3V3	>+32.0 < +39.0
8201	#		MGFO. CURRENT_1V5	>+55.0 < +77.0
8202	#		MGFO. VOLTAGE_PANA	> +7.3 < +7.9
8203	#		MGFO. VOLTAGE_MANA	> -7.9 < -7.5
8204	#		MGFO. VOLTAGE_P12V	>+11.3 < +12.5
8205	#		MGFO. VOLTAGE_M12V	>-12.5 < -11.3

文 書 番 号	シート No.	版 数
	61	1

8206	#	MGFO. VOLTAGE_3V3	> +3.1 < +3.5	N/A
8207	#	MGFO. VOLTAGE_P5V	> +4.7 < +5.3	N/A
8208	#	MGFO. VOLTAGE_M5V	> -5.3 < -4.7	N/A
8209	#	MGFO. VOLTAGE_1V5	>+1.37 < +1.63	N/A
8210	#	MGFO. VOLTAGE_EXCIT	> +7.8 < +8.4	N/A
8211	#	-----+-----+-----		
8212	# MGF-0	MGFO. TLM_OUT_STAT_L	OUTPUT	N/A
8213	#	MGFO. TLM_OUT_RATE_L	SPIN	N/A
8214	#	MGFO. TLM_OUT_STAT_M	OUTPUT	N/A
8215	#	MGFO. TLM_OUT_RATE_M	F8HZ	N/A
8216	#	MGFO. TLM_OUT_STAT_H	STOP	N/A
8217	#	MGFO. TLM_OUT_RATE_H	CHECKOUT	N/A
8218	#	-----+-----+-----		
8219	# MGF-0	MGFO. DELTA_T_SAMPLE		N/A
8220	#			
8221	#			
8222	#	<Spacewire LINK establish confirmation (MDP-MGF-0)>		
8223	#			
8224	# QL	Check-Item	Spec	Check
8225	#	-----+-----+-----		
8226	# MD1F OBS HK	MD1F. MSO_LINK_STAT	ON	N/A
8227	#			
8228	#	<MDP ERR CLEAR>		
8229	#	<< MDP1 / MGF-0 >>		
8230	#			
8231		MD1F. DPU_SW_ERR_CLR		
8232	#			
8233		WAIT_SEC 40		
8234	#			
8235	# QL	Check-Item	Spec	Check
8236	#	-----+-----+-----		
8237	# MD1F OBS HK	MD1F. MSO_LINK_ERR	na	N/A
8238	#	MD1F. SLOT_ERR	na	N/A
8239	#			
8240	#			
8241	#			
8242	#			
8243	#			
8244	#	<startTime>2018-03-16T00:00:00Z</startTime>		
8245	#	<stopTime>2018-03-16T20:00:00Z</stopTime>		
8246	#	-----		
8247	#	1.1.3. MGF-I Observation sequence (get mission data)		
8248	#	-----		
8249	#			
8250	#	<START CHECKOUT MISSION DATA>		
8251	#			
8252		MD2U. MGFI_TLM_PARM 0x01010108010C		
8253	#			
8254		WAIT_SEC 160		
8255	#	WAIT_SEC 40		
8256	#			
8257	# QL	Check-Item	Spec	Check

文 書 番 号	シート No.	版 数
	62	1

```

8258 # -----+-----+-----+-----
8259 # MGF-I          MGF1.TLM_OUT_STAT_H      OUTPUT          N/A
8260 #                MGF1.TLM_OUT_RATE_H      CHECKOUT         N/A
8261 #
8262 # <TLM DATA ACQUISITION>
8263 #
8264 # WAIT_SEC 120
8265 #
8266 # <TLM M-MODE DATA RATE 4Hz>
8267 #
8268 MD2U.MGF1_TLM_PARM 0x01010104010C
8269 #
8270 WAIT_SEC 40
8271 #
8272 # QL              Check-Item                Spec              Check
8273 # -----+-----+-----+-----
8274 # MGF-I          MGF1.TLM_OUT_STAT_M      OUTPUT          N/A
8275 #                MGF1.TLM_OUT_RATE_M      F4HZ            N/A
8276 #
8277 WAIT_SEC 80                      # 120 sec data acquisition in total
8278 #
8279 # <START MGFO CHECKOUT MISSION DATA>
8280 #
8281 MD1U.MGFO_TLM_PARM 0x01010108010C
8282 #
8283 WAIT_SEC 40
8284 #
8285 # QL              Check-Item                Spec              Check
8286 # -----+-----+-----+-----
8287 # MGF-O          MGFO.TLM_OUT_STAT_H      OUTPUT          N/A
8288 #                MGFO.TLM_OUT_RATE_H      CHECKOUT         N/A
8289 #
8290 # <STOP CHECKOUT MISSION DATA, M-mode=8Hz>
8291 #
8292 MD2U.MGF1_TLM_PARM 0x01010108000C
8293 #
8294 WAIT_SEC 40
8295 #
8296 # QL              Check-Item                Spec              Check
8297 # -----+-----+-----+-----
8298 # MGF-I          MGF1.TLM_OUT_STAT_H      STOP            N/A
8299 #                MGF1.TLM_OUT_RATE_H      CHECKOUT         N/A
8300 #                MGF1.TLM_OUT_STAT_M      OUTPUT          N/A
8301 #                MGF1.TLM_OUT_RATE_M      F8HZ            N/A
8302 #
8303 #
8304 # <startTime>2018-03-16T00:00:00Z</startTime>
8305 # <stopTime>2018-03-16T20:00:00Z</stopTime>
8306 # -----
8307 # 1.1.4. MGF-O Observation sequence (get mission data)
8308 # -----
8309 #

```

```
8310 #
8311 #
8312 #
8313 # <TLM M-MODE DATA RATE 4Hz>
8314 #
8315 MD1U.MGFO_TLM_PARM 0x01010104010C
8316 #
8317 WAIT_SEC 40
8318 #
8319 # QL          Check-Item          Spec          Check
8320 # -----+-----+-----+-----
8321 # MGF-0      MGFO.TLM_OUT_STAT_M    OUTPUT        N/A
8322 #            MGFO.TLM_OUT_RATE_M    F4HZ          N/A
8323 #
8324 WAIT_SEC 80          # 120 sec data acquisition in total
8325 #
8326 #
8327 # <TLM M-MODE DATA RATE 8Hz, continue CHECKOUT>
8328 #
8329 MD1U.MGFO_TLM_PARM 0x01010108010C
8330 #
8331 WAIT_SEC 40
8332 #
8333 # QL          Check-Item          Spec          Check
8334 # -----+-----+-----+-----
8335 # MGF-0      MGFO.TLM_OUT_STAT_M    OUTPUT        N/A
8336 #            MGFO.TLM_OUT_RATE_M    F8HZ          N/A
8337 #
8338 #
8339 #
8340 #
8341 #
8342 # <STOP CHECKOUT MISSION DATA>
8343 #
8344 # # MGF-0
8345 MD1U.MGFO_TLM_PARM 0x01010108000C
8346 #
8347 WAIT_SEC 40
8348 #
8349 # QL          Check-Item          Spec          N/A
8350 # -----+-----+-----+-----
8351 # MGF-0      MGFO.TLM_OUT_STAT_H    STOP          N/A
8352 #            MGFO.TLM_OUT_RATE_H    CHECKOUT      N/A
8353 #
8354 #
8355 # <startTime>2018-03-16T00:00:00Z</startTime>
8356 # <stopTime>2018-03-16T20:00:00Z</stopTime>
8357 # *****
8358 # 2. 立ち下げ
8359 # *****
8360 # =====
8361 # 2.1. MGF Power OFF
```

```

8362 # =====
8363 #
8364 DH.MGF_OFF # DH.MC_EACH_EXE 0x00AC
8365 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
8366 #: 0x00 MD1F.MGFO_CLCT_STOP
8367 #: 0x01 MD2F.MGFI_CLCT_STOP
8368 #: 0x0C MD1F.PME_MGFO_OFF
8369 #: 0x0D PCD.ON_OFF_ENA
8370 #: 0x0E PCD.PSU_MGF_I_OFF
8371 #: 0x0F PCD.ON_OFF_DIS
8372 #
8373 WAIT_SEC 40
8374 #
8375 # < CHECK >
8376 CHECK MD1F.MS0_DAT_CLCT =OFF
8377 CHECK MD2F.MS5_DAT_CLCT =OFF
8378 CHECK MD1F.PME_MGFO_STS =OFF
8379 CHECK PCD.PSU_MGF_I_ON_OFF =OFF
8380 CHECK MD1F.MS0_LINK_STAT =OFF
8381 CHECK MD2F.MS5_LINK_STAT =OFF
8382 #
8383 # QL Check-Item Spec Check
8384 # -----+-----+-----+-----
8385 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
8386 #
8387 # <startTime>2018-03-16T00:00:00Z</startTime>
8388 # <stopTime>2018-03-16T20:00:00Z</stopTime>
8389 # =====
8390 # 2.2. PME 立ち下げ
8391 # =====
8392 #
8393 PCD.ON_OFF_ENA
8394 #
8395 WAIT_SEC 40
8396 #
8397 CHECK PCD.ON_OFF_ENA_DIS =ENA
8398 #
8399 PCD.PSU_PME_OFF
8400 #
8401 WAIT_SEC 40
8402 #
8403 CHECK PCD.PSU_PME_ON_OFF =OFF
8404 #
8405 PCD.ON_OFF_DIS
8406 #
8407 WAIT_SEC 40
8408 #
8409 CHECK PCD.ON_OFF_ENA_DIS =DIS
8410 #
8411 # QL Check-Item Spec Check
8412 # -----+-----+-----+-----
8413 # CMD 出力装置 Message STATUS CHECK 結果 OK G N

```

文 書 番 号	シ-ト No.	版 数
	65	1

8414 #
8415 #
8416 #
8417 # *****
8418 # END OF PROCEDURE
8419 . # *****

文 書 番 号	シート No.	版 数
	66	1

dcsn(MEA CHECK)

```

#!HEAD: dcsn-MEA 2018-03-07 14:41:00 435          45 MMO          DCSM          //
SVT-1c MEA_CHECK
8001  #
8002  # <startTime>2018-03-16T00:00:00Z</startTime>
8003  # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004  # *****
8005  # 1. MEA1 初期 CO
8006  # *****
8007  # =====
8008  # 1.1. MEA1 function check
8009  # =====
8010  # -----
8011  # 1.1.1. MEA1 Startup 1 min
8012  # -----
8013  #
8014  # # MEA1 の電源を ON する。
8015  # Power on MEA1 by switching on.
8016  #
8017  # PCD.ON_OFF_ENA
8018  # WAIT_SEC 40
8019  #
8020  # < CHECK >
8021  CHECK PCD.ON_OFF_ENA_DIS          =ENA
8022  #
8023  # PCD.PSU_MEA1_ON
8024  # WAIT_SEC 40
8025  #
8026  #
8027  # < CHECK >
8028  CHECK PCD.PSU_MEA1_ON_OFF          =ON
8029  #
8030  # PCD.ON_OFF_DIS
8031  # WAIT_SEC 40
8032  #
8033  # < CHECK >
8034  CHECK PCD.ON_OFF_ENA_DIS          =DIS
8035  CHECK MD1F.MS1_LINK_STAT          =ON
8036  #
8037  # # SW Error clear
8038  # MD1F.DPU_SW_ERR_CLR
8039  # WAIT_SEC 40
8040  #
8041  # < CHECK >
8042  CHECK MD1F.MS1_LINK_ERR          =na
8043  CHECK MD1F.SLOT_ERR          =na
8044  #
8045  #
8046  #
8047  WAIT_SEC 20
8048  #
8049  #

```

文 書 番 号	シート No.	版 数
	67	1

```

8050  # # MEA1 テレメトリ表示の確認 (LINK 確立から 8 秒以上経過後に実施)
8051  #   Confirmation of the MEA1 telemetry indication
8052  #   (More than 8 minutes after from LINK establishment)
8053  #
8054  # < CHECK >
8055  CHECK MEA1.HK_I_P5V          <110 # mA
8056  CHECK MEA1.HK_I_P12V       <17  # mA
8057  CHECK MEA1.HK_MCP          0    # V
8058  CHECK MEA1.I_MCP           0    # uA
8059  CHECK MEA1.HK_TMP_FPG      <30  # deg.
8060  CHECK MEA1.HK_TMP_MCP      <30  # deg.
8061  CHECK MEA1.Status_reg      0x80 # (initial value)
8062  CHECK MEA1.l1_cmd_exe      =0xFFFF
8063  CHECK MEA1.l2_cmd_exe      =0xFFFF
8064  #
8065  #
8066  #
8067  # -----
8068  # 1.1.2. SWITCH ON SCAN, 2 min
8069  #
8070  # Start the scan of the sphere and test mode in order to verify the noise on A111
8071  #
8072  # -----
8073  #
8074  # Start the scan and verify if the instrument is synchronized or not ?
8075  #
8076  . MEA1.Scan_ON
8077  WAIT_SEC 40
8078  #
8079  # <CHECK>
8080  CHECK MEA1.l1_cmd_exe          =0x2500
8081  #
8082  # Select the sun pulse synchronization
8083  . MEA1.Md_S_Auto
8084  WAIT_SEC 40
8085  #
8086  # <CHECK>
8087  CHECK MEA1.l1_cmd_exe          =0x3100
8088  #
8089  # Select energies mode : mode 32 energies/32 sweeps
8090  # -> HV table n° 2, 3eV-3KeV (~300V maxi on the analyzer)"
8091  #
8092  . MEA1.mode_ROM_3 0x02
8093  WAIT_SEC 40
8094  #
8095  # <CHECK>
8096  CHECK MEA1.l1_cmd_exe          =0x0202
8097  #
8098  #
8099  # WAIT_SEC 40
8100  #
8101  # <CHECK>

```

```

8102 # Check HKP TmSStp = HKP TmSStp_dft
8103 #
8104 ## *****
8105 # WAIT until (5 min maximum) HKP TmSStp = TmSStp_dft to indicate the synchronization
8106 # between sun plus and MEA.
8107 # If not, do not execute the next steps!!"
8108 # Probably, sun pulse signal have a problem (cf previous test at ISAS)!!"
8109 # If already executed, you can pass at the next step."
8110 ## *****
8111 #
8112 #
8113 # QL          Check-Item          Spec          Check
8114 # -----+-----+-----+-----
8115 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
8116 #
8117 #
8118 # -----
8119 # 1.1.3. TEST MODE ON, 1 min
8120 # -----
8121 #
8122 #
8123 # Swtich ON the test mode for the Amplifier A111
8124 # to verify the good working of this
8125 . MEA1.Md_Tst_ON
8126 WAIT_SEC 60
8127 #
8128 # <CHECK>
8129 CHECK MEA1.l1_cmd_exe          =0x3900
8130 CHECK MEA1.Status_reg          0x81 # (bit n° 0 = 1)
8131 #
8132 # - wait 1 min to verify the Science Data
8133 # Check the science Data and compare with the previous value
8134 #
8135 #
8136 # QL          Check-Item          Spec          Check
8137 # -----+-----+-----+-----
8138 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
8139 #
8140 #
8141 # -----
8142 # 1.1.4. TEST MODE OFF, 2 min
8143 # -----
8144 #
8145 #
8146 # Stop the A111 test Mode
8147 . MEA1.Md_Tst_OF
8148 WAIT_SEC 120
8149 #
8150 # <CHECK>
8151 CHECK MEA1.l1_cmd_exe          =0x3800
8152 CHECK MEA1.Status_reg          0x80 # (bit n° 0 = 0)
8153 #

```

文 書 番 号	シート No.	版 数
	69	1

```

8154 . # - wait 2 min to verify the Science Data are return to zero
8155 # Check the science Data and compare with the previous value
8156 #
8157 #
8158 # QL Check-Item Spec Check
8159 # -----+-----+-----+-----
8160 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
8161 #
8162 #
8163 # -----
8164 . # 1.1.5. SWITCH OFF the SCAN (40 sec)
8165 # -----
8166 #
8167 #
8168 # SWITCH OFF THE SCAN
8169 . MEA1.Scan_OF
8170 WAIT_SEC 40
8171 #
8172 # <CHECK>
8173 . CHECK MEA1.l1_cmd_exe =0x2400
8174 #
8175 #
8176 # QL Check-Item Spec Check
8177 # -----+-----+-----+-----
8178 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
8179 #
8180 # =====
8181 . # 1.2. MEA1 SHUTDOWN (1 minute)
8182 # =====
8183 #
8184 . PCD.ON_OFF_ENA
8185 # WAIT_SEC 40
8186 #
8187 # < CHECK >
8188 CHECK PCD.ON_OFF_ENA_DIS =ENA
8189 #
8190 . MD1F.MEA1_CLCT_STOP
8191 # WAIT_SEC 40
8192 #
8193 # < CHECK >
8194 CHECK MD1F.MS1_DAT_CLCT =OFF
8195 #
8196 . PCD.PSU_MEA1_OFF
8197 # WAIT_SEC 40
8198 #
8199 . # < CHECK >
8200 CHECK PCD.PSU_MEA1_ON_OFF =OFF
8201 CHECK MD1F.MS1_LINK_STAT =OFF
8202 #
8203 . PCD.ON_OFF_DIS
8204 # WAIT_SEC 40
8205 #

```


文 書 番 号	シート No.	版 数
	70	1

```

8206 . # < CHECK >
8207 CHECK PCD.ON_OFF_ENA_DIS =DIS
8208 #
8209 # QL Check-Item Spec Check
8210 # -----+-----+-----+-----
8211 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
8212 #
8213 #
8214 # *****
8215 . # 2. MEA2 初期 CO
8216 # *****
8217 # =====
8218 . # 2.1. MEA2 function check
8219 # =====
8220 # -----
8221 . # 2.1.1. MEA2 Startup 1 min
8222 # -----
8223 #
8224 . # # MEA2 の電源を ON する。
8225 # Power on MEA2 by switching on.
8226 #
8227 . PCD.ON_OFF_ENA
8228 # WAIT_SEC 40
8229 #
8230 # < CHECK >
8231 CHECK PCD.ON_OFF_ENA_DIS =ENA
8232 #
8233 . PCD.PSU_MEA2_ON
8234 # WAIT_SEC 40
8235 #
8236 #
8237 # < CHECK >
8238 CHECK PCD.PSU_MEA2_ON_OFF =ON
8239 #
8240 . PCD.ON_OFF_DIS
8241 # WAIT_SEC 40
8242 #
8243 # < CHECK >
8244 CHECK PCD.ON_OFF_ENA_DIS =DIS
8245 CHECK MD1F.MS2_LINK_STAT =ON
8246 #
8247 # # SW Error clear
8248 . MD1F.DPU_SW_ERR_CLR
8249 # WAIT_SEC 40
8250 #
8251 # < CHECK >
8252 CHECK MD1F.MS2_LINK_ERR =na
8253 CHECK MD1F.SLOT_ERR =na
8254 #
8255 #
8256 #
8257 WAIT_SEC 20

```

文 書 番 号	シート No.	版 数
	71	1

```

8258 #
8259 #
8260 # # MEA2 テレメトリ表示の確認 (LINK 確立から 8 分以上経過後に実施)
8261 # Confirmation of the MEA2 telemetry indication
8262 # (More than 8 minutes after from LINK establishment)
8263 #
8264 # < CHECK >
8265 CHECK MEA2.HK_I_P5V <110 # mA
8266 CHECK MEA2.HK_I_P12V <17 # mA
8267 CHECK MEA2.HK_MCP 0 # V
8268 CHECK MEA2.I_MCP 0 # uA
8269 CHECK MEA2.HK_TMP_FPG <30 # deg. (depend on the environment,
maximum 85° C)
8270 CHECK MEA2.HK_TMP_MCP <30 # deg. (depend on the environment,
maximum 80° C)
8271 CHECK MEA2.Status_reg 0x80 # (initial value)
8272 CHECK MEA2.l1_cmd_exe =0xFFFF
8273 CHECK MEA2.l2_cmd_exe =0xFFFF
8274 #
8275 #
8276 #
8277 # QL Check-Item Spec Check
8278 # -----+-----+-----+-----
8279 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
8280 #
8281 #
8282 # -----
8283 # 2.1.2. SWITCH ON SCAN, 2 min
8284 #
8285 # Start the scan of the sphere and test mode to verify the noise on A111
8286 #
8287 # -----
8288 #
8289 # Start the scan and verify if the instrument is synchronized or not ?
8290 #
8291 MEA2.Scan_ON
8292 WAIT_SEC 40
8293 #
8294 # <CHECK>
8295 CHECK MEA2.l1_cmd_exe =0x2500
8296 #
8297 # Select the sun pulse synchronization
8298 MEA2.Md_S_Auto
8299 WAIT_SEC 40
8300 #
8301 # <CHECK>
8302 CHECK MEA2.l1_cmd_exe =0x3100
8303 #
8304 # Select energies mode : mode 32 energies/32 sweeps
8305 # -> HV table n° 2, 3eV-3KeV (~300V maxi on the analyzer)"
8306 #
8307 MEA2.mode_ROM_3 0x02

```

文 書 番 号	シート No.	版 数
	72	1

```

8308 WAIT_SEC 40
8309 #
8310 # <CHECK>
8311 CHECK MEA2.l1_cmd_exe =0x0202
8312 #
8313 #
8314 # WAIT_SEC 40
8315 #
8316 # <CHECK>
8317 # Check HKP TmSStp = HKP TmSStp_dft
8318 #
8319 ## *****
8320 # WAIT until (5 min maximum) HKP TmSStp = TmSStp_dft to indicate the synchronization
8321 # between sun plus and MEA.
8322 # If not, do not execute the next steps!!"
8323 # Probably, sun pulse signal have a problem (cf previous test at ISAS)!!"
8324 # If already executed, you can pass at the next step."
8325 ## *****
8326 #
8327 #
8328 # QL          Check-Item          Spec          Check
8329 # -----+-----+-----+-----
8330 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
8331 #
8332 #
8333 # -----
8334 # 2.1.3. TEST MODE ON,1 min
8335 # -----
8336 #
8337 #
8338 # Swtich ON the test mode for the Amplifier A111
8339 # to verify the good working of this
8340 # MEA2.Md_Tst_ON
8341 WAIT_SEC 60
8342 #
8343 # <CHECK>
8344 CHECK MEA2.l1_cmd_exe =0x3900
8345 CHECK MEA2.Status_reg 0x81 # (bit n° 0 = 1)
8346 #
8347 # - wait 1 min to verify the Science Data
8348 # Check the science Data and compare with the previous value
8349 #
8350 #
8351 #
8352 # QL          Check-Item          Spec          Check
8353 # -----+-----+-----+-----
8354 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
8355 #
8356 #
8357 # -----
8358 # 2.1.4. TEST MODE OFF, 2 min
8359 # -----

```

文 書 番 号	シート No.	版 数
	73	1

```

8360 #
8361 #
8362 # Stop the A111 test Mode
8363 . MEA2.Md_Tst_OF
8364 WAIT_SEC 120
8365 #
8366 . # <CHECK>
8367 CHECK MEA2.l1_cmd_exe =0x3800
8368 CHECK MEA2.Status_reg 0x80 # (bit n° 0 = 0)
8369 #
8370 # - wait 2 min to verify the Science Data are return to zero
8371 # Check the science Data and compare with the previous value
8372 #
8373 #
8374 # QL          Check-Item          Spec          Check
8375 # -----+-----+-----+-----
8376 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
8377 #
8378 #
8379 # -----
8380 . # 2.1.5. SWITCH OFF the SCAN (40 sec)
8381 # -----
8382 #
8383 #
8384 # SWITCH OFF THE SCAN
8385 . MEA2.Scan_OF
8386 WAIT_SEC 40
8387 #
8388 . # <CHECK>
8389 CHECK MEA2.l1_cmd_exe =0x2400
8390 #
8391 #
8392 # QL          Check-Item          Spec          Check
8393 # -----+-----+-----+-----
8394 # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
8395 #
8396 #
8397 # =====
8398 . # 2.2. MEA2 SHUTDOWN (1 min)
8399 # =====
8400 #
8401 . PCD.ON_OFF_ENA
8402 # WAIT_SEC 40
8403 #
8404 # < CHECK >
8405 CHECK PCD.ON_OFF_ENA_DIS =ENA
8406 #
8407 . MD1F.MEA2_CLCT_STOP
8408 # WAIT_SEC 40
8409 #
8410 # < CHECK >
8411 CHECK MD1F.MS2_DAT_CLCT =OFF

```

```
8412  #
8413  . PCD.PSU_MEA2_OFF
8414  # WAIT_SEC 40
8415  #
8416  . # < CHECK >
8417  CHECK PCD.PSU_MEA2_ON_OFF          =OFF
8418  CHECK MD1F.MS2_LINK_STAT          =OFF
8419  #
8420  . PCD.ON_OFF_DIS
8421  # WAIT_SEC 40
8422  #
8423  . # < CHECK >
8424  CHECK PCD.ON_OFF_ENA_DIS          =DIS
8425  #
8426  #
8427  #
8428  # QL          Check-Item          Spec          Check
8429  # -----+-----+-----+-----+
8430  # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
8431  #
8432  #
8433  . # *****
8434  . #   END OF PROCEDURE
8435  . # *****
```

文 書 番 号	シート No.	版 数
	75	1

dcsM(MIA CHECK)

```

#!HEAD: dcsM-MIA 2018-03-07 14:48:00 388      45 MM0      DCSM      //
SVT-1c MIA_CHECK
8001  #
8002  # <startTime>2018-03-16T00:00:00Z</startTime>
8003  # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004  # =====
8005  # 1.1. MIA 立上げ
8006  # =====
8007  #
8008  # PCD.ON_OFF_ENA
8009  # WAIT_SEC 40
8010  #
8011  # QL          Check-Item          Spec          check
8012  # -----+-----+-----+-----
8013  # PCS1        <PCD> ON/OFF          ENA           N/A
8014  #
8015  # PCD.PSU_MIA_ON
8016  # WAIT_SEC 40
8017  #
8018  # QL          Check-Item          Spec          check
8019  # -----+-----+-----+-----
8020  # PCS1        <PCD> MIA             ON            N/A
8021  #
8022  # PCD.ON_OFF_DIS
8023  # WAIT_SEC 40
8024  #
8025  # QL          Check-Item          Spec          check
8026  # -----+-----+-----+-----
8027  # PCS1        <PCD> ON/OFF          DIS           N/A
8028  #
8029  #
8030  # MD1F.DPU_SW_ERR_CLR
8031  # MD2F.DPU_SW_ERR_CLR
8032  # WAIT_SEC 40
8033  #
8034  # QL          Check-Item          Spec          check
8035  # -----+-----+-----+-----
8036  # MD1F OBS HK  MD1F.MS4_LINK_ERR      na            N/A
8037  #              MD1F.SLOT_ERR          na            N/A
8038  # MD1F OBS HK  MD2F.SLOT_ERR          na            N/A
8039  #
8040  #
8041  # < MDP MIA SW INIT>
8042  #
8043  # =====
8044  # 1.2. MIA チェック
8045  # =====
8046  #
8047  # WAIT_SEC 180
8048  # MIA status が以下となっていることを確認する。
8049  # なっていなければ待つ（数分程度かかる場合がある）。

```

文 書 番 号	シ-ト No.	版 数
	76	1

```

8050  #
8051  #
8052  # < CHECK >
8053  CHECK MIA.RMAP_ST1          0x00 0xff
8054  CHECK MIA.EDAC_1BIT_ERR      =NON
8055  CHECK MIA.EDAC_2BITS_ERR     =NON
8056  CHECK MIA.EMER1_ST           =NON
8057  CHECK MIA.EMER2_ST           =NON
8058  CHECK MIA.MODE               0
8059  CHECK MIA.SPIN_ID1           0 3
8060  CHECK MIA.SECT0_PHASE_H       0x00
8061  CHECK MIA.SECT0_PHASE_L       0x80
8062  CHECK MIA.CAL_ON_OFF          =OFF
8063  CHECK MIA.CAL_H               0x0b
8064  CHECK MIA.CAL_L               0x05
8065  CHECK MIA.DT_ED               =DIS
8066  CHECK MIA.DEADTIME            0x3c
8067  CHECK MIA.SERIAL_ED           =DIS
8068  CHECK MIA.SYNC                =SNC
8069  CHECK MIA.ACTIVATE_ED         =DIS
8070  CHECK MIA.ACTIVATE_H          0x00
8071  CHECK MIA.ACTIVATE_L          0x00
8072  CHECK MIA.SHUTDOWN_H          0x00
8073  CHECK MIA.SHUTDOWN_L          0x00
8074  CHECK MIA.PHSCAN_ON_OFF       =OFF
8075  CHECK MIA.PHSCAN_MODE_H       0x80
8076  CHECK MIA.PHSCAN_MODE_L       0x10
8077  CHECK MIA.HVSCAN_ED           =DIS
8078  CHECK MIA.HVSCAN_ON_OFF       =OFF
8079  CHECK MIA.HVSCAN_MODE_H       0x98
8080  CHECK MIA.HVSCAN_MODE_M       0xc0
8081  CHECK MIA.HVSCAN_MODE_L       0x03
8082  CHECK MIA.HK_SEL              0x00
8083  CHECK MIA.SELECT_HK1          0x00 0xff
8084  CHECK MIA.SELECT_HK2          0x00 0xff
8085  CHECK MIA.SVG_ED              =DIS
8086  CHECK MIA.SVG_ON_OFF          =OFF
8087  CHECK MIA.SVG                 0 40
8088  CHECK MIA.SVG_TGT             0x80
8089  CHECK MIA.SVG_LVL             0x00
8090  CHECK MIA.SVS_ED              =DIS
8091  CHECK MIA.SVS_ON_OFF          =OFF
8092  CHECK MIA.SVS                 -40 0
8093  CHECK MIA.SVS_TGT             0xB8
8094  CHECK MIA.SVS_LVL             0x00
8095  CHECK MIA.MHV_ED              =DIS
8096  CHECK MIA.MHV_ON_OFF          =OFF
8097  CHECK MIA.MHV                 -40 0
8098  CHECK MIA.MHV_TGT             0xa8
8099  CHECK MIA.MHV_LVL             0x00
8100  CHECK MIA.MCP_I               -2 2
8101  CHECK MIA.SECT1_8RAM          0x00

```

文 書 番 号	シ-ト No.	版 数
	77	1

8102	CHECK MIA. SECT9_16RAM	0x01
8103	CHECK MIA. SECT17_24RAM	0x01
8104	CHECK MIA. SECT25_32RAM	0x00
8105	CHECK MIA. SECT33_40RAM	0x00
8106	CHECK MIA. SECT41_48RAM	0x02
8107	CHECK MIA. SECT49_56RAM	0x02
8108	CHECK MIA. SECT57_64RAM	0x00
8109	CHECK MIA. SAFETY_ED	=DIS
8110	CHECK MIA. SAFETY_ON_OFF	=ON
8111	CHECK MIA. ASIC_T	10 40
8112	CHECK MIA. IF_T	10 40
8113	CHECK MIA. ASIC_SEL	=NON
8114	CHECK MIA. SEL_THRES	0xff
8115	CHECK MIA. ASIC_CONFIG_ERR	=NON
8116	CHECK MIA. ROM_SAVE_ED	=DIS
8117	CHECK MIA. RAM_LOAD_ED	=DIS
8118	CHECK MIA. ASIC_PARAM1	0x00
8119	CHECK MIA. ASIC_PARAM2	0x00
8120	CHECK MIA. ASIC_PARAM3	0x00
8121	CHECK MIA. ASIC_PARAM4	0x00
8122	CHECK MIA. ASIC_CALCH	0x00
8123	CHECK MIA. ASIC_CALLVL	0xff
8124	CHECK MIA. ASIC_THRES	0x14
8125	CHECK MIA. ASIC_OFCH1H	0x00
8126	CHECK MIA. ASIC_OFCH1L	0x00
8127	CHECK MIA. ASIC_OFCH2H	0x00
8128	CHECK MIA. ASIC_OFCH2L	0x00
8129	CHECK MIA. ASIC_OFCH3H	0x00
8130	CHECK MIA. ASIC_OFCH3L	0x00
8131	CHECK MIA. ASIC_OFCH4H	0x00
8132	CHECK MIA. ASIC_OFCH4L	0x00
8133	CHECK MIA. ASIC_DCAL	=DIS
8134	CHECK MIA. ASIC_DIV10	=DIS
8135	CHECK MIA. ASIC_I	40 60
8136	CHECK MIA. S_DATA_MODE_SA	=SNP
8137	CHECK MIA. S_DATA_MODE_SE	=SCI
8138	CHECK MIA. S_DATA_MODE	0 8
8139	CHECK MIA. S_ERNG	0xffffffff
8140	CHECK MIA. S_HK_MON_MHV	=DIS
8141	CHECK MIA. S_MHV_LIM_L	0x00
8142	CHECK MIA. S_MHV_LIM_U	0xff
8143	CHECK MIA. S_HK_MON_SVS	=DIS
8144	CHECK MIA. S_SVS_LIM_L	0x00
8145	CHECK MIA. S_SVS_LIM_U	0xff
8146	CHECK MIA. S_HK_MON_SVG	=DIS
8147	CHECK MIA. S_SVG_LIM_L	0x00
8148	CHECK MIA. S_SVG_LIM_U	0xff
8149	CHECK MIA. S_HK_MON_MCPI	=DIS
8150	CHECK MIA. S_MCPI_LIM_L	0x00
8151	CHECK MIA. S_MCPI_LIM_U	0xff
8152	CHECK MIA. S_HK_MON_ASICT	=DIS
8153	CHECK MIA. S_ASICT_LIM_L	0x00

文 書 番 号	シート No.	版 数
	78	1

8154	CHECK MIA. S_ASICT_LIM_U	0xff	
8155	CHECK MIA. S_HK_MON_IFT	=DIS	
8156	CHECK MIA. S_IFT_LIM_L	0x00	
8157	CHECK MIA. S_IFT_LIM_U	0xff	
8158	CHECK MIA. S_HK_MON_ASIC_SEL	=DIS	
8159	CHECK MIA. CMD_ANS1	0	
8160	CHECK MIA. CMD_ANS2	0	
8161	CHECK MIA. CMD_ANS3	0	
8162	CHECK MIA. CMD_ANS4	0	
8163	CHECK MIA. CMD_CNTR	0	
8164	CHECK MIA. ECMD_CNTR	0	
8165	CHECK MIA. S_CMD_CTR	0	
8166	CHECK MIA. S_CNT1	0x00cae3	
8167	#		
8168	#		
8169	#		
8170	#		
8171	# QL Check-Item	Spec	判 定
8172	# -----+-----+-----+-----		
8173	# MPPE-MIA2 SAFETY	DIS	N/A
8174	#	ON	N/A
8175	#		
8176	#		
8177	MIA. ACTIVAT_ENA		
8178	WAIT_SEC 2		
8179	MIA. ACTIVATE 0x0200		
8180	WAIT_SEC 300		
8181	#		
8182	# < CHECK >		
8183	CHECK MIA. RMAP_ST1	0x00 0xff	
8184	CHECK MIA. EDAC_1BIT_ERR	=NON	
8185	CHECK MIA. EDAC_2BITS_ERR	=NON	
8186	CHECK MIA. EMER1_ST	=NON	
8187	CHECK MIA. EMER2_ST	=NON	
8188	CHECK MIA. MODE	1	
8189	CHECK MIA. SPIN_ID1	0 3	
8190	CHECK MIA. SECT0_PHASE_H	0x00	
8191	CHECK MIA. SECT0_PHASE_L	0x80	
8192	CHECK MIA. CAL_ON_OFF	=OFF	
8193	CHECK MIA. CAL_H	0x0b	
8194	CHECK MIA. CAL_L	0x05	
8195	CHECK MIA. DT_ED	=DIS	
8196	CHECK MIA. DEADTIME	0x3c	
8197	CHECK MIA. SERIAL_ED	=DIS	
8198	CHECK MIA. SYNC	=SNC	
8199	CHECK MIA. ACTIVATE_ED	=DIS	
8200	CHECK MIA. ACTIVATE_H	0x02	
8201	CHECK MIA. ACTIVATE_L	0x00	
8202	CHECK MIA. SHUTDOWN_H	0x00	
8203	CHECK MIA. SHUTDOWN_L	0x00	
8204	CHECK MIA. PHSCAN_ON_OFF	=OFF	
8205	CHECK MIA. PHSCAN_MODE_H	0x80	

文 書 番 号	シ-ト No.	版 数
	79	1

8206	CHECK MIA. PHSCAN_MODE_L	0x10
8207	CHECK MIA. HVSCAN_ED	=DIS
8208	CHECK MIA. HVSCAN_ON_OFF	=OFF
8209	CHECK MIA. HVSCAN_MODE_H	0x98
8210	CHECK MIA. HVSCAN_MODE_M	0xc0
8211	CHECK MIA. HVSCAN_MODE_L	0x03
8212	CHECK MIA. HK_SEL	0x18
8213	CHECK MIA. SELECT_HK1	0x00 0xff
8214	CHECK MIA. SELECT_HK2	0x00 0xff
8215	CHECK MIA. SVG_ED	=DIS
8216	CHECK MIA. SVG_ON_OFF	=ON
8217	CHECK MIA. SVG	0 20
8218	CHECK MIA. SVG_TGT	0x80
8219	CHECK MIA. SVG_LVL	0x80
8220	CHECK MIA. SVS_ED	=DIS
8221	CHECK MIA. SVS_ON_OFF	=ON
8222	CHECK MIA. SVS	-20 0
8223	CHECK MIA. SVS_TGT	0xB8
8224	CHECK MIA. SVS_LVL	0xB8
8225	CHECK MIA. MHV_ED	=DIS
8226	CHECK MIA. MHV_ON_OFF	=ON
8227	CHECK MIA. MHV	-20 0
8228	CHECK MIA. MHV_TGT	0xa8
8229	CHECK MIA. MHV_LVL	0xa8
8230	CHECK MIA. MCP_I	-2 2
8231	CHECK MIA. SECT1_8RAM	0x03
8232	CHECK MIA. SECT9_16RAM	0x03
8233	CHECK MIA. SECT17_24RAM	0x03
8234	CHECK MIA. SECT25_32RAM	0x03
8235	CHECK MIA. SECT33_40RAM	0x03
8236	CHECK MIA. SECT41_48RAM	0x03
8237	CHECK MIA. SECT49_56RAM	0x03
8238	CHECK MIA. SECT57_64RAM	0x03
8239	CHECK MIA. SAFETY_ED	=DIS
8240	CHECK MIA. SAFETY_ON_OFF	=ON
8241	CHECK MIA. ASIC_T	10 40
8242	CHECK MIA. IF_T	10 40
8243	CHECK MIA. CMD_ANS1	0x54
8244	CHECK MIA. CMD_ANS2	0x00
8245	CHECK MIA. CMD_ANS3	0x02
8246	CHECK MIA. CMD_ANS4	0x00
8247	CHECK MIA. ECMD_CNTR	0
8248	CHECK MIA. S_CMD_CTR	0
8249	CHECK MIA. S_CNT1	0x00cae3
8250	CHECK MIA. ASIC_SEL	=NON
8251	CHECK MIA. SEL_THRES	0xff
8252	CHECK MIA. ASIC_CONFIG_ERR	=NON
8253	CHECK MIA. ROM_SAVE_ED	=DIS
8254	CHECK MIA. RAM_LOAD_ED	=DIS
8255	CHECK MIA. ASIC_CALCH	0x00
8256	CHECK MIA. ASIC_CALLVL	0xff
8257	CHECK MIA. ASIC_THRES	0x14

文 書 番 号	シート No.	版 数
	80	1

8258	CHECK MIA.ASIC_OFCH1H	0x00	
8259	CHECK MIA.ASIC_OFCH1L	0x00	
8260	CHECK MIA.ASIC_OFCH2H	0x00	
8261	CHECK MIA.ASIC_OFCH2L	0x00	
8262	CHECK MIA.ASIC_OFCH3H	0x00	
8263	CHECK MIA.ASIC_OFCH3L	0x00	
8264	CHECK MIA.ASIC_OFCH4H	0x00	
8265	CHECK MIA.ASIC_OFCH4L	0x00	
8266	CHECK MIA.ASIC_DCAL	=DIS	
8267	CHECK MIA.ASIC_DIV10	=DIS	
8268	CHECK MIA.S_DATA_MODE_SA	=SNP	
8269	CHECK MIA.S_DATA_MODE_SE	=SCI	
8270	CHECK MIA.S_DATA_MODE	0 7	
8271	CHECK MIA.S_ERNG	0xffffffff	
8272	CHECK MIA.S_HK_MON_MHV	=DIS	
8273	CHECK MIA.S_MHV_LIM_L	0x00	
8274	CHECK MIA.S_MHV_LIM_U	0xff	
8275	CHECK MIA.S_HK_MON_SVS	=DIS	
8276	CHECK MIA.S_SVS_LIM_L	0x00	
8277	CHECK MIA.S_SVS_LIM_U	0xff	
8278	CHECK MIA.S_HK_MON_SVG	=DIS	
8279	CHECK MIA.S_SVG_LIM_L	0x00	
8280	CHECK MIA.S_SVG_LIM_U	0xff	
8281	CHECK MIA.S_HK_MON_MCPI	=DIS	
8282	CHECK MIA.S_MCPI_LIM_L	0x00	
8283	CHECK MIA.S_MCPI_LIM_U	0xff	
8284	CHECK MIA.S_HK_MON_ASICT	=DIS	
8285	CHECK MIA.S_ASICT_LIM_L	0x00	
8286	CHECK MIA.S_ASICT_LIM_U	0xff	
8287	CHECK MIA.S_HK_MON_IFT	=DIS	
8288	CHECK MIA.S_IFT_LIM_L	0x00	
8289	CHECK MIA.S_IFT_LIM_U	0xff	
8290	CHECK MIA.S_HK_MON_ASIC_SEL	=DIS	
8291	#		
8292	#		
8293	# HK が返ってくるまで待つて確認し終わってから次に進む		
8294	#		
8295	# <CAL 1.953kHz CH0-CH64 every spin>		
8296	MIA.CAL_ON		
8297	WAIT_SEC 300		
8298	#		
8299	# QL Check-Item	Spec	check
8300	# -----+-----+-----+-----		
8301	# MPPE-MIA2 CAL	ON	N/A
8302	#	0x0B05	N/A
8303	#		
8304	#		
8305	MIA.MODE_SET 0x0100		
8306	WAIT_SEC 300		
8307	#		
8308	# QL Check-Item	Spec	check
8309	# -----+-----+-----+-----		

文 書 番 号	シ-ト No.	版 数
	81	1

8310	# MPPE-MIA2	MODE	1	N/A
8311	#			
8312	. MIA.MODE_SET	0x0200		
8313	WAIT_SEC	300		
8314	#			
8315	# QL	Check-Item	Spec	check
8316	#	-----+-----+-----+-----		
8317	# MPPE-MIA2	MODE	2	N/A
8318	#			
8319	#			
8320	. MIA.MODE_SET	0x0000		
8321	WAIT_SEC	40		
8322	#			
8323	# QL	Check-Item	Spec	check
8324	#	-----+-----+-----+-----		
8325	# MPPE-MIA2	MODE	0	N/A
8326	#			
8327	. MIA.CAL_OFF			
8328	WAIT_SEC	40		
8329	#			
8330	# QL	Check-Item	Spec	check
8331	#	-----+-----+-----+-----		
8332	# MPPE-MIA2	CAL	OFF	N/A
8333	#			
8334	#			
8335	#			
8336	#	=====		
8337	. #	1.3. MIA 立下げ		
8338	#	=====		
8339	#			
8340	. MIA.SHUTDOWN	0x0000		
8341	WAIT_SEC	40		
8342	#			
8343	# QL	Check-Item	Spec	check
8344	#	-----+-----+-----+-----		
8345	# MPPE-MIA2	SVG	DIS	N/A
8346	#		OFF	N/A
8347	#	MON	0.0 - 40.0	N/A
8348	#	TGT	0x00	N/A
8349	#	LVL	0x00	N/A
8350	#	SVS	DIS	N/A
8351	#		OFF	N/A
8352	#	MON	0.0 - -40.0	N/A
8353	#	TGT	0x00	N/A
8354	#	LVL	0x00	N/A
8355	#	MHV	DIS	N/A
8356	#		OFF	N/A
8357	#	MON	0.0 - -40.0	N/A
8358	#	TGT	0x00	N/A
8359	#	LVL	0x00	N/A
8360	#	MCP_I	-2.0 - 2.0	N/A
8361	#			

```
8362 . MD1F.MIA_CLCT_STOP
8363 . WAIT_SEC 40
8364 . # < CHECK >
8365 . #CHECK MD1F.MS4_DAT_CLCT =OFF
8366 . #
8367 . PCD.ON_OFF_ENA
8368 . WAIT_SEC 40
8369 . # QL Check-Item Spec check
8370 . # -----+-----+-----+-----
8371 . # PCS1 PCD.ON/OFF_ENA/DIS ENA N/A
8372 . #
8373 . # # MIA_OFF
8374 . PCD.PSU_MIA_OFF
8375 . WAIT_SEC 40
8376 . # QL Check-Item Spec check
8377 . # -----+-----+-----+-----
8378 . # PCS1 PCD.PSU_MIA_ON_OFF OFF N/A
8379 . #
8380 . PCD.ON_OFF_DIS
8381 . WAIT_SEC 40
8382 . # QL Check-Item Spec check
8383 . # -----+-----+-----+-----
8384 . # PCS1 PCD.ON/OFF_ENA/DIS DIS N/A
8385 . #
8386 . # *****
8387 . # END OF PROCEDURE
8388 . # *****
```

文 書 番 号	シート No.	版 数
	83	1

dcsn(TLMODE 11)

```
#!HEAD: dcsn-SET_TLM-MODE11 2018-03-07 14:51:00 26 45 MMO DCSM
// SVT-1c TLMODE_11
8001 . #
8002 . # <startTime>2018-03-16T00:00:00Z</startTime>
8003 . # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004 . # =====
8005 . # 1. SET TLM MODE 11 【interactive】
8006 . # =====
8007 . #
8008 . # # MSASI は、全帯域の 7/8 を Mission Data に割り当てないと Misson Data を出力できないの
      . # で、
8009 . # # テレメトリのモニタができなくなるため、Mission Data は出力しないモードとする。
8010 . # # 4.11 の Cruise Mode ミッションデータ確認時のみ Mission Data 出力するモードとする。
8011 . #
8012 . DH.MC_EACH_EXE 0x0037 # 1553B TLM mode 11 (SYS HK 1/8, BUS User HK 1/4, DUMP 1/16,
      . # # DR User HK 1/16, MDP1/2 User HK 1/8, Mission User HK 1/4)
8013 . #
8014 . #: << MACRO >> 内容について以下に示す(DH.DUMMY は省略)
8015 . #: 0x00 DH.DMP_PERIOD 0x04
8016 . #: 0x02 ACS.ACS_PKT_FMT_1
8017 . #: 0x05 DH.OPE_MODE 0x0C 0x3B 0x2D 0x03 0x00000000000000000000000000000000
8018 . #: 0x06 DH.TLMFMT1553B_CHG 0x0B
8019 . #
8020 . WAIT_SEC 40
8021 . CHECK DH.DMP_PERIOD =SEC_16
8022 . CHECK DH.CATEGORY_TBL_NO 0x2D
8023 . CHECK DH.DWNLNK_RATE =BPS16384
8024 . CHECK DH.TLM1553BFMTNO 0x0B
8025 . CHECK ACS.F_ACS_PKT_FMT =ACSHK1
8026 . #
```

文 書 番 号	シ-ト No.	版 数
	84	1

dcsM(MDP OPE CRUISE 設定)

```
#!HEAD: dcsM-MD2U_OPE_CRUISE 2018-03-07 14:10:00 14 45 MMO DCSM
// SVT-1c MDDPU2_OPE_CRUISE
8001 #
8002 # <startTime>2018-03-16T00:00:00Z</startTime>
8003 # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004 # =====
8005 . # 1. SET MD2U Cruise mode 【interactive】
8006 # =====
8007 . #
8008 . MD2U_OPE_CRUISE
8009 #
8010 # QL          Check-Item          Spec          Judgment
8011 # -----+-----+-----+-----+
8012 # MD2U          *****CHECK by MDP-CORE*****
8013 #          U_TIm_OpeMode          CHK          N/A
8014 #
```

文 書 番 号	シート No.	版 数
	85	1

dcsM(MSASI CHECK)

```

#!HEAD: dcsM-MSASI 2018-03-07 14:56:00 332      45 MMO      DCSM      //
SVT-1c MSASI_CHECK
8001  #
8002  # =====
8003  . # 1.1. Initial power ON 【interactive】
8004  # =====
8005  #
8006  # 本手順は interactive operations にて実施する。
8007  # 実際の運用時には往復 180 秒程度の電波伝搬遅延がかかることを考慮して
8008  # 手順を進めること。
8009  . #
8010  . PCD.ON_OFF_ENA
8011  #
8012  . # < CHECK >
8013  CHECK PCD.ON_OFF_ENA_DIS      =ENA
8014  . #
8015  . PCD.MSASI_ON
8016  #
8017  . # < CHECK >
8018  CHECK PCD.MSASI_ON_OFF      =ON
8019  #
8020  . MD1F.DPU_SW_ERR_CLR
8021  #
8022  # QL      Check-Item      Spec      Judgment
8023  # -----+-----+-----+-----
8024  # MD1F_OBS << DPU1 S/W HK >>
8025  #      SWCMD_CODE      0x030E01**      N/A
8026  #      SWCMD_CNT      (increment)      N/A
8027  #      SWREJ_CNT      0      N/A
8028  #      SWREJ_CODE      0x00      N/A
8029  . #
8030  . MD2F.DPU_SW_ERR_CLR
8031  #
8032  # QL      Check-Item      Spec      Judgment
8033  # -----+-----+-----+-----
8034  # MD2F_OBS << DPU2 S/W HK >>
8035  #      SWCMD_CODE      0x030E01**      N/A
8036  #      SWCMD_CNT      (increment)      N/A
8037  #      SWREJ_CNT      0      N/A
8038  #      SWREJ_CODE      0x00      N/A
8039  #
8040  # MSASI 電源供給直後のステータスを QL モニタにてチェックする。
8041  #
8042  . # < CHECK >
8043  CHECK MSAS.RMAP_STATUS      0
8044  CHECK MSAS.CMD_CNT      0
8045  CHECK MSAS.MSASI_ON_OFF      =RUN
8046  CHECK MSAS.LLM_STATUS      =OK
8047  CHECK MSAS.SP_STATUS      =OK
8048  CHECK MSAS.LINK_STATUS      =OK
8049  CHECK MSAS.PARTITION_AB      =A

```


文 書 番 号	シート No.	版 数
	86	1

8050	CHECK MSAS. PACKET_CNT	0
8051	CHECK MSAS. OBS_MODE	=AUT
8052	CHECK MSAS. AUTOOBS_STATUS	=STP
8053	CHECK MSAS. CMOS_ON_OFF	=ON
8054	CHECK MSAS. CMOS_RO	=STP
8055	CHECK MSAS. CMOS_INPUT	=IMG
8056	CHECK MSAS. G_ON_OFF	=OFF
8057	CHECK MSAS. G_CRC	=OK
8058	CHECK MSAS. G_TLM_ANGLE	0
8059	CHECK MSAS. G_STEP	0
8060	CHECK MSAS. HV_ON_OFF	=OFF
8061	CHECK MSAS. HV_ENA_DIS	=DIS
8062	CHECK MSAS. HV_MONI_N4K	0
8063	CHECK MSAS. HV_MONI_P6K	0
8064	CHECK MSAS. HT_CTRL_ON_OFF	=OFF
8065	CHECK MSAS. HT_ON_OFF	=OFF
8066	CHECK MSAS. CENTER_CTS	0
8067	CHECK MSAS. DATA_TYPE	=DUM
8068	CHECK MSAS. SEQ_PACKET_CNT	0
8069	CHECK MSAS. SEQ_CNT	0
8070	CHECK MSAS. MSASI_SP_ENA_DIS	=ENA
8071	CHECK MSAS. CHANGE_DUM_FLG	=OFF
8072	CHECK MSAS. SP_AVG_TIME	8
8073	CHECK MSAS. SHOT_TIMING	0
8074	CHECK MSAS. SHOT_NUMBER	256
8075	CHECK MSAS. INTEG_SPIN	0
8076	CHECK MSAS. LINE_NUMBER	21
8077	CHECK MSAS. TOTAL_CNT	0
8078	CHECK MSAS. CMOS_GAIN	=X1
8079	CHECK MSAS. CMOS_BIN_MODE	=XYBIN
8080	CHECK MSAS. CMOS_EXP_TIME	2000
8081	CHECK MSAS. CMOS_ROIX_START	494
8082	CHECK MSAS. CMOS_ROIY_START	480
8083	CHECK MSAS. CMOS_ROIX_NUMBER	10
8084	CHECK MSAS. CMOS_ROIY_NUMBER	10
8085	CHECK MSAS. CMOS_ROIX_STEP	4
8086	CHECK MSAS. CMOS_ROIY_STEP	4
8087	CHECK MSAS. CMOS_OFFSET	111
8088	CHECK MSAS. COM_SENT_TO_G	0
8089	CHECK MSAS. G_TLM_CMD	0
8090	CHECK MSAS. G_TLM_P15V	=NG
8091	CHECK MSAS. G_TLM_M15V	=NG
8092	CHECK MSAS. G_TLM_ADC	=NG
8093	CHECK MSAS. G_TLM_ANG_SENS	=NG
8094	CHECK MSAS. G_TLM_PS_SENS1	=NG
8095	CHECK MSAS. G_TLM_PS_SENS2	=NG
8096	CHECK MSAS. G_TLM_CMD_RES	=ERR
8097	CHECK MSAS. HV_AUTO_N4K_L	0
8098	CHECK MSAS. HV_AUTO_P6K_L	0
8099	CHECK MSAS. HV_LIM_N4K	255
8100	CHECK MSAS. HV_LIM_P6K	255
8101	CHECK MSAS. HV_AUTO_STEP	100

文 書 番 号	シート No.	版 数
	87	1

```

8102 CHECK MSAS. HT_MODE =AUT
8103 CHECK MSAS. HT_STATUS =OUT
8104 CHECK MSAS. OBS_CHECK_STATUS 0
8105 CHECK MSAS. P15V_V_L 12 16
8106 CHECK MSAS. P15V_I_L 0.01 0.3
8107 CHECK MSAS. M15V_V_L 12 16
8108 CHECK MSAS. M15V_I_L 0.01 0.3
8109 CHECK MSAS. P5V_V_L 4.5 5.2
8110 CHECK MSAS. P5V_I_L 0.1 1
8111 CHECK MSAS. TEMP1_L 15 40
8112 CHECK MSAS. TEMP2_L 15 40
8113 CHECK MSAS. TEMP3_L 15 40
8114 CHECK MSAS. TEMP4_L 15 40
8115 CHECK MSAS. TEMP5_L 15 40
8116 CHECK MSAS. TEMP6_L 15 40
8117 CHECK MSAS. TEMP7_L 15 40
8118 CHECK MSAS. TEMP8_L 15 40
8119 #
8120 . # 以下は MSASI 側で確認
8121 . # QL Check-Item Spec Check
8122 . # -----+-----+-----+-----
8123 . # MSASI SP_CNT spin ごとに1カウントアップ N/A
8124 . # SP_TIME_USED spin ごとに1カウントアップ N/A
8125 . # TIME_CODE 変化することを確認 N/A
8126 . #
8127 . #
8128 . PCD. ON_OFF_DIS
8129 . #
8130 . # < CHECK >
8131 CHECK PCD. ON_OFF_ENA_DIS =DIS
8132 . #
8133 . # QL Check-Item Spec Check
8134 . # -----+-----+-----+-----
8135 . # CMD 出力装置 Message STATUS CHECK 結果 OK G N
8136 . #
8137 . # =====
8138 . # 1.2. OBSERVATION SEQUENCE #2 (with SHORT MACRO) 【interactive】
8139 . # =====
8140 . #
8141 . # *** ショートマクロ MSASI_GENERAL_SETUP 実行 ***
8142 . #
8143 . # コマンド実行前のコマンドカウンタ値を確認する。
8144 . # QL Check-Item Spec Check
8145 . # -----+-----+-----+-----
8146 . # MSASI
8147 . # SYSTEM CMD_CNT - N/A
8148 . #
8149 . # < CHECK >
8150 LET MSASI_CNT = ( {MSAS. CMD_CNT} + 8 ) & 0xFF
8151 . #
8152 . DH. MSASI_GENERAL_SETUP # DH. MC_EACH_EXE 0x00F0
8153 . #

```

文 書 番 号	シート No.	版 数
	88	1

```

8154 # 下記コマンドが実行される
8155 # MSAS.SET_OBS_MODE 0x00
8156 # MSAS.SET_SHOT_TIMING 0x000000
8157 # MSAS.SET_SHOT_NUMBER 0x000100
8158 # MSAS.SELECT_PARTITION 0x00
8159 # MSAS.SET_INTEG_SPIN 0x0000
8160 # MSAS.SET_LINE_NUMBER 0x15
8161 # MSAS.SET_SP_AVG_TIME 0x08
8162 # MSAS.MSASI_SP_ENA
8163 #
8164 # < CHECK >
8165 SYSTEM test {MSAS.CMD_CNT} = {MSASI_CNT}
8166 CHECK MSAS.PARTITION_AB =A
8167 CHECK MSAS.MSASI_SP_ENA_DIS =ENA
8168 CHECK MSAS.OBS_MODE =AUT
8169 CHECK MSAS.SP_AVG_TIME 8
8170 CHECK MSAS.SHOT_TIMING 0
8171 CHECK MSAS.SHOT_NUMBER 256
8172 CHECK MSAS.INTEG_SPIN 0
8173 CHECK MSAS.LINE_NUMBER 21
8174 #
8175 # *** ショートマクロ MSASI_CMOS_SETUP 実行 ***
8176 #
8177 # < CHECK >
8178 LET MSASI_CNT = ({MSAS.CMD_CNT} + 10 ) & 0xFF
8179 #
8180 DH.MSASI_CMOS_SETUP # DH.MC_EACH_EXE 0x00F1
8181 #
8182 # 下記コマンドが実行される
8183 # MSAS.CMOS_SET_GAIN 0x00
8184 # MSAS.CMOS_SET_BIN 0x02
8185 # MSAS.CMOS_EXP_TIME 0x0007D0
8186 # MSAS.CMOS_ROIX_START 0x01F4
8187 # MSAS.CMOS_ROIY_START 0x01EA
8188 # MSAS.CMOS_ROIX_NUMBER 0x000A
8189 # MSAS.CMOS_ROIY_NUMBER 0x000A
8190 # MSAS.CMOS_ROIX_STEP 0x04
8191 # MSAS.CMOS_ROIY_STEP 0x04
8192 # MSAS.CMOS_SET_OFFSET 0x71
8193 #
8194 # < CHECK >
8195 CHECK MSAS.CMOS_GAIN =X1
8196 CHECK MSAS.CMOS_INPUT =IMG
8197 CHECK MSAS.CMOS_BIN_MODE =XYBIN
8198 CHECK MSAS.CMOS_EXP_TIME 2000
8199 CHECK MSAS.CMOS_ROIX_START 500
8200 CHECK MSAS.CMOS_ROIY_START 490
8201 CHECK MSAS.CMOS_ROIX_NUMBER 10
8202 CHECK MSAS.CMOS_ROIY_NUMBER 10
8203 CHECK MSAS.CMOS_ROIX_STEP 4
8204 CHECK MSAS.CMOS_ROIY_STEP 4
8205 CHECK MSAS.CMOS_OFFSET 113

```

文 書 番 号	シート No.	版 数
	89	1

```

8206 SYSTEM test {MSAS.CMD_CNT} = {MSASI_CNT}
8207 #
8208 . MSAS.G_INIT
8209 #
8210 . # < CHECK >
8211 CHECK MSAS.G_ON_OFF =ON
8212 CHECK MSAS.G_STEP 13
8213 CHECK MSAS.G_TLM_CMD 0x01
8214 CHECK MSAS.G_TLM_ANGLE 33000 33800
8215 CHECK MSAS.G_TLM_CMD_RES =CMD_EXE =CAL_REQ
8216 CHECK MSAS.G_P5V 4.54 4.94 #4.74
8217 CHECK MSAS.P15V_V_L 14.1 14.5 #14.3
8218 CHECK MSAS.P15V_I_L 0.04 0.1 #0.074
8219 CHECK MSAS.M15V_V_L 14.1 14.5 #14.3
8220 CHECK MSAS.M15V_I_L 0.07 0.13 #0.107
8221 CHECK MSAS.P5V_V_L 4.7 5.2 #4.96
8222 CHECK MSAS.P5V_I_L 0.4 0.8 #0.595
8223 #
8224 # *** ショートマクロ MSASI_OBS_START 実行 ***
8225 . #
8226 . MSAS.SET_SHOT_TIMING 0x0002F0
8227 #
8228 . # < CHECK >
8229 CHECK MSAS.SHOT_TIMING 752
8230 #
8231 . # !! 以下の手順で MSASI ミッションデータを出力するため、
8232 # システム側テレメモードの変更により HK: 1/8, Mission: 7/8 へ変更 !!
8233 #
8234 # Set SYSTEM 1553B TLM MODE 5
8235 # <Period>
8236 # SYS HK: 8 sec
8237 # (Mission data: 7 kbps)
8238 #
8239 . DH.MC_EACH_EXE 0x0031 # 1553B TLM mode 5 (SYS HK 1/8, Mission data 7/8),
8240 #
8241 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
8242 #: 0x00 DH.DMP_PERIOD 0x04
8243 #: 0x02 ACS.ACS_PKT_FMT_1
8244 #: 0x05 DH.OPE_MODE 0x0C 0x42 0x27 0x00 0x00000000000000000000000000000000
8245 #: 0x06 DH.TLMFMT1553B_CHG 0x05
8246 #
8247 . CHECK DH.CATEGORY_TBL_NO 0x27
8248 CHECK DH.DWNLNK_RATE =BPS16384
8249 CHECK DH.TLM1553BFMTNO 0x05
8250 CHECK ACS.F_ACS_PKT_FMT =ACSHK1
8251 #
8252 . # < CHECK >
8253 LET MSASI_CNT = ({MSAS.CMD_CNT} + 1 ) & 0xFF
8254 #
8255 . DH.MSASI_OBS_START # DH.MC_EACH_EXE 0x00F4
8256 #
8257 # 下記コマンドが実行される

```

文 書 番 号	シート No.	版 数
	90	1

```

8258 # MD2U. BUF_CLCT_MSAS 0x22
8259 # DH. DUMMY (×14)
8260 # MSAS. AUTO_OBS_START
8261 #
8262 # < CHECK >
8263 SYSTEM test {MSAS.CMD_CNT} = {MSASI_CNT}
8264 CHECK MSAS. AUTOOBS_STATUS =RUN
8265 #
8266 # *** PI の判断で次の手順に進む ***
8267 #
8268 # !! MDP から全ミッションデータが出力されるまで待つ !!
8269 # *必要時間見積り : 168 packets / (7.5 packets/min) = ~23 min
8270 #
8271 # # 1553B 通信ををテレメトリ出力に変更 (TLM mode 11)
8272 # # 1553B TLM mode 11 (SYS HK 1/8, BUS User HK 1/4, DUMP 1/16,
8273 # # DR User HK 1/16, MDP1/2 User HK 1/8, Mission User HK 1/4)
8274 #
8275 # Set SYSTEM 1553B TLM MODE 11
8276 # <Period>
8277 # SYS HK: 8 sec
8278 # BUS User HK: 4 sec
8279 # DUMP: 16 sec
8280 # DR User HK: 16 sec
8281 # MDP1 User HK: 8 sec
8282 # MDP2 User HK: 8 sec
8283 # Mission User HK: 4 sec
8284 #
8285 . DH.MC_EACH_EXE 0x0037 # 1553B TLM mode 11 (SYS HK 1/8, BUS User HK 1/4, DUMP 1/16,
8286 #
8287 # # DR User HK 1/16, MDP1/2 User HK 1/8, Mission User HK 1/4)
8288 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
8289 #: 0x00 DH. DMP_PERIOD 0x04
8290 #: 0x01 DH. DR_PTLOGCYC_SET 0x00
8291 #: 0x02 ACS. ACS_PKT_FMT_1
8292 #: 0x05 DH. OPE_MODE 0x0C 0x3B 0x2D 0x03 0x00000000000000000000000000000000
8293 #: 0x06 DH. TLMFMT1553B_CHG 0x0B
8294 #
8295 . CHECK DH. DMP_PERIOD =SEC_16
8296 CHECK DH. CATEGORY_TBL_NO 0x2D
8297 CHECK DH. DWNLNK_RATE =BPS16384
8298 CHECK DH. TLM1553BFMTNO 0x0B
8299 CHECK ACS. F_ACS_PKT_FMT =ACSHK1
8300 #
8301 # !! MDP にたまった MSASI データの送出 !!
8302 #
8303 # MDP に MSASI mission data が残っている場合は、
8304 # 必要があれば下記コマンドを計画外で送信してデータをすべて送出する
8305 #
8306 # MD2U. OPE_NRM
8307 #
8308 # =====
8309 # 1.3. MSASI 立ち下げ

```

文 書 番 号	シ-ト No.	版 数
	91	1

```
8310  # =====
8311  . #
8312  . PCD.ON_OFF_ENA
8313  #
8314  . # < CHECK >_
8315  CHECK PCD.ON_OFF_ENA_DIS          =ENA
8316  #
8317  . PCD.MSASI_OFF
8318  #
8319  . # < CHECK >
8320  CHECK PCD.MSASI_ON_OFF            =OFF
8321  #
8322  . PCD.ON_OFF_DIS
8323  #
8324  . # < CHECK >
8325  CHECK PCD.ON_OFF_ENA_DIS          =DIS
8326  #
8327  #
8328  . # QL          Check-Item          Spec          Check
8329  # -----+-----+-----+-----+
8330  # CMD 出力装置  Message          STATUS CHECK 結果 OK          G  N
8331  #
8332  #
```

文 書 番 号	シート No.	版 数
TPP-7S8238-42K	92	1

dcsM(TLMMODE 11)

```
#!HEAD: dcsM-SET_TLM-MODE11 2018-03-07 14:51:00 26 45 MMO DCSM
// SVT-1c TLMMODE_11
8001 . #
8002 . # <startTime>2018-03-16T00:00:00Z</startTime>
8003 . # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004 . # =====
8005 . # 1. SET TLM MODE 11 【interactive】
8006 . # =====
8007 . #
8008 . # # MSASI は、全帯域の 7/8 を Mission Data に割り当てないと Misson Data を出力できないの
      で、
8009 . # # テレメトリのモニタができなくなるため、Mission Data は出力しないモードとする。
8010 . # # 4.11 の Cruise Mode ミッションデータ確認時のみ Mission Data 出力するモードとする。
8011 . #
8012 . DH.MC_EACH_EXE 0x0037 # 1553B TLM mode 11 (SYS HK 1/8, BUS User HK 1/4, DUMP 1/16,
8013 . # # DR User HK 1/16, MDP1/2 User HK 1/8, Mission User HK 1/4)
8014 . #: << MACRO >> 内容について以下に示す(DH.DUMMY は省略)
8015 . #: 0x00 DH.DMP_PERIOD 0x04
8016 . #: 0x02 ACS.ACS_PKT_FMT_1
8017 . #: 0x05 DH.OPE_MODE 0x0C 0x3B 0x2D 0x03 0x00000000000000000000000000000000
8018 . #: 0x06 DH.TLMFMT1553B_CHG 0x0B
8019 . #
8020 . WAIT_SEC 40
8021 . CHECK DH.DMP_PERIOD =SEC_16
8022 . CHECK DH.CATEGORY_TBL_NO 0x2D
8023 . CHECK DH.DWNLNK_RATE =BPS16384
8024 . CHECK DH.TLM1553BFMTNO 0x0B
8025 . CHECK ACS.F_ACS_PKT_FMT =ACSHK1
8026 . #
```

文 書 番 号	シ-ト No.	版 数
	93	1

dcsM(MDP OPE NRM 設定)

```
#!HEAD: dcsM-MD2U_OPE_NORMAL 2018-03-07 14:15:00 14 45 MMO DCSM
// SVT-1c MDDPU2 OPE NRM
8001 #
8002 # <startTime>2018-03-16T00:00:00Z</startTime>
8003 # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004 # =====
8005 . # 1. SET MD2U Normal mode 【interactive】
8006 # =====
8007 . #
8008 . MD2U_OPE_NRM
8009 #
8010 # QL          Check-Item          Spec          Judgment
8011 # -----+-----+-----+-----+
8012 # MD2U          *****CHECK by MDP-CORE*****
8013 #              U_TIm_OpeMode          nrm          N/A
8014 #
```


文 書 番 号	シート No.	版 数
TPP-7S8238-42K	94	1

dcsn(TLMODE 10)

```
#!HEAD: dcsn-SET_TLM-MODE10 2018-03-07 14:58:00 26 45 MMO DCSM
// SVT-1c TLMODE10
8001 #
8002 # <startTime>2018-03-16T00:00:00Z</startTime>
8003 # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004 # =====
8005 . # 1. SET TLM MODE 10 【interactive】
8006 # =====
8007 . #
8008 . # # Mission Data を 1553B 通信の半分の帯域 (約 4kbps) で出力するモードとする
8009 . DH.MC_EACH_EXE 0x0036 # 1553B TLM mode 10 (SYS HK 1/16, BUS User HK 1/8, DUMP 1/16,
8010 # # MDP User HK 1/8, Mission User HK 1/8, Mission Data 1/2)
8011 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
8012 #: 0x00 DH. DMP_PERIOD 0x04
8013 #: 0x01 DH. DR_PTLOGCYC_SET 0x00
8014 #: 0x02 ACS. ACS_PKT_FMT_1
8015 #: 0x05 DH. OPE_MODE 0x0C 0x3E 0x2C 0x04 0x00000000000000000000000000000000
8016 #: 0x06 DH. TLMFMT1553B_CHG 0x0A
8017 #
8018 WAIT_SEC 40
8019 #
8020 CHECK DH. DMP_PERIOD =SEC_16
8021 CHECK DH. CATEGORY_TBL_NO 0x2C
8022 CHECK DH. DWNLNK_RATE =BPS16384
8023 CHECK DH. TLM1553BFMTNO 0x0A
8024 CHECK ACS. F_ACS_PKT_FMT =ACSHK1
8025 #
8026 #
```

文 書 番 号	シート No.	版 数
	95	1

dcsM(MSA CHECK)

```

#!HEAD: dcsM-MSA 2018-03-07 15:03:00 332      45 MMO      DCSM      //
SVT-1c MSA_CHECK
8001  #
8002  # <startTime>2018-03-16T00:00:00Z</startTime>
8003  # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004  # *****
8005  # 1. MSA 初期 CO
8006  # *****
8007  # =====
8008  # 1.1. MSA 立上げ
8009  # =====
8010  #
8011  # PCD.ON_OFF_ENA
8012  # WAIT_SEC 40
8013  #
8014  # QL      Check-Item      Spec      check
8015  # -----+-----+-----+-----
8016  # PCS1      <PCD> ON/OFF      ENA      N/A
8017  #
8018  # PCD.PSU_MSA_ON
8019  # WAIT_SEC 40
8020  #
8021  # QL      Check-Item      Spec      check
8022  # -----+-----+-----+-----
8023  # PCS1      <PCD> MSA      ON      N/A
8024  #
8025  # PCD.ON_OFF_DIS
8026  # WAIT_SEC 40
8027  #
8028  # QL      Check-Item      Spec      check
8029  # -----+-----+-----+-----
8030  # PCS1      <PCD> ON/OFF      DIS      N/A
8031  #
8032  # =====
8033  # 1.2. N/A(DR は使用しない)
8034  # =====
8035  # =====
8036  # 1.3. MSA TLM/CMD 機能確認
8037  # =====
8038  # -----
8039  # 1.3.1. MSA CMD/TLM チェック
8040  # -----
8041  #
8042  # WAIT_SEC 120
8043  # WAIT_SEC 300      #Flight only
8044  #
8045  # MSA status が以下となっていることを確認する。
8046  # なっていなければ待つ（数分程度かかる場合がある）。
8047  #
8048  # QL      Check-Item      Spec      check
8049  # -----+-----+-----+-----

```

文 書 番 号	シート No.	版 数
	96	1

8050	#	MSA ALL HK	CPU_SW	40	N/A
8051	#				
8052	#				
8053	#		HV_REF_HV1	(参考値 0x040)	N/A
8054	#		HV_REF_HV2	(参考値 0x049)	N/A
8055	#		HV_REF_VHV1	(参考値 0x117)	N/A
8056	#		HV_REF_VHV2	(参考値 0x17C)	N/A
8057	#		HV_REF_VHV3	(参考値 0x115)	N/A
8058	#		HV_REF_VHV4	(参考値 0x03E)	N/A
8059	#		HV_REF_OL	(参考値 0x020)	N/A
8060	#		HV_DAC_VHV3	000	N/A
8061	#		HV_DAC_HV1	00	N/A
8062	#		HV_DAC_HV2	00	N/A
8063	#		HV_DAC_VHV1	00	N/A
8064	#		HV_DAC_VHV2	00	N/A
8065	#		HV_DAC_VHV4	00	N/A
8066	#		HV_DAC_AMP1	00	N/A
8067	#		HV_DAC_AMP2	00	N/A
8068	#		HV_DAC_AMP3	00	N/A
8069	#				
8070	#		ADC_HV_VHV1	000	N/A
8071	#		ADC_HV_VHV2	(参考値 0x008)	N/A
8072	#		ADC_HV_VHV3	000	N/A
8073	#		ADC_HV_VHV4	000	N/A
8074	#		ADC_HV_HV1	000	N/A
8075	#		ADC_HV_HV2	000	N/A
8076	#		ADC_HV_OL	(参考値 0x003)	N/A
8077	#		ADC_HV_AMP1	(参考値 0x08C)	N/A
8078	#		ADC_HV_AMP2	(参考値 0x08A)	N/A
8079	#		ADC_HV_AMP3	(参考値 0x028)	N/A
8080	#		ADC_HV_TMP	(参考値 0xC55)	N/A
8081	#				
8082	#		ADC_CFD_LEFA	FFF	N/A
8083	#		ADC_CFD_LEFB	FFF	N/A
8084	#		ADC_CFD_LEFS	FFF	N/A
8085	#		ADC_CFD_STC	FFF	N/A
8086	#		ADC_CFD_STE	FFF	N/A
8087	#		ADC_AMP_TEMP	(参考値 0xCC8)	N/A
8088	#		ADC_CPU_+5	(参考値 0xCA2)	N/A
8089	#		ADC_CPU_+3.3	(参考値 0xA3D)	N/A
8090	#		ADC_CPU_+1.8	(参考値 0x5BF)	N/A
8091	#		ADC_CPU_+2.5	(参考値 0x821)	N/A
8092	#		ADC_CPU_+12	(参考値 0xC46)	N/A
8093	#		ADC_CPU_-12	(参考値 0xC3C)	N/A
8094	#		ADC_CPU_TEMP	(参考値 0xCC7)	N/A
8095	#				
8096	#		<CPU> AMP_ENA	DIS	N/A
8097	#		<HV> ERR	ERR	N/A
8098	#		<HV> ON_OPTICS	OFF	N/A
8099	#		<HV> ON_BOARD	OFF	N/A
8100	#		<HV> SAFE	ON	N/A
8101	#		<CPU> ENA_OPTICS	DIS	N/A

文 書 番 号	シート No.	版 数
	97	1

```

8102 # <CPU> ENA_BOARD DIS N/A
8103 # <CPU> SAFE_ENA DIS N/A
8104 #
8105 #
8106 #
8107 WAIT_SEC 40
8108 #
8109 . MSA. HK_RATE 0x01000040
8110 # command : 007001000040
8111 #
8112 WAIT_SEC 40
8113 #
8114 # HV_sw_enable(both)
8115 . MSA. HV_ENA 0x0F000000
8116 # command : 07000F000000
8117 #
8118 WAIT_SEC 40
8119 #
8120 # QL Check-Item Spec check
8121 # -----+-----+-----+
8122 # <CPU> ENA_BOARD ENA N/A
8123 # <CPU> SAFE_ENA ENA N/A
8124 #
8125 . # 下記手順は地上試験においてスキップする。
8126 #
8127 # HV_Board_PwrON
8128 . MSA. HV_ON 0x0A000000
8129 # command : 05000A000000
8130 #
8131 WAIT_SEC 40
8132 # WAIT_SEC 300 #Flight only
8133 #
8134 # QL Check-Item Spec check
8135 # -----+-----+-----+
8136 # MSA ALL HK <HV> ON_BOARD ON N/A
8137 # <HV> ERR OK N/A
8138 #
8139 #
8140 # HV_Optics_PwrON
8141 . MSA. HV_ON 0x05000000
8142 # command : 050005000000
8143 #
8144 WAIT_SEC 40
8145 # WAIT_SEC 300 #Flight only
8146 #
8147 # QL Check-Item Spec check
8148 # -----+-----+-----+
8149 # MSA ALL HK <HV> ON_OPTICS ON N/A
8150 # <HV> ERR OK N/A
8151 #
8152 # ** スキップ手順はここまで **
8153 #

```

文 書 番 号	シート No.	版 数
	98	1

```

8154 #
8155 # .....
8156 # 1.3.1-1. Cal Check
8157 # .....
8158 #
8159 # < MSA AMPSW CAL command Check >
8160 #
8161 #
8162 #
8163 #
8164 # Discriminator Level Setting
8165 #
8166 # Ch1
8167 MSA.CFD_DAC 0x30300B00
8168 # command : 0D0130300B00
8169 #
8170 WAIT_SEC 5
8171 #
8172 # Ch2
8173 MSA.CFD_DAC 0x28280B00
8174 # command : 0D0128280B00
8175 #
8176 WAIT_SEC 5
8177 #
8178 # Ch3
8179 MSA.CFD_DAC 0x24240B00
8180 # command : 0D0124240B00
8181 #
8182 WAIT_SEC 5
8183 #
8184 # Ch4
8185 MSA.CFD_DAC 0x22220B00
8186 # command : 0D0122220B00
8187 #
8188 WAIT_SEC 5
8189 #
8190 # Ch5
8191 MSA.CFD_DAC 0x21210B00
8192 # command : 0D0121210B00
8193 #
8194 WAIT_SEC 10
8195 #
8196 # QL          Check-Item          Spec          check
8197 # -----+-----+-----+-----
8198 # MSA ALL HK  ADC_CFD_LEFA  (参考値 0xB4C)          N/A
8199 #
8200 # QL          Check-Item          Spec          check
8201 # -----+-----+-----+-----
8202 # MSA ALL HK  ADC_CFD_LEFB  (参考値 0xB44)          N/A
8203 #
8204 # QL          Check-Item          Spec          check
8205 # -----+-----+-----+-----

```

文 書 番 号	シート No.	版 数
	99	1

```

8206 # MSA ALL HK ADC_CFD_LEFS (参考値 0xB3F) N/A
8207 #
8208 # QL Check-Item Spec check
8209 # -----+-----+-----+-----
8210 # MSA ALL HK ADC_CFD_STC (参考値 0xB3A) N/A
8211 #
8212 # QL Check-Item Spec check
8213 # -----+-----+-----+-----
8214 # MSA ALL HK ADC_CFD_STE (参考値 0xB2E) N/A
8215 #
8216 #
8217 #
8218 # Collect mission data
8219 MD1F.MSA_CLCT_START
8220 #
8221 WAIT_SEC 40
8222 #
8223 # QL Check-Item Spec check
8224 # -----+-----+-----+-----
8225 # MD1F OBS HK <<PAYLOAD>> MS5 CLCT ON N/A
8226 #
8227 # Measure on
8228 MSA.CPU_AMP_ENA 0x01000100
8229 #
8230 WAIT_SEC 40
8231 #
8232 # QL Check-Item Spec check
8233 # -----+-----+-----+-----
8234 # MSA ALL HK AMP_ENA ENA N/A
8235 #
8236 #
8237 #
8238 # AMPSW CAL ON
8239 MSA.CAL_SET 0x8050125310
8240 # command : fa8050125310
8241 #
8242 WAIT_SEC 180
8243 #
8244 # waiting for 20min?
8245 #
8246 #
8247 #
8248 MD1F.MSA_CLCT_STOP
8249 #
8250 WAIT_SEC 40
8251 #
8252 # QL Check-Item Spec check
8253 # -----+-----+-----+-----
8254 # MD1F OBS HK <<PAYLOAD>> MS5 CLCT OFF N/A
8255 #
8256 #
8257 #

```

```

8258 # Measure off(コマンド確認)
8259 . MSA.CPU_AMP_ENA 0x00000000
8260 #
8261 WAIT_SEC 40
8262 #
8263 # QL          Check-Item          Spec          check
8264 # -----+-----+-----+-----+
8265 # MSA ALL HK  AMP_ENA          DIS          N/A
8266 #
8267 #
8268 #
8269 # 下記手順は地上試験において (HVON をスキップしているため) スキップする。
8270 #
8271 # #HV_OFF
8272 . MSA.HV_OFF 0x0A000000
8273 # command : 09000a000000
8274 #
8275 WAIT_SEC          40
8276 #
8277 # QL          Check-Item          Spec          check
8278 # -----+-----+-----+-----+
8279 # MSA ALL HK  <HV> ON_BOARD      OFF          N/A
8280 #
8281 . MSA.HV_OFF 0x05000000
8282 # command : 090005000000
8283 #
8284 WAIT_SEC          40
8285 #
8286 # QL          Check-Item          Spec          check
8287 # -----+-----+-----+-----+
8288 # MSA ALL HK  <HV> ON_OPTICS      OFF          N/A
8289 #
8290 # ** スキップ手順はここまで **
8291 #
8292 # HV_sw_disable
8293 . MSA.HV_DIS 0x0F000000
8294 # command : 08000F000000
8295 #
8296 #
8297 WAIT_SEC 40
8298 #
8299 # QL          Check-Item          Spec          check
8300 # -----+-----+-----+-----+
8301 #          <CPU> ENA_BOARD      DIS          N/A
8302 #          <CPU> SAFE_ENA      DIS          N/A
8303 #
8304 #
8305 #
8306 #
8307 # < MSA を立ち下げる >
8308 PCD.ON_OFF_ENA
8309 #

```

8310	WAIT_SEC 40
8311	#
8312	# QL Check-Item Spec check
8313	# -----+-----+-----+-----
8314	# PCS1 <PCD> ON/OFF ENA N/A
8315	#
8316	PCD. PSU_MSA_OFF
8317	#
8318	WAIT_SEC 40
8319	#
8320	# QL Check-Item Spec check
8321	# -----+-----+-----+-----
8322	# PCS1 <PCD> MSA OFF N/A
8323	#
8324	#
8325	PCD. ON_OFF_DIS
8326	#
8327	WAIT_SEC 40
8328	#
8329	# QL Check-Item Spec check
8330	# -----+-----+-----+-----
8331	# PCS1 <PCD> ON/OFF DIS N/A
8332	#

文 書 番 号	シート No.	版 数
	102	1

dcsn(ENA CHECK)

```

#!HEAD: dcsn-ENA 2018-03-07 15:11:00 156      45 MMO      DCSM      //
SVT-1c ENA_CHECK
8001  #
8002  # <startTime>2018-03-16T00:00:00Z</startTime>
8003  # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004  # =====
8005  # 1.1. ENA PWR ON
8006  # =====
8007  #
8008  # PCD.ON_OFF_ENA
8009  # WAIT_SEC 40
8010  #
8011  # < CHECK >
8012  # CHECK PCD.ON_OFF_ENA_DIS      =ENA
8013  #
8014  # PCD.PSU_ENA_ON
8015  # WAIT_SEC 2
8016  # MD1F.DPU_SW_ERR_CLR
8017  # WAIT_SEC 2
8018  # MD2F.DPU_SW_ERR_CLR
8019  #
8020  # WAIT_SEC 40
8021  #
8022  # < CHECK >
8023  # CHECK PCD.PSU_ENA_ON_OFF      =ON
8024  #
8025  # WAIT_SEC 40
8026  #
8027  # WAIT_UNTIL ({ENA.ENA_TBL_WRT_IFE_END} =END) -timeoutsec 200
8028  #
8029  # < CHECK >
8030  # CHECK ENA.CMD_ERR_FLG      =FALSE
8031  # CHECK ENA.HV_SAFTY_DIS      =ENA
8032  # CHECK ENA.HV_ON      =OFF
8033  # CHECK ENA.HV_ON_ENA      =DIS
8034  # CHECK ENA.HV_SHUTDOWN      =NON
8035  # CHECK ENA.HV_SHUTDOWN_MODE      =NON
8036  # CHECK ENA.ENA_TBL_WRT_IFE_END      =END
8037  #
8038  # QL      Check-Item      Spec      Check
8039  # -----+-----+-----+-----
8040  # ENA      PSYNCCTR      カウントアップする      N/A
8041  #      BASECTR      カウントアップする      N/A
8042  # MD1F      MD1F.MS3_LINK_ERR      na      N/A
8043  #      OBS_HK      MD1F.SLOT_ERR      na      N/A
8044  # MD2F      MD2F.SLOT_ERR      na      N/A
8045  #      OBS_HK
8046  #
8047  #
8048  # PCD.ON_OFF_DIS
8049  # WAIT_SEC 40

```

文 書 番 号	シート No.	版 数
	103	1

```

8050 # < CHECK >
8051 CHECK PCD.ON_OFF_ENA_DIS =DIS
8052 #
8053 #
8054 # =====
8055 # 1.2. ENA CMD/TLM チェック 1
8056 # =====
8057 #
8058 # ENA HK packet 生成間隔の設定 (16sec)
8059 #
8060 MD1U.HK_SET 0x0b 0x04
8061 WAIT_SEC 40
8062 # QL          Check-Item          Spec          Judgment
8063 # -----+-----+-----+-----+
8064 # MD1U      HU_Hk_RepInt_ENA      4(16sec)      N/A
8065 #
8066 #
8067 #
8068 #
8069 # < mission packet 生成 (HK) >
8070 # 16s
8071 MD1U.ENA_PRI0_HK_SET 0x05
8072 #
8073 WAIT_SEC 40
8074 # < CHECK >
8075 CHECK ENA.ENA_HK_INTVAL =sec_16
8076 #
8077 #
8078 # < mass accumulation mode >
8079 MD1U.ENA_PROCESS_MODE_SET 0x00 0x88 0x10 0x08 0x02 0xff 0xff 0x00
8080 & 0x00 0x20 0x00 0x00 0x20 0x00 0x22 0x00
8081 & 0x00 0x00 0x00
8082 #
8083 WAIT_SEC 40
8084 #
8085 # < CHECK >
8086 CHECK ENA.ENA_PROCESS_MODE =MASS
8087 CHECK ENA.OBS_MODE =COINCI
8088 CHECK ENA.SV_WAVE1_TBL 2
8089 CHECK ENA.SV_WAVE2A_TBL 2
8090 CHECK ENA.SV_WAVE2B_TBL 2
8091 CHECK ENA.SV_LENS_TBL 2
8092 CHECK ENA.ENA_PHASE_NUMID =n_16
8093 CHECK ENA.ENA_ENGY_NUMID =n_8
8094 CHECK ENA.ENA_CH_NUMID =n_8
8095 CHECK ENA.ENA_MASS_NUMID =n_8
8096 CHECK ENA.ENA_ACCUM_NUMID =sp_4
8097 CHECK ENA.ENA_TRG_ENADIS =DIS
8098 #
8099 WAIT_SEC 60
8100 #
8101 # < cal pulse 発生(1kHz) >

```

文 書 番 号	シート No.	版 数
	104	1

```

8102 . MD1U. ENA_S_CAL_MODE_SET 0x31 0x01 0x01 0x00 0x00 0x02
8103 #
8104 WAIT_SEC 40
8105 #
8106 # < CHECK >
8107 CHECK ENA. CAL_FREQ =f_1k
8108 CHECK ENA. CAL_STARTR1_ON =ON
8109 CHECK ENA. CAL_STARTS1_ON =ON
8110 CHECK ENA. CAL_STOPS1_ON =ON
8111 CHECK ENA. CAL_TIMING_STARTR 0x00
8112 CHECK ENA. CAL_TIMING_STARTS 0x00
8113 CHECK ENA. CAL_TIMING_STOPS 0x02
8114 #
8115 #
8116 WAIT_SEC 60
8117 #
8118 # =====
8119 . # 1.3. ENA shutdown
8120 # =====
8121 #
8122 ENA. SHUTDOWN 0x00
8123 WAIT_SEC 40
8124 #
8125 WAIT_UNTIL ({ENA. HV_ON} =OFF) -timeoutsec 200
8126 #
8127 CHECK ENA. HV_MAIN_LVLSET 0x00
8128 CHECK ENA. HV_STARTMCP_LVLSET 0x00
8129 CHECK ENA. HV_STOPMCP_LVLSET 0x00
8130 CHECK ENA. HV_DEF_LVLSET 0x00
8131 CHECK ENA. HV_ON =OFF
8132 CHECK ENA. HV_SHUTDOWN_MODE =NML
8133 #
8134 ENA. CLEAR_FLG 0x0F
8135 WAIT_SEC 40
8136 CHECK ENA. HV_SHUTDOWN_MODE =NON
8137 #
8138 # =====
8139 . # 1.4. ENA 立ち下げ
8140 # =====
8141 #
8142 . PCD. ON_OFF_ENA
8143 WAIT_SEC 2
8144 PCD. PSU_ENA_OFF
8145 WAIT_SEC 2
8146 PCD. ON_OFF_DIS
8147 WAIT_SEC 40
8148 #
8149 # < CHECK >
8150 CHECK PCD. PSU_ENA_ON_OFF =OFF
8151 CHECK PCD. ON_OFF_ENA_DIS =DIS
8152 #
8153 . # QL          Check-Item          Spec          Check

```

```
8154 # -----+-----+-----+-----+-----+-----+-----+-----+-----+-----+
8155 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
8156 . #
```

文 書 番 号	シ-ト No.	版 数
	106	1

dcsn(HEP CHECK)

```

#!HEAD: dcsn-HEP_2 2018-03-07 15:22:00 336      45 MM0      DCSM      //
SVT-1c HEP_CHECK
8001  #
8002  # <startTime>2018-03-16T00:00:00Z</startTime>
8003  # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004  #*****
8005  # ESOC test procedure
8006  #*****
8007  # *****
8008  # 1. HEP-ele Initial Check Out; short time version for ESOC test "ONLY"
8009  # *****
8010  # =====
8011  # 1.1. HEP-ele Turn ON
8012  # =====
8013  #
8014  #
8015  # # HEP-ele Turn ON
8016  #
8017  # PCD.ON_OFF_ENA
8018  #
8019  WAIT_SEC 40
8020  #
8021  # QL          Check-Item          Spec          Check
8022  # -----+-----+-----+-----
8023  # PCS1        PCD.ON_OFF_ENA_DIS    ENA          N/A
8024  #
8025  PCD.PSU_HEP_e_ON
8026  #
8027  WAIT_SEC 40
8028  #
8029  # QL          Check-Item          Spec          Check
8030  # -----+-----+-----+-----
8031  # PCS1        PCD.PSU_HEP_E_ON_OFF  ON          N/A
8032  # MD1F_OBS_HK MD1F.MS6_LINK_STAT    ON          N/A
8033  # MD1F_OBS_HK MD1F.MS6_DAT_CLCT     ON          N/A
8034  #
8035  PCD.ON_OFF_DIS
8036  #
8037  WAIT_SEC 40
8038  #
8039  # QL          Check-Item          Spec          Check
8040  # -----+-----+-----+-----
8041  # PCS1        PCD.ON_OFF_ENA_DIS    DIS          N/A
8042  #
8043  #
8044  # # SW Error clear
8045  MD1F.DPU_SW_ERR_CLR
8046  #
8047  WAIT_SEC 120
8048  #
8049  # QL          Check-Item          Spec          Check

```

文 書 番 号	シート No.	版 数
	107	1

```

8050 # -----+-----+-----+-----
8051 # MD1F_OBS_HK    MD1F.MS6_LINK_ERR    na    N/A
8052 #                MD1F.SLOT_ERR        na    N/A
8053 #
8054 # <Initial STATUS check after Turn ON>
8055 #
8056 # < CHECK >
8057 CHECK HEPE.TEMP_MONITOR1    -0.459 43.209
8058 CHECK HEPE.TEMP_MONITOR2    3.425 48.380
8059 CHECK HEPE.ELE_ION_FL      =ELE
8060 CHECK HEPE.STBY_OBS_F      =SBY
8061 CHECK HEPE.HV_ELE_CTL      =DIS
8062 CHECK HEPE.HE_ELE_DAC      =OFF
8063 #
8064 # QL                Check-Item                Spec                Check
8065 # -----+-----+-----+-----
8066 # MPPE-HEPE1    HV_MONITOR1    0x0000 or 0x0100    G  N
8067 #
8068 #
8069 # =====
8070 # 1.2. Parameters set
8071 # =====
8072 #
8073 . HEPE.ASIC_REG_SET 0x0000 0xC000 0x1401 0x0000
8074 & 0x0000 0x0000 0x0000 0x0000
8075 & 0x0000 0x0000 0x0000 0x0000
8076 & 0x0000 0x0000 0x0000 0x2060
8077 & 0x00000000 0x00000000 0x00000000
8078 & 0x00010000 0x00000000 0x00000000
8079 & 0x00000000 0x00000000 0x00010000
8080 #
8081 WAIT_SEC 10
8082 #
8083 #
8084 HEPE.REG_WR_GR1
8085 #
8086 WAIT_SEC 10
8087 #
8088 #
8089 HEPE.REG_WR_GR2
8090 #
8091 WAIT_SEC 10
8092 #
8093 #
8094 # # LOAD コマンド送信
8095 HEPE.LOAD_START
8096 #
8097 WAIT_SEC 30
8098 #
8099 #
8100 # # Temp. correct table para set
8101 HEPE.ASIC_CAL_TBL_SEL 0x0000 0x0001 0x0002 0x0003 0x0004

```

文 書 番 号	シート No.	版 数
	108	1

```

8102 & 0x0000 0x0001 0x0002 0x0003 0x0004
8103 #
8104 WAIT_SEC 4
8105 #
8106 #
8107 # # Temp. correct TABLE0 set
8108 HEPE.CAL_TBL0_SET 0x0FFF 0x0400 0x0400 0x0400 0x0400
8109 & 0x0400 0x0400 0x0400 0x0400 0x0400
8110 & 0x0400 0x0400 0x0400 0x0400 0x0400
8111 & 0x0400 0x0400 0x0400 0x0400 0x0400
8112 & 0x0400 0x0400 0x0400 0x0400 0x0400
8113 & 0x0400 0x0400 0x0400 0x0400 0x0400
8114 & 0x0400 0x0400 0x0400 0x0400 0x0400
8115 & 0x0400 0x0400 0x0400 0x0400 0x0400
8116 & 0x0400 0x0400 0x0400 0x0400 0x0400
8117 & 0x0400 0x0400 0x0400 0x0400 0x0400
8118 #
8119 WAIT_SEC 8
8120 #
8121 # QL          Check-Item          Spec          Check
8122 # -----+-----+-----+-----+
8123 # HEPE          HEPE.ASIC0_TEMP_COEF      0x0400          N/A
8124 # HEPE          HEPE.ASIC5_TEMP_COEF      0x0400          N/A
8125 #
8126 #
8127 # # FIFO_RESET
8128 HEPE.FIFO_RESET
8129 #
8130 WAIT_SEC 4
8131 #
8132 HEPE.SSD_ON
8133 #
8134 WAIT_SEC 4
8135 #
8136 HEPE.OBS_START
8137 #
8138 WAIT_SEC 30
8139 #
8140 # < CHECK >
8141 CHECK HEPE.SSD_OFF_FL          =OBS
8142 CHECK HEPE.STBY_OBS_F          =OBS
8143 #
8144 WAIT_SEC 70
8145 #
8146 HEPE.OBS_STBY
8147 #
8148 WAIT_SEC 4
8149 #
8150 HEPE.SSD_OFF
8151 #
8152 #
8153 #

```

文 書 番 号	シート No.	版 数
	109	1

```

8154 # =====
8155 # 1.3. HEP-ele Turn OFF
8156 # =====
8157 #
8158 #
8159 . MD1F.HEPE_CLCT_STOP
8160 #
8161 WAIT_SEC 40
8162 #
8163 # QL          Check-Item          Spec          Check
8164 # -----+-----+-----+-----
8165 # MD1F_OBS_HK  MD1F.MS6_DAT_CLCT    OFF          N/A
8166 #
8167 PCD.ON_OFF_ENA
8168 #
8169 WAIT_SEC 40
8170 #
8171 # QL          Check-Item          Spec          Check
8172 # -----+-----+-----+-----
8173 # PCS1         PCD.ON_OFF_ENA_DIS    ENA          N/A
8174 #
8175 PCD.PSU_HEP_e_OFF
8176 #
8177 WAIT_SEC 40
8178 #
8179 # QL          Check-Item          Spec          Check
8180 # -----+-----+-----+-----
8181 # PCS1         PCD.PSU_HEP_E_ON_OFF  OFF          N/A
8182 #
8183 PCD.ON_OFF_DIS
8184 #
8185 WAIT_SEC 40
8186 #
8187 # QL          Check-Item          Spec          Check
8188 # -----+-----+-----+-----
8189 # PCS1         PCD.ON_OFF_ENA_DIS    DIS          N/A
8190 #
8191 #
8192 #
8193 #
8194 #
8195 #
8196 # *****
8197 . # 2. HEP-ion Initial Check Out; short time version for ESOC test "ONLY"0
8198 # *****
8199 # =====
8200 . # 2.1. HEP-ion Turn ON
8201 # =====
8202 . #
8203 # # HEP-ion Turn ON
8204 . #
8205 . PCD.ON_OFF_ENA

```


文 書 番 号	シート No.	版 数
	110	1

```

8206 #
8207 WAIT_SEC 40
8208 #
8209 # QL          Check-Item          Spec          Check
8210 # -----+-----+-----+-----
8211 # PCS1        PCD. ON_OFF_ENA_DIS   ENA            N/A
8212 #
8213 PCD. PSU_HEP_i_ON
8214 #
8215 WAIT_SEC 40
8216 #
8217 # QL          Check-Item          Spec          Check
8218 # -----+-----+-----+-----
8219 # PCS1        PCD. PSU_HEP_I_ON_OFF   ON            N/A
8220 # MD1F_OBS_HK MD1F. MS7_LINK_STAT      ON            N/A
8221 # MD1F_OBS_HK MD1F. MS7_DAT_CLCT      ON            N/A
8222 #
8223 PCD. ON_OFF_DIS
8224 #
8225 WAIT_SEC 40
8226 #
8227 # QL          Check-Item          Spec          Check
8228 # -----+-----+-----+-----
8229 # PCS1        PCD. ON_OFF_ENA_DIS   DIS            N/A
8230 #
8231 # # SW Error clear
8232 MD1F. DPU_SW_ERR_CLR
8233 #
8234 WAIT_SEC 120
8235 #
8236 # QL          Check-Item          Spec          Check
8237 # -----+-----+-----+-----
8238 # MD1F_OBS_HK MD1F. MS7_LINK_ERR      na            N/A
8239 #             MD1F. SLOT_ERR      na            N/A
8240 #
8241 # # ASIC para set
8242 #
8243 . HEPI.ASIC_REG_SET 0x0000 0xC000 0x0401 0x0000 0x0000 0x0000 0x0000
8244 & 0x0000 0x0000 0x0000 0x0000 0x0000 0x0000 0x0002 0x0000 0x2060
8245 & 0x00010000 0x00010000 0x00010000 0x00010000 0x00010000
8246 & 0xFFFFFFFF 0xFFFFFFFF 0xFFFFFFFF 0xFFFFFFFF
8247 #
8248 WAIT_SEC 10
8249 #
8250 #
8251 HEPI. REG_WR_GR1
8252 #
8253 WAIT_SEC 10
8254 #
8255 #
8256 HEPI. REG_WR_GR2
8257 #

```

文 書 番 号	シート No.	版 数
	111	1

```
8258 WAIT_SEC 10
8259 #
8260 #
8261 # # LOAD コマンド送信
8262 HEPI.LOAD_START
8263 #
8264 WAIT_SEC 20
8265 #
8266 #
8267 # # Temp correct table set
8268 HEPI.ASIC_CAL_TBL_SEL 0x0000 0x0001 0x0002 0x0003 0x0004 0x0000 0x0001 0x0002 0x0003
0x0004
8269 #
8270 #
8271 WAIT_SEC 4
8272 #
8273 #
8274 #
8275 # # FIFO_RESET
8276 HEPI.FIFO_RESET
8277 #
8278 WAIT_SEC 4
8279 #
8280 #
8281 #
8282 # # SSD/OFF=SSD
8283 HEPI.SSD_ON
8284 #
8285 WAIT_SEC 4
8286 #
8287 #
8288 # # STBY/START=START
8289 HEPI.OBS_START
8290 #
8291 # SSD+TOF mode data
8292 WAIT_SEC 60
8293 #
8294 #
8295 #
8296 # # STBY/START=STBY
8297 HEPI.OBS_STBY
8298 #
8299 #
8300 # =====
8301 . # 2.2. HEP-ion Turn OFF
8302 # =====
8303 #
8304 . #
8305 . MD1F.HEPI_CLCT_STOP
8306 #
8307 WAIT_SEC 40
8308 #
```

文 書 番 号	シ-ト No.	版 数
	112	1

8309	# QL	Check-Item	Spec	Check
8310	#	-----+-----+-----+-----		
8311	# MD1F_OBS_HK	MD1F. MS7_DAT_CLCT	OFF	N/A
8312	#			
8313	PCD. ON_OFF_ENA			
8314	#			
8315	WAIT_SEC 40			
8316	#			
8317	# QL	Check-Item	Spec	Check
8318	#	-----+-----+-----+-----		
8319	# PCS1	PCD. ON_OFF_ENA_DIS	ENA	N/A
8320	#			
8321	PCD. PSU_HEP_i_OFF			
8322	#			
8323	WAIT_SEC 40			
8324	#			
8325	# QL	Check-Item	Spec	Check
8326	#	-----+-----+-----+-----		
8327	# PCS1	PCD. PSU_HEP_I_ON_OFF	OFF	N/A
8328	#			
8329	PCD. ON_OFF_DIS			
8330	#			
8331	WAIT_SEC 40			
8332	#			
8333	# QL	Check-Item	Spec	Check
8334	#	-----+-----+-----+-----		
8335	# PCS1	PCD. ON_OFF_ENA_DIS	DIS	N/A
8336	#			

文 書 番 号	シート No.	版 数
	113	1

dcsn(MDM CHECK)

```

#!HEAD: dcsn-MDM 2018-03-07 16:13:00 185          45 MMO          DCSM          //
SVT-1c MDM_CHECK
8001 . #
8002 . LET          @ONERR=NEXT
8003 . # <startTime>2018-03-16T00:00:00Z</startTime>
8004 . # <stopTime>2018-03-16T20:00:00Z</stopTime>
8005 . # *****
8006 . # 13. MDM CHECK
8007 . # *****
8008 . #
8009 . # =====
8010 . # 13.1. MDM Initial Power ON
8011 . # =====
8012 . #
8013 . # 本手順は interactive operations にて実施する。
8014 . # 実際の運用時には往復 180 秒程度の電波伝搬遅延がかかることを考慮して
8015 . # 手順を進めること。
8016 . #
8017 . # MDM-E の電源を ON する。
8018 . #
8019 . PCD.ON_OFF_ENA
8020 . #
8021 . CHECK PCD.ON_OFF_ENA_DIS          =ENA
8022 . #
8023 . PCD.MDM_E_ON
8024 . #
8025 . CHECK PCD.MDM_E_ON_OFF          =ON
8026 . #
8027 . PCD.ON_OFF_DIS
8028 . #
8029 . CHECK PCD.ON_OFF_ENA_DIS          =DIS
8030 . #
8031 . # SW Error clear
8032 . MD2F.DPU_SW_ERR_CLR
8033 . MD1F.DPU_SW_ERR_CLR
8034 . #
8035 . # Spacewire LINK 確立の確認 (MDP~MDM-E 間)
8036 . # QL          Check-Item          Spec          Check
8037 . # -----+-----+-----+-----
8038 . # MD2F OBS HK  MD2F.MS7_LINK_STAT  ON          N/A
8039 . #              (MDM LINK STAT)
8040 . #              MD2F.MS7_LINK_ERR    na          N/A
8041 . #              MD2F.SLOT_ERR        na          N/A
8042 . #              MD2F.MS7_DAT_CLCT    ON          N/A
8043 . #
8044 . # MDM ショートバッファへ変更
8045 . MD2U.BUF_SHORT 0x0E
8046 . #
8047 . # QL          Check-Item          Spec          Check
8048 . # -----+-----+-----+-----
8049 . # MD2F          MS7_BUFF_SEL (MDM)    S

```

```

8050 . #
8051 . # =====
8052 . # HK check
8053 . # =====
8054 . #
8055 . # MDM 電源供給後 MDM はイニシャル処理を行うため、定常動作となる約 3 分後
8056 . # ステータスを QL モニタにてチェックする。
8057 . #
8058 . # < CHECK >
8059 . CHECK MDM. MDM_DAUTO =OFF
8060 . CHECK MDM. MDM_TRIG =MODE1
8061 . CHECK MDM. MDM_CAL =OFF
8062 . CHECK MDM. MDM_OBS =MODE1
8063 . CHECK MDM. MDM_MUX 0x00
8064 . CHECK MDM. MDM_SEL 0x0f
8065 . CHECK MDM. MDM_SAMP 0x1
8066 . CHECK MDM. MDM_ACLK 0x1
8067 . CHECK MDM. MDM_DISCRI2 0x06
8068 . CHECK MDM. MDM_PARA 0x00
8069 . #
8070 . # QL          Check-Item          Spec          Data          Check
8071 . # -----+-----+-----+-----+-----
8072 . # MDM          MDM_DISCRI1          0x01~0x40          _____ G    N
8073 . #
8074 . # =====
8075 . # Observation mode check (Science data check)
8076 . # =====
8077 . #
8078 . #
8079 . #
8080 . # ミッションデータの中身を MDM チームが確認する。
8081 . #
8082 . # =====
8083 . # 13.2. Initial check
8084 . # =====
8085 . #
8086 . # 自動ディスクリ設定機能確認
8087 . # 下記コマンド送信後 1 分以内に HK 画面のステータス確認を行う
8088 . MDM. MDM_DISCRI1_AUTO
8089 . #
8090 . # < CHECK >
8091 . CHECK MDM. MDM_DAUTO =ON
8092 . #
8093 . # 上記コマンドを送信して 4 分経過後に以下のステータス確認を行う
8094 . #
8095 . #
8096 . # QL          Check-Item          Spec          Check
8097 . # -----+-----+-----+-----
8098 . # MDM          MDM_DAUTO          OFF          N/A
8099 . # MDM          MDM_TRIG          MODE1        N/A
8100 . # MDM          MDM_OBS          MODE1        N/A
8101 . # MDM          MDM_DISCRI1        0x01~0x40    N/A

```

```
8102 #
8103 # 【キャリブレーションを実施して確実にミッションデータを生成する】
8104 #
8105 # CAL-ON の設定確認
8106 MDM. MDM_CAL_ON
8107 WAIT_SEC 60
8108 # QL          Check-Item          Spec          Check
8109 # -----+-----+-----+-----
8110 # MDM          MDM_CAL              ON             N/A
8111 #
8112 # MISSION 画面でデータ確認
8113 # QL          Check-Item          Spec          Check
8114 # -----+-----+-----+-----
8115 # MDM          MDM_OBS_DATA        0x01           N/A
8116 # MDM          MDM_OBS_INFO        -              N/A
8117 # MDM          MDM_TRIG_DATA        0x01           N/A
8118 # MDM          MDM_TRIG_INFO        0x0F           N/A
8119 #
8120 # CAL-OFF の設定確認
8121 MDM. MDM_CAL_OFF
8122 WAIT_SEC 60
8123 # QL          Check-Item          Spec          Check
8124 # -----+-----+-----+-----
8125 # MDM          MDM_CAL              OFF            N/A
8126 #
8127 #
8128 # 【キャリブレーション停止、ミッションデータストップ】
8129 #
8130 # =====
8131 # 13.3. MDM commissioning end
8132 # =====
8133 #
8134 # MDM から Mission Data 収集停止
8135 MD2F. MDM_CLCT_STOP
8136 WAIT_SEC 40
8137 #
8138 # QL          Check-Item          Spec          Check
8139 # -----+-----+-----+-----
8140 # MD2F OBS HK  MD2F. MS7_DAT_CLCT  OFF            N/A
8141 #
8142 #
8143 #
8144 #
8145 #
8146 #
8147 #
8148 #
8149 #
8150 #
8151 #
8152 #
8153 #
```

```
8154 #
8155 # MDM-E/MDP を立ち下げる
8156 PCD. ON_OFF_ENA
8157 WAIT_SEC 40
8158 # QL          Check-Item          Spec          Check
8159 # -----+-----+-----+-----
8160 # PCS1        PCD. ON/OFF_ENA/DIS   ENA          N/A
8161 #
8162 #
8163 #
8164 #
8165 #
8166 #
8167 #
8168 PCD. MDM_E_OFF
8169 WAIT_SEC 40
8170 # QL          Check-Item          Spec          Check
8171 # -----+-----+-----+-----
8172 # PCS1        PCD. PSU_MDM_E_ON_OFF OFF          N/A
8173 #
8174 # # Spacewire LINK 切断を確認 (MDP～MDM 間)
8175 #
8176 # QL          Check-Item          Spec          Check
8177 # -----+-----+-----+-----
8178 # MD2F OBS HK MD2F. MS71_LINK_STAT  OFF          N/A
8179 #              (MDM LINK STAT)
8180 #
8181 PCD. ON_OFF_DIS
8182 # QL          Check-Item          Spec          Check
8183 # -----+-----+-----+-----
8184 # PCS1        PCD. ON/OFF_ENA/DIS   DIS          N/A
8185 #
```

文 書 番 号	シート No.	版 数
	117	1

dcsn(HTR(MTL) CHECK)

```

#!HEAD: dcsn-HTR_CHK 2018-03-07 15:34:00 240          45  MMO          DCSM
// OPE_HTR_CHK at Ccruise Phase (SVT-1c)
8001 . #
8002  # *****
8003 . # 1. ヒータ健全性確認
8004  # *****
8005  # =====
8006 . # 1.1. CH1~8
8007  # =====
8008 . #
8009 . DH.MC_EACH_EXE 0x002C # 1553B TLM mode 0 (SYS HK 1)
8010  #:          << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
8011  #:          0x00 DH. DMP_PERIOD 0x04
8012  #:          0x04 ACS. ACS_PKT_FMT_1
8013  #:          0x05 DH. OPE_MODE 0x0C 0x0A 0x00 0x00 0x000000000000000000000000
8014  #:          0x06 DH. TLMFMT1553B_CHG 0x00
8015  #
8016  WAIT_SEC 40
8017  #
8018  # < CHECK >
8019  CHECK DH. DMP_PERIOD          =SEC_16
8020  CHECK DH. CATEGORY_TBL_NO    0x00
8021  CHECK DH. DWNLNK_RATE        =BPS16384
8022  CHECK DH. TLM1553BFMTNO      0x00
8023  #
8024  TCS. MODE_STBY
8025  #
8026  WAIT_SEC 40
8027  #
8028  # < CHECK >
8029  CHECK TCS. MODE_ST            =STBY
8030  #
8031  # TCS_STBY を検出して NMNL_MODE へ再設定するさせるマクロを実行
8032  DH.MC_EACH_EXE 0x0105
8033  #:          << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
8034  #:          0x03 DH. ATMC_EACH_ENA 0x09
8035  #
8036  WAIT_SEC 4
8037  #
8038  TCS. CTRL_MANU_ON
8039  #
8040  WAIT_SEC 10
8041  #
8042  # < CHECK >
8043  #CHECK TCS. MANU_CTR          =ON
8044  #
8045  # # ヒータ CH1 ON
8046  TCS. CTRL_HTR_ON 0x00 0x01
8047  #
8048  WAIT_SEC 4
8049  #

```


文 書 番 号	シ-ト No.	版 数
	118	1

```

8050 # < CHECK >
8051 #CHECK TCS. HTR01_VOL_ST           =ON
8052 #CHECK PCD. DRU_HTR_I             0. 540   0. 945
8053 #
8054 # # ヒ-タ CH1 OFF
8055 TCS. CTRL_HTR_ON 0x00 0x00
8056 #
8057 WAIT_SEC 4
8058 #
8059 # < CHECK >
8060 #CHECK TCS. HTR01_VOL_ST           =OFF
8061 #CHECK PCD. DRU_HTR_I             0  0. 01
8062 #
8063 # # ヒ-タ CH2 ON
8064 TCS. CTRL_HTR_ON 0x01 0x01
8065 #
8066 WAIT_SEC 4
8067 #
8068 # < CHECK >
8069 #CHECK TCS. HTR02_VOL_ST           =ON
8070 #CHECK PCD. DRU_HTR_I             0. 321   0. 672
8071 #
8072 # # ヒ-タ CH2 OFF
8073 TCS. CTRL_HTR_ON 0x01 0x00
8074 #
8075 WAIT_SEC 4
8076 #
8077 # < CHECK >
8078 #CHECK TCS. HTR02_VOL_ST           =OFF
8079 #CHECK PCD. DRU_HTR_I             0  0. 01
8080 #
8081 # # ヒ-タ CH3 ON
8082 TCS. CTRL_HTR_ON 0x02 0x01
8083 #
8084 WAIT_SEC 4
8085 #
8086 # < CHECK >
8087 #CHECK TCS. HTR03_VOL_ST           =ON
8088 #CHECK PCD. DRU_HTR_I             0. 322   0. 672
8089 #
8090 # # ヒ-タ CH3 OFF
8091 TCS. CTRL_HTR_ON 0x02 0x00
8092 #
8093 WAIT_SEC 4
8094 #
8095 # < CHECK >
8096 #CHECK TCS. HTR03_VOL_ST           =OFF
8097 #CHECK PCD. DRU_HTR_I             0  0. 01
8098 #
8099 # # ヒ-タ CH4 ON
8100 TCS. CTRL_HTR_ON 0x03 0x01
8101 #

```

文 書 番 号	シ-ト No.	版 数
	119	1

```

8102 WAIT_SEC 4
8103 #
8104 # < CHECK >
8105 #CHECK TCS. HTR04_VOL_ST =ON
8106 #CHECK PCD. DRU_HTR_I 0.105 0.401
8107 #
8108 # # ヒ-タ CH4 OFF
8109 TCS. CTRL_HTR_ON 0x03 0x00
8110 #
8111 WAIT_SEC 4
8112 #
8113 # < CHECK >
8114 #CHECK TCS. HTR04_VOL_ST =OFF
8115 #CHECK PCD. DRU_HTR_I 0 0.01
8116 #
8117 # # ヒ-タ CH5 ON
8118 TCS. CTRL_HTR_ON 0x04 0x01
8119 #
8120 WAIT_SEC 4
8121 #
8122 # < CHECK >
8123 #CHECK TCS. HTR05_VOL_ST =ON
8124 #CHECK PCD. DRU_HTR_I 0.111 0.409
8125 #
8126 # # ヒ-タ CH5 OFF
8127 TCS. CTRL_HTR_ON 0x04 0x00
8128 #
8129 WAIT_SEC 4
8130 #
8131 # < CHECK >
8132 #CHECK TCS. HTR05_VOL_ST =OFF
8133 #CHECK PCD. DRU_HTR_I 0 0.01
8134 #
8135 WAIT_SEC 4
8136 #
8137 TCS. SET_HTR_ENA 0x05 0x01 # ヒ-タ CH06: BAT1-1 ENA(実運用では不要)
8138 #
8139 WAIT_SEC 4
8140 #
8141 # # ヒ-タ CH6 ON
8142 TCS. CTRL_HTR_ON 0x05 0x01
8143 #
8144 WAIT_SEC 4
8145 #
8146 # < CHECK >
8147 #CHECK TCS. HTR06_VOL_ST =ON
8148 #CHECK PCD. DRU_HTR_I 0.655 1.089
8149 #
8150 TCS. SET_HTR_ENA 0x05 0x00 # ヒ-タ CH06: BAT1-1 DIS(実運用では不要)
8151 #
8152 WAIT_SEC 4
8153 #

```

文 書 番 号	シ-ト No.	版 数
	120	1

```

8154 # # ヒ-タ CH6 OFF
8155 TCS. CTRL_HTR_ON 0x05 0x00
8156 #
8157 WAIT_SEC 4
8158 #
8159 # < CHECK >
8160 #CHECK TCS. HTR06_VOL_ST =OFF
8161 #CHECK PCD. DRU_HTR_I 0 0.01
8162 #
8163 # # ヒ-タ CH7 ON
8164 TCS. CTRL_HTR_ON 0x06 0x01
8165 #
8166 WAIT_SEC 4
8167 #
8168 # < CHECK >
8169 #CHECK TCS. HTR07_VOL_ST =ON
8170 #CHECK PCD. DRU_HTR_I 0.111 0.409
8171 #
8172 # # ヒ-タ CH7 OFF
8173 TCS. CTRL_HTR_ON 0x06 0x00
8174 #
8175 WAIT_SEC 4
8176 #
8177 # < CHECK >
8178 #CHECK TCS. HTR07_VOL_ST =OFF
8179 #CHECK PCD. DRU_HTR_I 0 0.01
8180 #
8181 # # ヒ-タ CH8 ON
8182 TCS. CTRL_HTR_ON 0x07 0x01
8183 #
8184 WAIT_SEC 4
8185 #
8186 # < CHECK >
8187 #CHECK TCS. HTR08_VOL_ST =ON
8188 #CHECK PCD. DRU_HTR_I 0.382 0.748
8189 #
8190 # # ヒ-タ CH8 OFF
8191 TCS. CTRL_HTR_ON 0x07 0x00
8192 #
8193 WAIT_SEC 4
8194 #
8195 # < CHECK >
8196 #CHECK TCS. HTR08_VOL_ST =OFF
8197 #CHECK PCD. DRU_HTR_I 0 0.01
8198 #
8199 TCS. CTRL_MANU_OFF
8200 #
8201 WAIT_SEC 40
8202 #
8203 # < CHECK >
8204 CHECK TCS. MANU_CTR =OFF
8205 #

```

文 書 番 号	シート No.	版 数
	121	1

```
8206 # < ノミナル制御モード移行 >
8207 TCS. MODE_NMNL
8208 #
8209 WAIT_SEC 40
8210 #
8211 # < CHECK >
8212 CHECK TCS. MODE_ST =NMNL
8213 #
8214 WAIT_SEC 600
8215 #
8216 # マクロコマンド 0x0105 を実行から 11 分以上経過していることを確認し、以下のコマンドを実
行する。
8217 # (TCS_NMNL 移行 AT を DIS するが、本モードでステータスは確認できない)
8218 #
8219 DH. ATMC_EACH_DIS 0x09
8220 #
8221 #
8222 WAIT_SEC 40
8223 #
8224 #
8225 DH. MC_EACH_EXE 0x002C # 1553B TLM mode 0 (SYS HK 1)
8226 #: << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
8227 #: 0x00 DH. DMP_PERIOD 0x04
8228 #: 0x04 ACS. ACS_PKT_FMT_1
8229 #: 0x05 DH. OPE_MODE 0x0C 0x0A 0x00 0x00 0x00000000000000000000000000000000
8230 #: 0x06 DH. TLMFMT1553B_CHG 0x00
8231 #
8232 WAIT_SEC 40
8233 #
8234 # < CHECK >
8235 CHECK DH. DMP_PERIOD =SEC_16
8236 CHECK DH. CATEGORY_TBL_NO 0x00
8237 CHECK DH. DWNLNK_RATE =BPS16384
8238 CHECK DH. TLM1553BFMTNO 0x00
8239 #
8240 #
```

文 書 番 号	シート No.	版 数
	122	1

dcsn(MDP 立ち下げ)

```

#!HEAD: dcsn-MDP_OFF 2018-03-07 15:31:00 98          45 MMO          DCSM
// SVT-1c MDP_OFF
8001  #
8002  # <startTime>2018-03-16T00:00:00Z</startTime>
8003  # <stopTime>2018-03-16T20:00:00Z</stopTime>
8004  # =====
8005  # 1.1. MDP 立ち下げ
8006  # =====
8007  #
8008  # <<< MDP-DPU RST/OFF >>>
8009  # DH.MDP_DPU12_CPU_OFF          # DH.MC_EACH_EXE 0x00B1
8010  #:          << MACRO >> 内容について以下に示す (DH. DUMMY は省略)
8011  #:          0x00 DH. ATMC_EACH_DIS 0x32
8012  #:          0x03 DH. ATMC_EACH_DIS 0x33
8013  #:          0x06 DH. ATMC_EACH_DIS 0x30
8014  #:          0x09 DH. ATMC_EACH_DIS 0x31
8015  #:          0x0A MD1F. CPU_RESET_ENA
8016  #:          0x0B MD1F. CPU_RESET
8017  #:          0x0C MD1F. CPU_OFF
8018  #:          0x0D MD2F. CPU_RESET_ENA
8019  #:          0x0E MD2F. CPU_RESET
8020  #:          0x0F MD2F. CPU_OFF
8021  #
8022  # < CHECK >
8023  CHECK DH. ATMC_ENA_DIS30          =DIS
8024  CHECK DH. ATMC_ENA_DIS31          =DIS
8025  CHECK DH. ATMC_ENA_DIS32          =DIS
8026  CHECK DH. ATMC_ENA_DIS33          =DIS
8027  #
8028  # QL          Check-Item          Spec          Check
8029  # -----+-----+-----+-----
8030  # MD1F_OBS_HK  << DPU1 H/W-HK >>
8031  #          HW_STATE2          CPU_RST_ENA (short)          N/A
8032  #          HWCMD_CODE          0x0801 (short)          N/A
8033  #          HW_STATE2          STANDBY (short)          N/A
8034  #          HWCMD_CODE          0x0702 (short)          N/A
8035  #
8036  # < CHECK >
8037  CHECK MD1F. DPU_HW_STATE2          =CPUOFF_DPUBUS
8038  CHECK MD1F. DPU_CMD_CODE          0x0202
8039  #
8040  # QL          Check-Item          Spec          Check
8041  # -----+-----+-----+-----
8042  # MD2F_OBS_HK  << DPU2 H/W-HK >>
8043  #          HW_STATE2          CPU_RST_ENA (short)          N/A
8044  #          HWCMD_CODE          0x0801 (short)          N/A
8045  #          HW_STATE2          STANDBY (short)          N/A
8046  #          HWCMD_CODE          0x0702 (short)          N/A
8047  #
8048  # < CHECK >
8049  CHECK MD2F. DPU_HW_STATE2          =CPUOFF_DPUBUS

```

文 書 番 号	シート No.	版 数
	123	1

```

8050 CHECK MD2F.DPU_CMD_CODE 0x0202
8051 #
8052 . DH.MC_EACH_EXE 0x002C # 1553B TLM mode 0 (SYS HK 1)
8053 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
8054 #: 0x00 DH.DMP_PERIOD 0x04
8055 #: 0x04 ACS.ACS_PKT_FMT_1
8056 #: 0x05 DH.OPE_MODE 0x0C 0x0A 0x00 0x00 0x00000000000000000000000000000000
8057 #: 0x06 DH.TLMFMT1553B_CHG 0x00
8058 #
8059 # <CHECK>
8060 CHECK DH.DMP_PERIOD =SEC_16
8061 CHECK DH.CATEGORY_TBL_NO 0x00
8062 CHECK DH.DWNLNK_RATE =BPS16384
8063 CHECK DH.TLM1553BFMTNO 0x00
8064 #
8065 . # <<< MDP-DPU1/2 Power OFF >>>
8066 . DH.MDP_DPU12_OFF # DH.MC_EACH_EXE 0x00B0
8067 #: << MACRO >> 内容について以下に示す (DH.DUMMY は省略)
8068 #: 0x00 DH.ATMC_EACH_DIS 0x34
8069 #: 0x01 DH.SPW_RTEFDIR_SET 0x18
8070 #: 0x03 DH.ATMC_EACH_DIS 0x36
8071 #: 0x06 DH.ATMC_EACH_DIS 0x32
8072 #: 0x09 DH.ATMC_EACH_DIS 0x33
8073 #: 0x0A PCD.ON_OFF_ENA
8074 #: 0x0B PCD.PSU_MDP1_OFF
8075 #: 0x0C DH.ATMC_EACH_DIS 0x30
8076 #: 0x0D PCD.PSU_MDP2_OFF
8077 #: 0x0E PCD.ON_OFF_DIS
8078 #: 0x0F DH.ATMC_EACH_DIS 0x31
8079 #
8080 . # < CHECK >
8081 # CHECK DH.ATMC_ENA_DIS30 =DIS
8082 # CHECK DH.ATMC_ENA_DIS31 =DIS
8083 # CHECK DH.ATMC_ENA_DIS32 =DIS
8084 # CHECK DH.ATMC_ENA_DIS33 =DIS
8085 # CHECK DH.ATMC_ENA_DIS34 =DIS
8086 # CHECK DH.ATMC_ENA_DIS36 =DIS
8087 CHECK DH.SPW_FDIR_ENA_MDP1 =DIS
8088 CHECK DH.SPW_FDIR_ENA_MDP2 =DIS
8089 CHECK DH.SPW_FDIR_ENA_PCD =ENA
8090 CHECK DH.SPW_FDIR_ENA_RMTRTR =ENA
8091 CHECK PCD.PSU_MDP1_ON_OFF =OFF
8092 CHECK PCD.PSU_MDP2_ON_OFF =OFF
8093 CHECK PCD.ON_OFF_ENA_DIS =DIS
8094 #
8095 . # QL Check-Item Spec Check
8096 # -----+-----+-----+-----
8097 # CMD 出力装置 Message STATUS CHECK 結果 OK G N
8098 #

```

文 書 番 号	シート No.	版 数
TPP-7S8238-42K	124	1

4-1 ソフトウェアバージョン管理表

Component	Software(オブジェクト)/Table	Version	Program (ソース)	Revision
DMC	IPLソフトウェア	1.00.00	IPL	77
	DMCソフトウェア	1.01.23	PLTS	1850
			DHFS	1615
			ACFS	1553
			TCFS	1443
			T-Kernel	293
			T-Monitor	110
	MC-S_EEPROM	5.37	-	-
	MC-L_EEPROM	5.42	-	-
	ST_EEPROM	5.36	-	-
	AT_EEPROM	5.42	-	-
	RQ_EEPROM	5	-	-
	CATEGORY_TABLE_EEPROM	3	-	-
	PARTITION_TABLE_EEPROM	3	-	-
	TM_DATA_BLOCK_TABLE_EEPROM	3	-	-
MDP (DPU1)	Middleware	2018/01/16(Ver. 7.4)	-	-
	Application software	2018/01/16(Ver. 7.4)	-	-
MDP (DPU2)	Middleware	2018/01/16(Ver. 7.4)	-	-
	Application software	2018/01/16(Ver. 7.4)	-	-
MSA	-	2017/11/16(Ver. 40)	-	-
MIA	-	2015/2/13	-	-
MEA1	-	2012/12/19	-	-
MEA2	-	2012/12/19	-	-

*供試体がない場合はN/Aとすること。

文 書 番 号	シート No.	版 数
	125	1

5 コンフィギュレーション

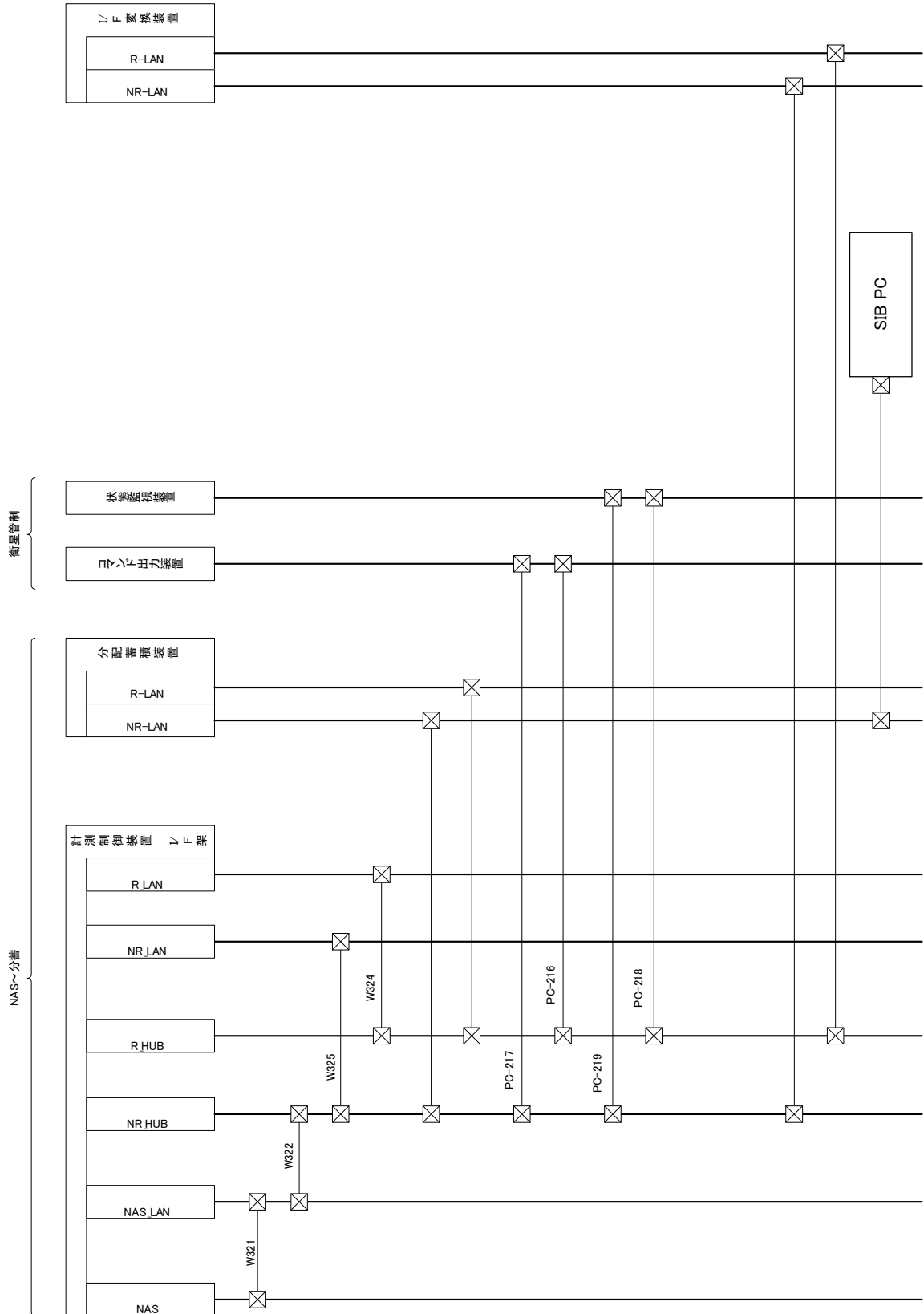


図5-1 MMO EGSE間接続

文 書 番 号	シート No.	版 数
	126	1

6 データシート

N/A

表 7-1 不具合記録表

[illegible]